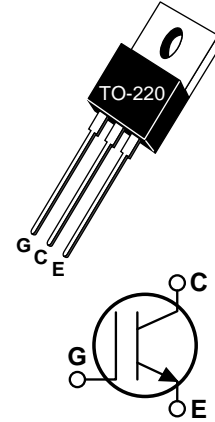


Fast IGBT

The Fast IGBT is a new generation of high voltage power IGBTs. Using Non-Punch Through Technology the Fast IGBT offers superior ruggedness, fast switching speed and low Collector-Emitter On voltage.

- Low Forward Voltage Drop
- Low Tail Current
- Avalanche Rated
- High Freq. Switching to 20KHz
- Ultra Low Leakage Current
- RBSOA and SCSOA Rated



MAXIMUM RATINGS

All Ratings: $T_C = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Parameter	APT20GF120KR	UNIT
V_{CES}	Collector-Emitter Voltage	1200	Volts
V_{CGR}	Collector-Gate Voltage ($R_{GE} = 20K\Omega$)	1200	
V_{EC}	Emitter-Collector Voltage	15	
V_{GE}	Gate-Emitter Voltage	± 20	
I_{C1}	Continuous Collector Current @ $T_C = 25^\circ\text{C}$	32	Amps
I_{C2}	Continuous Collector Current @ $T_C = 90^\circ\text{C}$	20	
I_{CM1}	Pulsed Collector Current ^① @ $T_C = 25^\circ\text{C}$	64	
I_{CM2}	Pulsed Collector Current ^① @ $T_C = 90^\circ\text{C}$	40	
E_{AS}	Single Pulse Avalanche Energy ^②	22	mJ
P_D	Total Power Dissipation	200	Watts
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	$^\circ\text{C}$
T_L	Max. Lead Temp. for Soldering: 0.063" from Case for 10 Sec.	300	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
BV_{CES}	Collector-Emitter Breakdown Voltage ($V_{GE} = 0V, I_C = 0.8mA$)	1200			Volts
RBV_{CES}	Collector-Emitter Reverse Breakdown Voltage ($V_{GE} = 0V, I_C = 50mA$)	-15			
$V_{GE(TH)}$	Gate Threshold Voltage ($V_{CE} = V_{GE}, I_C = 350\mu A, T_J = 25^\circ\text{C}$)	4.5	5.5	6.5	
$V_{CE(ON)}$	Collector-Emitter On Voltage ($V_{GE} = 15V, I_C = 15A, T_J = 25^\circ\text{C}$)		2.7	3.2	
	Collector-Emitter On Voltage ($V_{GE} = 15V, I_C = 15A, T_J = 125^\circ\text{C}$)		3.3	3.9	
I_{CES}	Collector Cut-off Current ($V_{CE} = V_{CES}, V_{GE} = 0V, T_J = 25^\circ\text{C}$)			0.8	mA
	Collector Cut-off Current ($V_{CE} = V_{CES}, V_{GE} = 0V, T_J = 125^\circ\text{C}$)			5.0	
I_{GES}	Gate-Emitter Leakage Current ($V_{GE} = \pm 20V, V_{CE} = 0V$)			± 100	nA

 **CAUTION:** These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

USA
405 S.W. Columbia Street

EUROPE
Avenue J.F. Kennedy Bât B4 Parc Cadéra Nord

APT Website - <http://www.advancedpower.com>

Bend, Oregon 97702-1035

Phone: (541) 382-8028

FAX: (541) 388-0364

F-33700 Merignac - France

Phone: (33) 5 57 92 15 15

FAX: (33) 5 56 47 97 61

DYNAMIC CHARACTERISTICS

APT20GF120KR

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C _{ies}	Input Capacitance	Capacitance V _{GE} = 0V V _{CE} = 25V f = 1 MHz		1100	1500	pF
C _{oes}	Output Capacitance			110	165	
C _{res}	Reverse Transfer Capacitance			70	105	
Q _g	Total Gate Charge ^③	Gate Charge V _{GE} = 15V V _{CC} = 0.50V _{CES} I _C = I _{C2}		95	150	nC
Q _{ge}	Gate-Emitter Charge			13	20	
Q _{gc}	Gate-Collector ("Miller") Charge			55	85	
t _{d(on)}	Turn-on Delay Time	Resistive Switching (25°C) V _{GE} = 15V V _{CC} = 0.8V _{CES} I _C = I _{C2} R _G = 10Ω		17		ns
t _r	Rise Time			75		
t _{d(off)}	Turn-off Delay Time			95		
t _f	Fall Time			170		
t _{d(on)}	Turn-on Delay Time	Inductive Switching (150°C) V _{CLAMP(Peak)} = 0.66V _{CES} V _{GE} = 15V I _C = I _{C2} R _G = 10Ω T _J = +150°C		20	30	ns
t _r	Rise Time			35	70	
t _{d(off)}	Turn-off Delay Time			175	260	
t _f	Fall Time			90	135	
E _{on}	Turn-on Switching Energy			1.2		
E _{off}	Turn-off Switching Energy		1.3			
E _{ts}	Total Switching Losses		2.5			
t _{d(on)}	Turn-on Delay Time	Inductive Switching (25°C) V _{CLAMP(Peak)} = 0.66V _{CES} V _{GE} = 15V I _C = I _{C2} R _G = 10Ω T _J = +25°C		20		ns
t _r	Rise Time			35		
t _{d(off)}	Turn-off Delay Time			150		
t _f	Fall Time			90		
E _{ts}	Total Switching Losses			2.3		
g _{fe}	Forward Transconductance	V _{CE} = 20V, I _C = 15A		12		S

THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
R _{θJC}	Junction to Case			0.63	°C/W
R _{θJA}	Junction to Ambient			80	
Torque	Mounting Torque using a 6-32 or 3mm Binding Head Machine Screw		10		lb•in

① Repetitive Rating: Pulse width limited by maximum junction temperature.

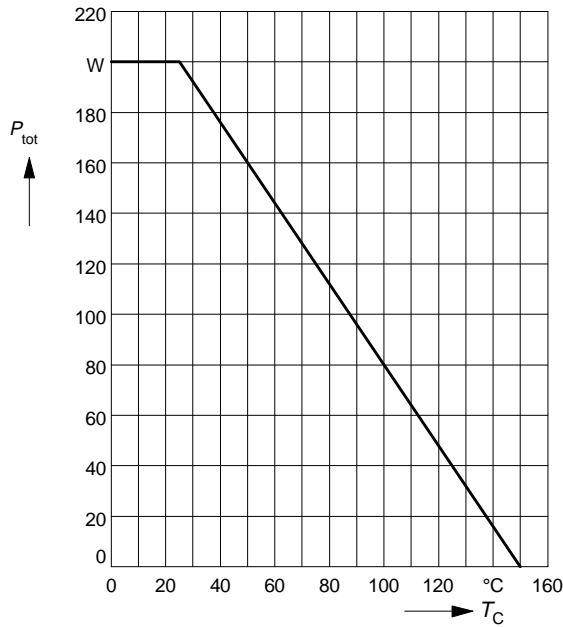
② I_C = 15A, V_{CC} = 50V, R_{GE} = 25Ω, L = 200μH, T_J = 25°C

③ See MIL-STD-750 Method 3471

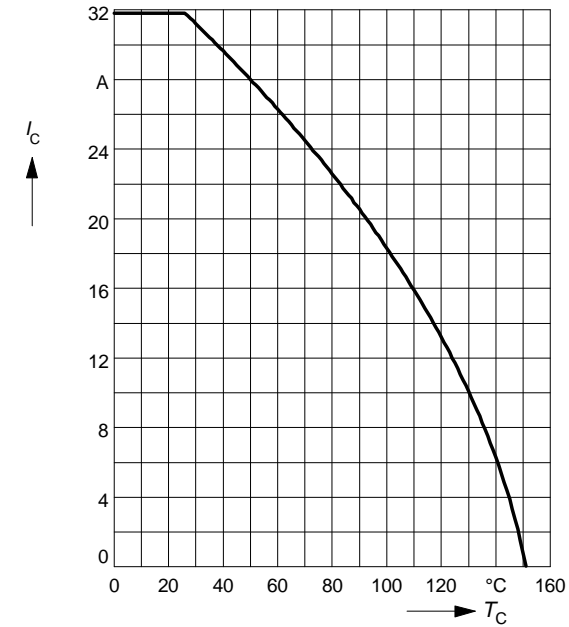
APT Reserves the right to change, without notice, the specifications and information contained herein.

Power dissipation

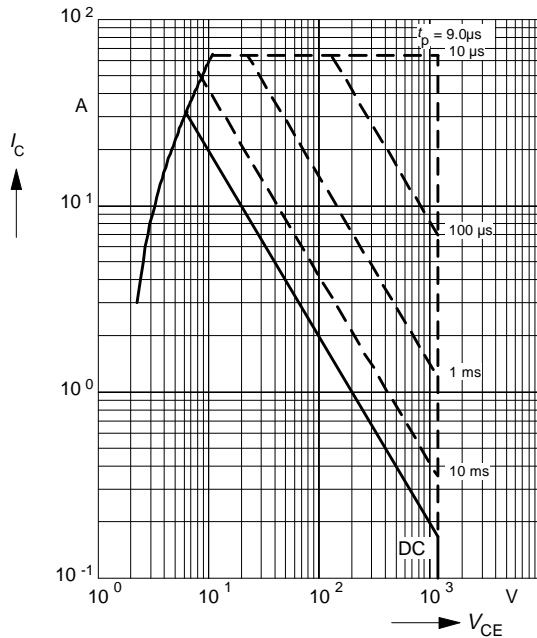
$$P_{tot} = f(T_C)$$

 parameter: $T_j \leq 150\text{ }^\circ\text{C}$

Collector current

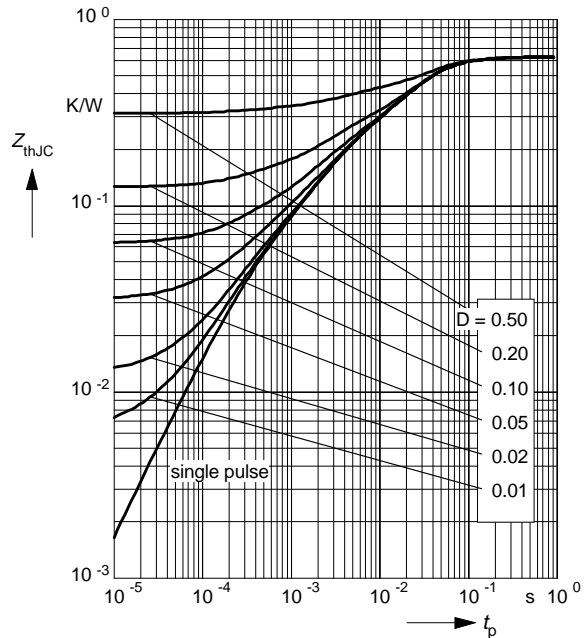
$$I_C = f(T_C)$$

 parameter: $V_{GE} \geq 15\text{ V}$, $T_j \leq 150\text{ }^\circ\text{C}$

Safe operating area

$$I_C = f(V_{CE})$$

 parameter: $D = 0$, $T_C = 25\text{ }^\circ\text{C}$, $T_j \leq 150\text{ }^\circ\text{C}$

Transient thermal impedance IGBT

$$Z_{thJC} = f(t_p)$$

 parameter: $D = t_p / T$

EUROPE

 Avenue J.F. Kennedy Bât B4 Parc Cadéra Nord
USA

405 S.W. Columbia Street

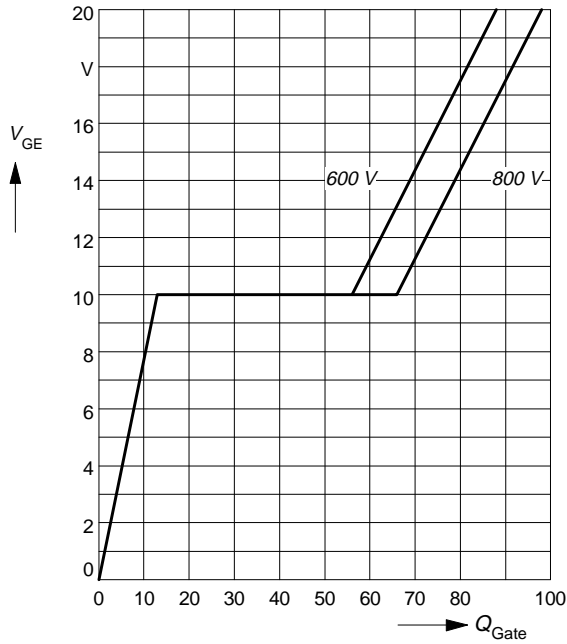
F-33700 Merignac - France

Bend, Oregon 97702-1035

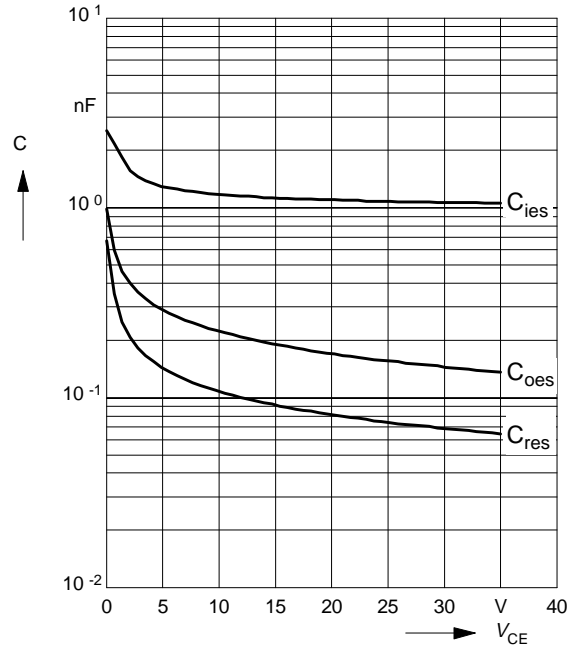
Phone: (33) 5 57 92 15 15
Phone: (541) 382-8028
FAX: (33) 5 56 47 97 61
FAX: (541) 388-0364

Typ. gate charge

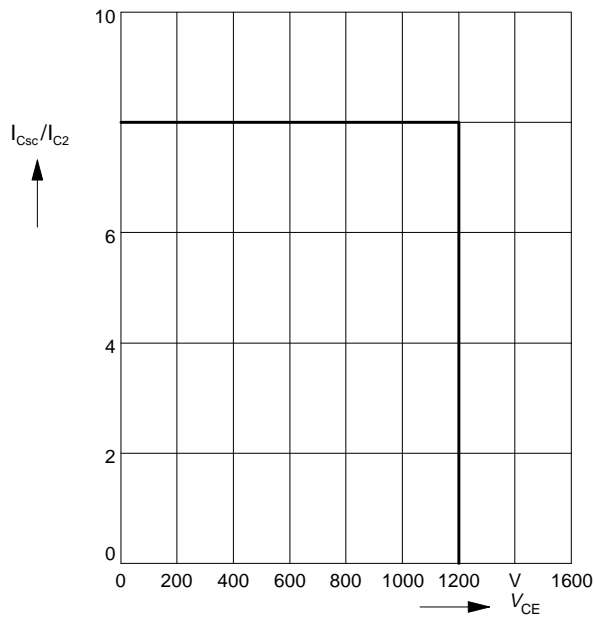
$$V_{GE} = f(Q_{Gate})$$

 parameter: $I_{C\ puls} = 16A$

Typ. capacitances

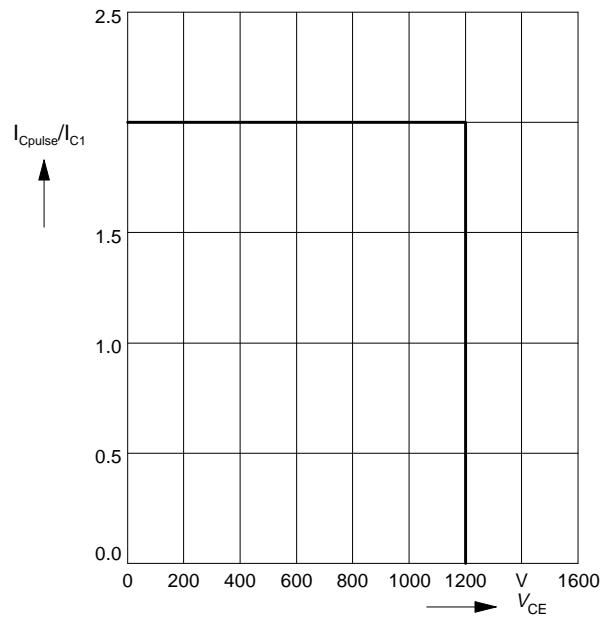
$$C = f(V_{CE})$$

 parameter: $V_{GE} = 0V, f = 1\ MHz$

Short circuit safe operating area

$$I_{Csc} = f(V_{CE}), T_j = 150^\circ C$$

 parameter: $V_{GE} = \pm 15V, t_{sc} \leq 10\ \mu s, L < 25\ nH$

Reverse biased safe operating area

$$I_{Cpuls} = f(V_{CE}), T_j = 150^\circ C$$

 parameter: $V_{GE} = 15V$

EUROPE

Avenue J.F. Kennedy Bât B4 Parc Cadéra Nord

USA

405 S.W. Columbia Street

F-33700 Merignac - France

Bend, Oregon 97702-1035

Phone: (33) 5 57 92 15 15

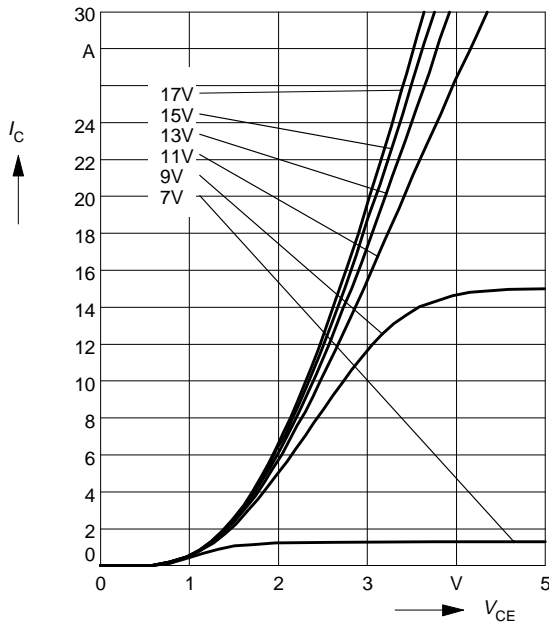
Phone: (541) 382-8028

FAX: (33) 5 56 47 97 61

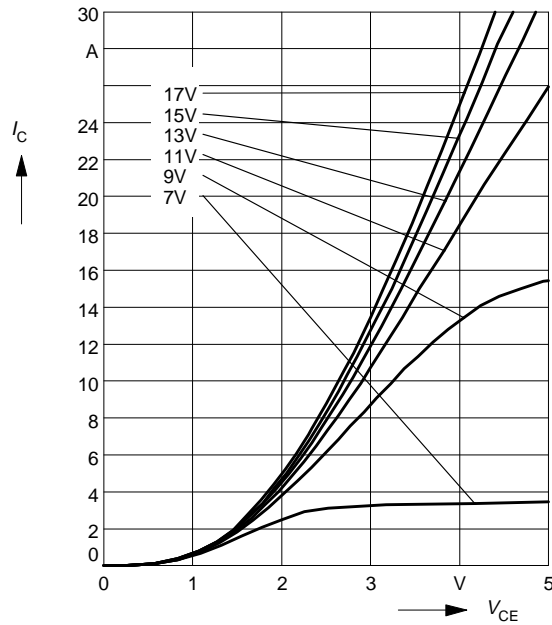
FAX: (541) 388-0364

Typ. output characteristics

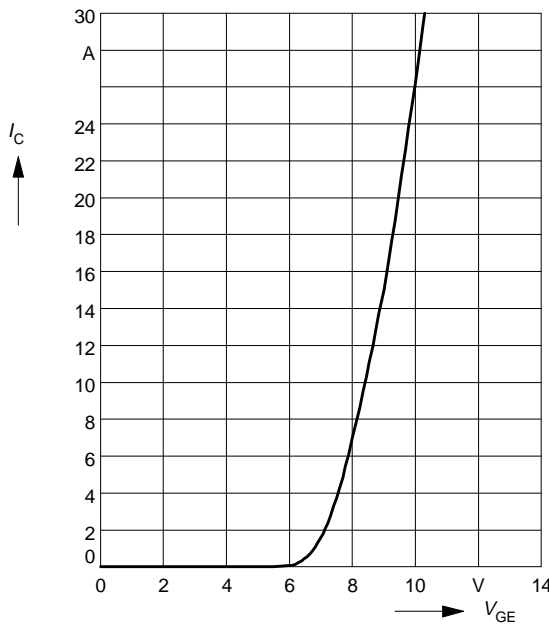
$$I_C = f(V_{CE})$$

 parameter: $t_p = 80 \mu s, T_j = 25 \text{ }^\circ\text{C}$

Typ. output characteristics

$$I_C = f(V_{CE})$$

 parameter: $t_p = 80 \mu s, T_j = 125 \text{ }^\circ\text{C}$

Typ. transfer characteristics

$$I_C = f(V_{GE})$$

 parameter: $t_p = 80 \mu s, V_{CE} = 20 \text{ V}$

EUROPE

 Avenue J.F. Kennedy Bât B4 Parc Cadéra Nord
USA

405 S.W. Columbia Street

F-33700 Merignac - France

Bend, Oregon 97702-1035

Phone: (33) 5 57 92 15 15

Phone: (541) 382-8028

FAX: (33) 5 56 47 97 61

FAX: (541) 388-0364