

**ADVANCED
POWER
TECHNOLOGY®**
APL1001J 1000V 18.0A 0.60Ω

"UL Recognized" File No. E145592 (S)

POWER MOS IV®

SINGLE DIE ISOTOP® PACKAGE

N-CHANNEL ENHANCEMENT MODE HIGH VOLTAGE POWER MOSFETS

MAXIMUM RATINGS

All Ratings: $T_C = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Parameter	APL1001J	UNIT
V_{DSS}	Drain-Source Voltage	1000	Volts
I_D	Continuous Drain Current @ $T_C = 25^\circ\text{C}$	18	Amps
I_{DM}, I_{LM}	Pulsed Drain Current ^① and Inductive Current Clamped	72	
V_{GS}	Gate-Source Voltage	± 30	Volts
P_D	Total Power Dissipation @ $T_C = 25^\circ\text{C}$	520	Watts
	Linear Derating Factor	4.16	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	$^\circ\text{C}$
T_L	Lead Temperature: 0.063" from Case for 10 Sec.	300	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions / Part Number	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage ($V_{GS} = 0V, I_D = 250 \mu\text{A}$)	1000			Volts
$I_D(\text{ON})$	On State Drain Current ^② ($V_{DS} > I_D(\text{ON}) \times R_{DS}(\text{ON})$ Max, $V_{GS} = 8V$)	18			Amps
$R_{DS}(\text{ON})$	Drain-Source On-State Resistance ^② ($V_{GS} = 10V, 0.5 I_D$ [Cont.])			0.60	Ohms
I_{DSS}	Zero Gate Voltage Drain Current ($V_{DS} = V_{DSS}, V_{GS} = 0V$)			25	μA
	Zero Gate Voltage Drain Current ($V_{DS} = 0.8 V_{DSS}, V_{GS} = 0V, T_C = 125^\circ\text{C}$)			250	
I_{GSS}	Gate-Source Leakage Current ($V_{GS} = \pm 30V, V_{DS} = 0V$)			± 100	nA
$V_{GS}(\text{TH})$	Gate Threshold Voltage ($V_{DS} = V_{GS}, I_D = 2.5\text{mA}$)	2		4	Volts

THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			0.24	$^\circ\text{C/W}$
$R_{\theta CS}$	Case to Sink (Use High Efficiency Thermal Joint Compound and Planer Heat Sink Surface.)		0.06		

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

APT Website - <http://www.advancedpower.com>

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Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1 \text{ MHz}$		6100		pF
C_{oss}	Output Capacitance			780		
C_{rss}	Reverse Transfer Capacitance			285		
$t_d(\text{on})$	Turn-on Delay Time	$V_{GS} = 15V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D[\text{Cont.}] @ 25^\circ\text{C}$ $R_G = 0.6\Omega$		14		ns
t_r	Rise Time			14		
$t_d(\text{off})$	Turn-off Delay Time			60		
t_f	Fall Time			14		

SAFE OPERATING AREA CHARACTERISTICS

Symbol	Characteristic	Test Conditions / Part Number	MIN	TYP	MAX	UNIT
SOA1	Safe Operating Area	$V_{DS} = 400V, I_{DS} = 0.813A, t = 20 \text{ sec.}, T_C = 60^\circ\text{C}$	300	375		Watts

- ① Repetitive Rating: Pulse width limited by maximum junction temperature. See Transient Thermal Impedance Curve. (Fig.1)
- ② Pulse Test: Pulse width < 380 μs , Duty Cycle < 2%
- ③ See MIL-STD-750 Method 3471

APT Reserves the right to change, without notice, the specifications and information contained herein.

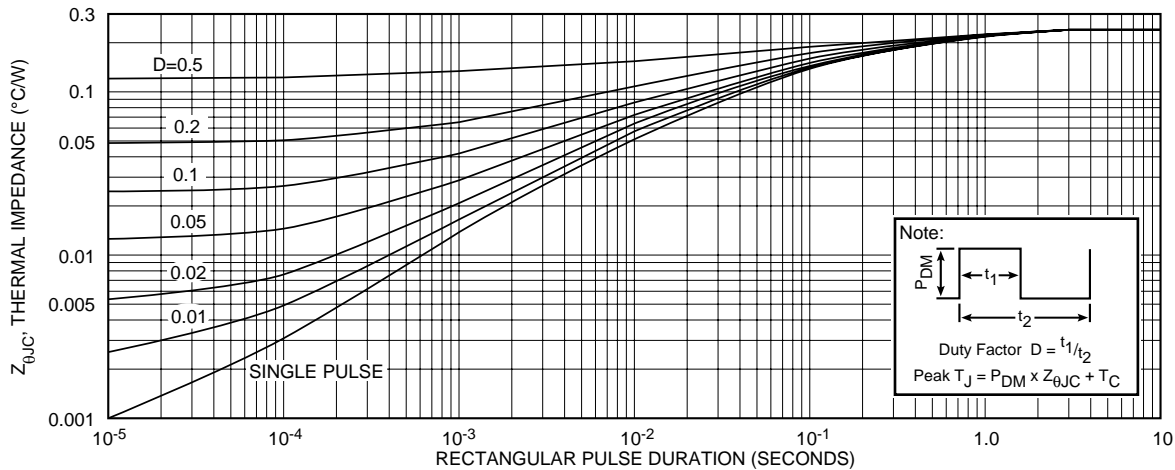
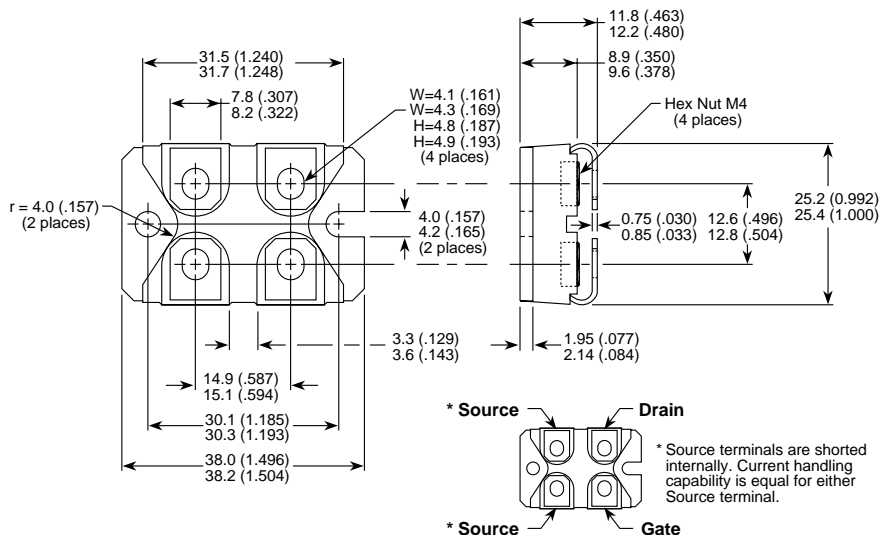


FIGURE 1, MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs PULSE DURATION



Dimensions in Millimeters and (Inches)

APT's devices are covered by one or more of the following U.S. patents: 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336
5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058