

NPN SILICON RF POWER TRANSISTOR

DESCRIPTION:

The **ASI ALR100** is Designed for

FEATURES:

- Input Matching Network
- **Omnigold™** Metalization System

MAXIMUM RATINGS

I_C	13.5 A
V_{CC}	32 V
P_{DISS}	270 W @ T _C = 25 °C
T_J	-65 °C to +250 °C
T_{STG}	-65 °C to +200 °C
θ_{JC}	0.55 °C/W

PACKAGE STYLE .400 2L FLG

DIM	MINIMUM inches / mm	MAXIMUM inches / mm
A		.140 / 3.56
B		.110 / 2.80
C		.110 / 2.80
D	.395 / 10.03	.407 / 10.34
E		.193 / 4.90
F		.230 / 5.84
G	.003 / 0.08	.006 / 0.15
H	.118 / 3.00	.131 / 3.33
I		.063 / 1.60
J		.650 / 16.51
K		.386 / 9.80
L		.900 / 22.86
M		.450 / 11.43
N		.125 / 3.18
O	.050 / 1.27	
P		.405 / 10.29
Q		.170 / 4.32
R		.062 / 1.58

ORDER CODE: ASI10514

CHARACTERISTICS T_C = 25 °C

SYMBOL	TEST CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
BV_{CBO}	I _C = 50 mA	65			V
BV_{CES}	I _C = 100 mA	65			V
BV_{EBO}	I _E = 10 mA	3.5			V
I_{CES}	V _{CE} = 32 V			20	mA
h_{FE}	V _{CE} = 5.0 V I _C = 5.0 A	15		---	---
P_G	V _{CC} = 28 V P _{OUT} = 100 W f = 1.2 to 1.4	6.0			dB
η_C	GHz	50			%