

# NPN SILICON RF POWER TRANSISTOR

**DESCRIPTION:**

The ASI CBSL6 is Designed for

**FEATURES:**

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- **Omnigold™** Metalization System

**MAXIMUM RATINGS**

<b>I<sub>C</sub></b>	2.4 A
<b>V<sub>CB0</sub></b>	50 V
<b>V<sub>CES</sub></b>	35 V
<b>V<sub>EBO</sub></b>	3.5 V
<b>P<sub>DISS</sub></b>	53 W @ T <sub>C</sub> = 25 °C
<b>T<sub>J</sub></b>	-65 °C to +200 °C
<b>T<sub>STG</sub></b>	-65 °C to +150 °C
<b>θ<sub>JC</sub></b>	3.3 °C/W

**PACKAGE STYLE .230 6L FLG**

DIM	MINIMUM inches / mm	MAXIMUM inches / mm
A	.355 / 9.02	.365 / 9.27
B	.115 / 2.92	.125 / 3.18
C	.075 / 1.91	.085 / 2.16
D	.225 / 5.72	.235 / 5.97
E	.090 / 2.29	.110 / 2.79
F	.720 / 18.29	.730 / 18.54
G	.970 / 24.64	.980 / 24.89
H	.355 / 9.02	.365 / 9.27
I	.004 / 0.10	.006 / 0.15
J	.120 / 3.05	.130 / 3.30
K	.160 / 4.06	.180 / 4.57
L	.230 / 5.84	.260 / 6.60

**ORDER CODE: ASI10580**

**CHARACTERISTICS** T<sub>C</sub> = 25 °C

SYMBOL	TEST CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
<b>BV<sub>CEO</sub></b>	I <sub>C</sub> = 5 mA	24			V
<b>BV<sub>CB0</sub></b>	I <sub>C</sub> = 5 mA	50			V
<b>BV<sub>EBO</sub></b>	I <sub>E</sub> = 5 mA	3.5			V
<b>I<sub>CEO</sub></b>	V <sub>CE</sub> = 24 V			1.0	mA
<b>I<sub>CB0</sub></b>	V <sub>CB</sub> = 24 V			1.0	mA
<b>h<sub>FE</sub></b>	V <sub>CE</sub> = 10 V      I <sub>C</sub> = 0.1 A	20		100	---
<b>C<sub>OB</sub></b>	V <sub>CB</sub> = 24 V      f = 1.0 MHz			8.5	pF
<b>P<sub>G</sub></b> <b>η<sub>C</sub></b>	V <sub>CC</sub> = 24 V      I <sub>CQ</sub> = 25 mA      f = 960 MHz P <sub>OUT</sub> = 6.0 W	10	50	---	dB %