QUAD HIGH-CURRENT DARLINGTON SWITCHES

These quad Darlington arrays are designed to serve as interface between low-level logic and peripheral power devices such as solenoids, motors, incandescent displays, heaters, and similar loads of up to 320 W per channel. Both integrated circuits include transient-suppression diodes that enable use with inductive loads. The input logic is compatible with most TTL, DTL, LSTTL, and 5 V CMOS logic.

Type UDN2878W and UDN2879W 4 A arrays are identical except for output-voltage ratings. The former is rated for operation to 50 V (35 V sustaining), while the latter has a minimum output breakdown rating of 80 V (50 V sustaining). The lower-cost UDN2879W-2 is recommended for applications requiring load currents of 3 A or less. These less expensive devices are identical to the basic parts except for the maximum allowable load-current rating.

For maximum power-handling capability, all drivers are supplied in a 12-pin single in-line power-tab package. The tab needs no insulation. External heat sinks are usually required for proper operation of these devices.

FEATURES

- Output Currents to 4 A
- Output Voltages to 80 V
- Loads to 1280 W
- TTL, DTL, or CMOS Compatible Inputs
- Internal Clamp Diodes
- Plastic Single In-Line Package
- Heat-Sink Tab

SUB

Vs

4 5 6 7 6 9 10 11 12

C2 K1-2 B2 B3 V5 C3 K3-4 C4 B4

Dwg. No. A-11,974

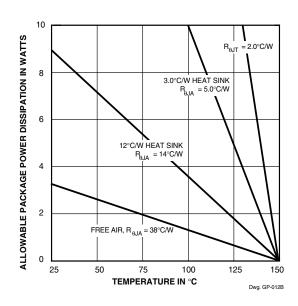
ABSOLUTE MAXIMUM RATINGS at +25°C Free-Air Temperature for any driver (unless otherwise noted)

Output Voltage, V _{CEX} (UDN2878W)
Output Current, I _C
(UDN2878W & UDN2879W) 5.0 A
(UDN2879W-2) 4.0 A
Input Voltage, V _{IN}
Input Current, I _{IN} 25 mA
Supply Voltage, V _S 10 V
Total Package Power Dissipation,
P _D See Graph
Operating Ambient Temperature Range,
T _A 20°C to +85°C
Storage Temperature Range,
T _S 55°C to +150°C

Always order by complete part number:

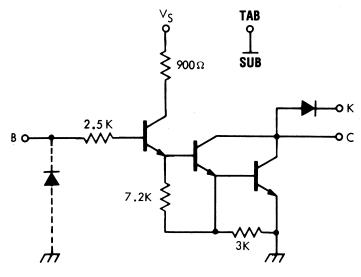
Part Number	Max. I _C	Max. V _{CEX}	Min. V _{CE (sus)}
UDN2878W	5.0 A	50 V	35 V
UDN2879W	5.0 A	80 V	50 V
UDN2879W-2	4.0 A	80 V	50 V





PARTIAL SCHEMATIC

One of 4 Drivers



Dwg. No. A-12,037

NOTE: Pin 3 must be connected to ground for proper operation.

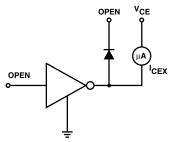


ELECTRICAL CHARACTERISTICS at $V_S = 5.0 \text{ V}$, $T_A = +25^{\circ}\text{C}$ (unless otherwise noted).

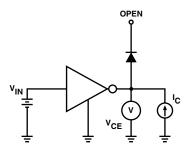
		Test	Applicable		Limits		
Characteristic	Symbol	Fig.	Devices	Test Conditions	Min.	Max.	Units
Output Leakage Current	I _{CEX}	1	UDN2878W	V _{CE} = 50 V	_	100	μА
				V _{CE} = 50 V, T _A = +70°C	_	500	μА
			UDN2879W/W-2	V _{CE} = 80 V	_	100	μА
				V _{CE} = 80 V, T _A = +70°C	_	500	μА
Output Sustaining	V _{CE(sus)}	_	UDN2878W	I _C = 4 A, L = 10 mH	35	_	V
Voltage			UDN2879W	I _C = 4 A, L = 10 mH	50	_	V
			UDN2879W-2	I _C = 3 A, L = 10 mH	50	_	V
Collector-Emitter	V _{CE(SAT)}	2	All	$I_C = 500 \text{ mA}, V_{IN} = 2.75 \text{ V}$	_	1.1	V
Saturation Voltage				$I_C = 1.0 \text{ A}, V_{IN} = 2.75 \text{ V}$	_	1.3	V
				$I_C = 2.0 \text{ A}, V_{IN} = 2.75 \text{ V}$	_	1.5	٧
				$I_C = 3.0 \text{ A}, V_{IN} = 2.75 \text{ V}$	_	1.9	V
			UDN2878/79W	$I_C = 4.0 \text{ A}, V_{IN} = 3.0 \text{ V}$	_	2.4	V
Input Current	I _{IN}	3	All	V _{IN} = 2.75 V	_	550	μА
				V _{IN} = 3.75 V	_	1000	μА
Input Voltage	V _{IN(ON)}	4	All	$V_{CE} = 2.2 \text{ V}, I_{C} = 3.0 \text{ A}$	_	2.75	V
			UDN2878/79W	V _{CE} = 2.2 V, I _C = 4.0 A	_	2.75	V
Supply Current per Driver	I _S	7	All	$I_C = 500 \text{ mA}, V_{IN} = 2.75 \text{ V}$	_	6.0	mA
Turn-On Delay	t _{PLH}	_	All	0.5 E _{in} to 0.5 E _{out}	_	1.0	μs
Turn-Off Delay	t _{PHL}	_	All	$0.5 E_{in}$ to $0.5 E_{out}$, $I_C = 3.0 A$	_	1.5	μs
Clamp Diode	I _R	5	All	V _R = 50 V	_	50	μА
Leakage Current				V _R = 50 V, T _A = +70°C	_	100	μΑ
			UDN2879W/W-2	V _R = 80 V	_	50	μΑ
				$V_{R} = 80 \text{ V}, T_{A} = +70^{\circ}\text{C}$	_	100	μА
Clamp Diode	V _F	6	All	I _F = 3.0 A	_	2.5	V
Forward Voltage			UDN2878/79W	I _F = 4.0 A	_	3.0	V

Caution: High-current tests are pulse tests or require heat sinking.

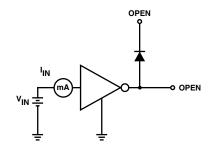
TEST FIGURES







Dwg. No. A-10,350

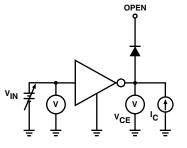


Dwg. No. A-9732

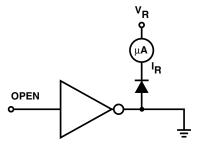
FIGURE 1



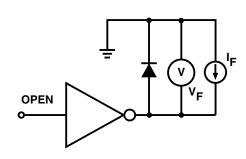
FIGURE 3



Dwg. No. A-9734A



Dwg. No. A-9735A



Dwg. No. A-9736

FIGURE 4

FIGURE 5

FIGURE 6

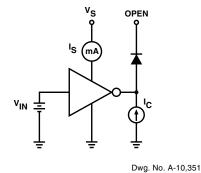
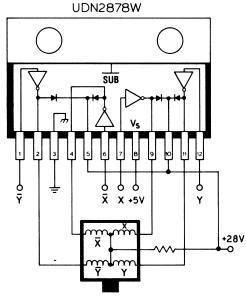


FIGURE 7

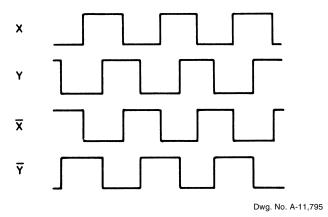
TYPICAL APPLICATIONS

INPUT WAVEFORMS



Dwg. No. A-11,975

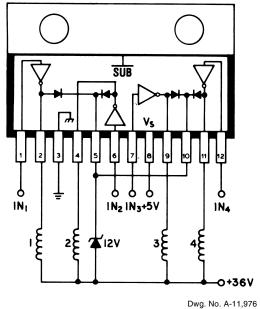
STEPPER-MOTOR DRIVER

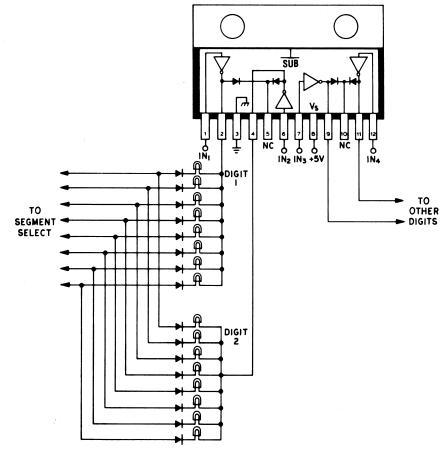


DIGIT DRIVER FOR MULTIPLEXED INCANDESCENT LAMP DISPLAY

UDN2879W

PRINT-HAMMER DRIVER UDN2879W

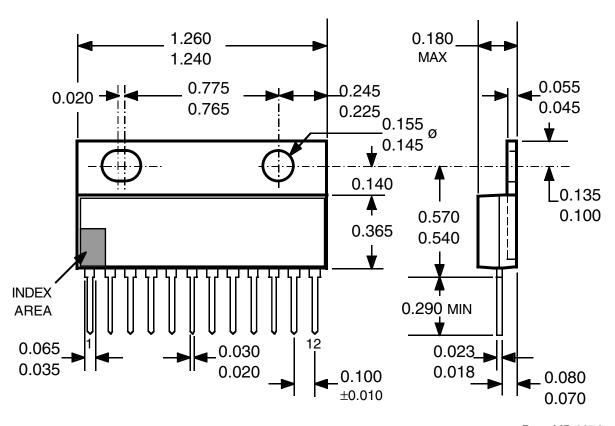




Dwg. No. B-1512

Dimensions in Inches

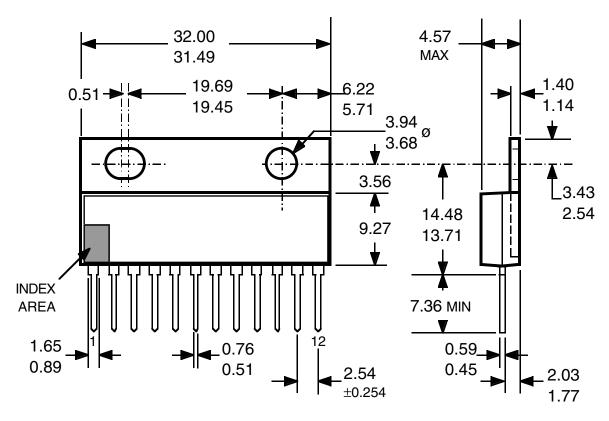
(controlling dimensions)



Dwg. MP-007 in

- NOTES: 1. Lead thickness is measured at seating plane or below.
 - 2. Lead spacing tolerance is non-cumulative.
 - 3. Exact body and lead configuration at vendor's option within limits shown.
 - 4. Lead gauge plane is 0.030" below seating plane.
 - 5. Supplied in standard sticks/tubes of 15 devices.

Dimensions in Millimeters (for reference only)



Dwg. MP-007 mm

- NOTES: 1. Lead thickness is measured at seating plane or below.
 - 2. Lead spacing tolerance is non-cumulative.
 - 3. Exact body and lead configuration at vendor's option within limits shown.
 - 4. Lead gauge plane is 0.762 mm below seating plane.
 - 5. Supplied in standard sticks/tubes of 15 devices.

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POWER SINK DRIVERS

IN ORDER OF 1) OUTPUT CURRENT, 2) OUTPUT VOLTAGE, 3) NUMBER OF DRIVERS

					Features			
Output Ratings * ——		Serial	Latched	Diode		Internal	•	
A	W	щ				Outputa		Part Number [†]
mA 75	V 17	#	Input	Drivers	Clamp		Protection	Part Number
/5	17 17	8 16	X X	X X		onstant current onstant current		6275 6276
100	20	8				saturated		2595
100	30	32	X	X	_	Saturateu	_	5833
	40	32	x	x	_	saturated	_	5832
	50	8		ssable deco	der/driver	DMOS	_	6B259
	50	8	_	Χ	_	DMOS	_	6B273
	50	8	X	X	_	DMOS	_	6B595
250	50	8	addre	ssable decod	der/driver	DMOS	_	6259
	50	8	X	X	_	DMOS	_	6273
	50	<u>8</u>		X	_	DMOS	_	6595
	135	7	_		X	_	_	7003
300	45	1	– Ha	ıll sensor/driv	er X		Х	5140
	50	8	_	_	X	saturated	_	2596
050	60	4	_		X	saturated	X	2557
350	50 50	4	_	Х	X	_	_	5800
	50 50	7 7	_	_	X X	_	_	2003 2004
	50 50	8	_	_	X	_	_	2803
	50	8	_	_	â	_	_	2804
	50	8	_	Χ	X	_	_	5801
	50	8	X	X	_	_	_	5821
	50	8	X	X	X		_	5841
	50	8		ssable decod	der/driver	DMOS	_	6A259
	50 80	8	X	X X	_	DMOS	_	6A595
	80 80	8 8	X X	â	X	_	_	5822 5842
	95	7	_	_	X	_	_	2023
	95	7	_	_	â	_	_	2024
	95	8	_	_	X	_	_	2823
	95	8	_	_	X	_	_	2824
450	30	28	dual 4	l- to 14-line d	lecoder/drive	er –	-	6817
600	60	4	_	_	_	saturated	X	2547
	60	4	_	_	X	saturated	X	2549 and 2559
700	60	4	_	_	Х	saturated	X	2543
750	50	8	_	_	Х	saturated	_	2597
900	14	2	– Ha	II sensor/driv	er X	saturated	X	3625
	26	2	– Ha	III sensor/driv		saturated	Χ	3626
1000	46	4	stepp	er motor con	troller/driver	MOS	_	7024 and 7029
1200	46	4	micro	stepping con	troller/driver	MOS	_	7042
1250	50	4	stepp	er motor tran	slator/driver	_	X	5804
	50	4		_	X	_	_	2064 and 2068
1500	80	4	_	_	Х	_	_	2065 and 2069
1800	50	4	_	_	Х	_	_	2544
	50	4		<u> </u>	X	<u> </u>	<u> </u>	2540
3000	46	4	stepp	er motor con	troller/driver	MOS	_	7026
	46	4	micro	stepping con		MOS		7044
4000	50	4	_	_	Х	_	_	2878
	80	4	_	_	X	_	_	2879

^{*} Current is maximum specified test condition, voltage is maximum rating. See specification for sustaining voltage limits or over-current protection voltage limits.

[†] Complete part number includes additional characters to indicate operating temperature range and package style.

