V. an Indicics

## FEATURES

- Low Insertion Loss ( $0.4 \mathrm{~dB} @ 0.9 \mathrm{GHz}$ )
- Complementary Positive Control Voltages ( $0 /+3 \mathrm{~V}$ to $0 /+5 \mathrm{~V}$ )
- Positive Voltage Supply ( +3 to +5 V )
- Low DC Power Consumption
- Ultra Miniature 6 Lead SOT-6 Package


## DESCRIPTION

The AWS5502 is a Single Pole Double Throw GaAs MMIC Switch assembled in a SOT-6 plastic package. The AWS5502 is designed for analog and digital application that require low insertion loss, small size, and low cost. State selection is achieved with a complimentary positive voltage (requires positive bias Vs , and blocking caps) or negative voltage (no Vs or blocking caps required).

Typical applications include: selection of synthesizers,

ELECTRICAL SPECIFICATIONS AT $25^{\circ} \mathrm{C}(0,+3 \mathrm{~V})$

| Parameter ${ }^{1}$ | Frequency ${ }^{2}$ | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss ${ }^{3}$ | $\begin{aligned} & \mathrm{DC}-0.5 \mathrm{GHz} \\ & \mathrm{DC}-1.0 \mathrm{GHz} \\ & \mathrm{DC}-2.0 \mathrm{GHz} \\ & \mathrm{DC}-2.5 \mathrm{GHz} \end{aligned}$ | - | $\begin{gathered} 0.4 \\ 0.45 \\ 0.6 \\ 0.9 \end{gathered}$ | $\begin{aligned} & 0.5 \\ & 0.6 \\ & 0.8 \\ & 1.1 \end{aligned}$ | dB |
| Isolation | $\begin{aligned} & \mathrm{DC}-0.5 \mathrm{GHz} \\ & \mathrm{DC}-1.0 \mathrm{GHz} \\ & \mathrm{DC}-2.0 \mathrm{GHz} \\ & \mathrm{DC}-2.5 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & 22 \\ & 17 \\ & 11 \\ & 10 \end{aligned}$ | $\begin{aligned} & 25 \\ & 20 \\ & 14 \\ & 13 \end{aligned}$ | - | dB |
| VSWR ${ }^{4}$ | $\begin{aligned} & \mathrm{DC}-1.0 \mathrm{GHz} \\ & \mathrm{DC}-2.5 \mathrm{GHz} \end{aligned}$ | - | $\begin{aligned} & 1.2: 1 \\ & 1.5: 1 \end{aligned}$ | $\begin{aligned} & 1.3: 1 \\ & 1.7: 1 \end{aligned}$ | - |

OPERATING CHARACTERISTICS AT $25^{\circ} \mathrm{C}(0,+3 \mathrm{~V})$

| Parameter | Condition | Frequency | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Switching Characteritics ${ }^{5}$ | Rise, Fall (10/90\% or $90 / 10 \%$ RF) ON, Off ( $50 \%$ CTL to $90 \% / 10 \%$ RF) Video Feedthru | - | - | $\begin{aligned} & 10 \\ & 20 \\ & 25 \end{aligned}$ | - | ns ns $\mathrm{mV}$ |
| Intermodulation Intercept Point (IP3) | For Two-tone Input Power +10 dBm | $0.5-2.0 \mathrm{GHz}$ | - | +45 | - | dBm |
| Input Power for 1 dB Compression | $\begin{aligned} & @+3 V \\ & @+5 V \end{aligned}$ | $\begin{aligned} & 0.5-2.0 \mathrm{GHz} \\ & 0.5-2.0 \mathrm{GHz} \end{aligned}$ | - | $\begin{aligned} & +21 \\ & +28 \end{aligned}$ | - | dBm |
| Control Voltage | $\begin{aligned} & \mathrm{V}_{\text {Low }}=0 \text { to } 0.2 \mathrm{~V} @ 20 \text { uA Max } \\ & \mathrm{V}_{\text {HIGH }}=+3 \mathrm{~V} @ 100 \text { uA Max to }+5 \mathrm{~V} @ 200 \text { uA Max } \\ & \mathrm{V}_{\mathrm{S}}=\mathrm{V}_{\text {HIGH }} \pm 0.2 \mathrm{~V} \end{aligned}$ |  |  |  |  |  |

[^0]
## ABSOLUTE MAXIMUM RATINGS

| Characteristics | Value |
| :--- | :---: |
| RF Input Power | $2 \mathrm{~W}>500 \mathrm{MHz}, 0 /+7 \mathrm{~V}$ Control |
| Control Voltage | $-0.2 \mathrm{~V},+8 \mathrm{~V}$ |
| Operating Temperature | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |
| Storage Temperature | $-50^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| $\theta_{\mathrm{Jc}}$ | $25^{\circ} \mathrm{C} / \mathrm{W}$ |

TRUTH TABLE Positive Operation

| $\mathrm{V}_{1}$ | $\mathrm{~V}_{2}$ | $J_{1}-J_{2}$ | $J_{1}-J_{3}$ |
| :---: | :---: | :---: | :---: |
| $\mathrm{~V}_{\text {High }}$ | 0 | Insertion | Isolation |
| 0 | $\mathrm{~V}_{\text {High }}$ | Isolation | Insertion |

$\mathrm{V}_{\text {High }}=+3$ to $+5 \mathrm{~V}\left(\mathrm{~V}_{\mathrm{s}}=\mathrm{V}_{\text {High }} \pm 0.2 \mathrm{~V}\right)$

## PIN OUT



DC blocking capacitors ( $\mathrm{C}_{1,2,4}$ ) and biasing resistor must be supplied externally for positive voltage operation.

| Pin | Function | Description |
| :---: | :---: | :--- |
| 1 | RFOUT (J2) | RF port (can be used as an input or as an <br> output) |
| 2 | GND | Ground connection (keep as short as <br> possible) |
| 3 | RFIN (J1)Ns | RF common port and bias voltage for positive <br> control (3V to 5V) |
| 4 | RFOUT 1 (J2) | RF port (can be used as an input or as an <br> output) |
| 5 | V2 | Control voltage 2 (low 0V , high 3V to 5V ) |
| 6 | V1 | Control voltage 1 (low 0V , high 3V to 5V) |

$\mathrm{C}_{1,2,4}=100 \mathrm{pF}$ for operation $>500 \mathrm{MHz}$.

## TEST CIRCUIT LAYOUT




## NOTES:

1. Package body sizes exclude mold flash and gate burrs.
2. Dimension $L$ is measured in gage plane
3. Coplanarity: 0.1000 mm
4. Tolerance $\pm 0.1000 \mathrm{~mm}$ ( $\mathbf{4} \mathbf{~ m i l}$ ) unless otherwise specified.

| SYMBOLS | DIMENSIONS IN MILLIMETERS |  |  | DIMENSIONS IN INCHES |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN | NOM | MAX | MIN | NOM | MAX |  |
| A | 1.00 | 1.10 | 1.30 | 0.039 | 0.043 | 0.051 |  |
| A 1 | 0.00 | -- | 0.10 | 0.00 | --- | 0.004 |  |
| A 2 | 0.70 | 0.80 | 0.90 | 0.027 | 0.031 | 0.035 |  |
| b | 0.35 | 0.40 | 0.50 | 0.014 | 0.016 | 0.020 |  |
| C | 0.10 | 0.15 | 0.25 | 0.004 | 0.006 | 0.010 |  |
| D | 2.70 | 2.90 | 3.10 | 0.106 | 0.114 | 0.122 |  |
| E | 1.40 | 1.60 | 1.80 | 0.055 | 0.063 | 0.071 |  |
| e | $1.90(\mathrm{TYP})$ |  |  |  | $0.075(\mathrm{TYP})$ |  |  |
| H | 2.60 | 2.80 | 3.00 | 0.102 | 0.110 | 0.118 |  |
| L | 0.37 | -- | --- | 0.015 | -- | - |  |
| $\theta 1$ | $1^{\circ}$ | $5^{\circ}$ | $9^{\circ}$ | $1^{\circ}$ | $5^{\circ}$ | $9^{\circ}$ |  |

## ANADIGICS, Inc.

35 Technology Drive

## Warren, New Jersey 07059

Tel: (908) 668-5000 / Fax: (908) 668-5132
Email: Mktg@anadigics.com
www.anadigics.com

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[^0]:    All measurements made in a 50 ohm system, unless otherwise specified.
    $D C=300 \mathrm{kHz}$.
    Insertion loss changes by $0.003 \mathrm{~dB} /{ }^{\circ} \mathrm{C}$.
    Insertion loss state.
    . Video feedthru measured wirh 1 ns rise time pulse and 500 MHz bandwidth.

