

KEY FEATURES

- Power Modules for PCB Mounting
- Regulated Output
- Low Ripple and Noise
- 2-Year Product Warranty

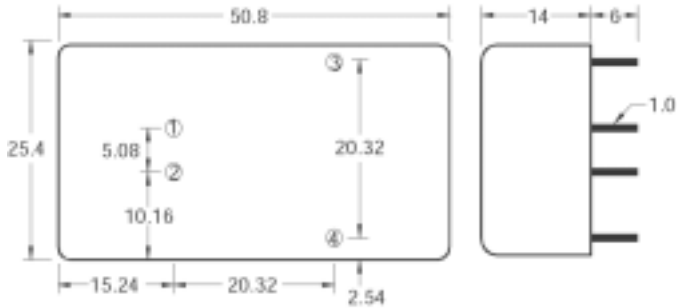

ELECTRICAL SPECIFICATIONS

Model No.	SA10-48-3.3S	SA10-48-5S	SA10-48-12S	SA10-48-15S	SA10-48-24S
Max. output wattage (W)	8.25W	10W	10W	10W	10W
Input voltage (V.DC.)	48V (36-75V)	48V (36-75V)	48V (36-75V)	48V (36-75V)	48V (36-75V)
Output voltage (V.DC.)	3.3V / 2500mA	5V / 2000mA	12V / 833mA	15V / 666mA	24V / 416mA

Model No.	SA10-48-3.3S	SA10-48-5S	SA10-48-12S	SA10-48-15S	SA10-48-24S	
Max output wattage (W)	8.25W	10W	10W	10W	10W	
Input	Input filter	π type				
	Voltage (V.DC.)	3.3	5	12	15	24
Output	Voltage accuracy	$\pm 2\%$	$\pm 2\%$	$\pm 2\%$	$\pm 2\%$	$\pm 2\%$
	Current (mA) max	2500	2000	833	666	416
	Line regulation (HL-LL) (typ.)	$\pm 0.5\%$	$\pm 0.5\%$	$\pm 0.5\%$	$\pm 0.5\%$	$\pm 0.5\%$
	Load regulation (10-100%) (typ.)	$\pm 1\%$	$\pm 1\%$	$\pm 1\%$	$\pm 1\%$	$\pm 1\%$
	Ripple	<0.2% Vout +20mV max (Vp-p)				
	Noise	<0.5% Vout +50mV max (Vp-p)				
	Efficiency	77%	79%	82%	82%	83%
	Switching frequency	125KHz	125KHz	125KHz	125KHz	125KHz
Protection	Over current protection	Works over 120% of rating and recovers automatically.				
	Over voltage protection	Zener diode clamp				
	Short circuit protection	Current limit, auto-recovery				
Isolation	Voltage	1600 VDC.				
	Resistance	10^9 ohms				
	Capacitance	1000 pF				
Environment	Operating temperature	-25°C...+71°C				
	Storage temperature	-55°C...+105°C				
	Case temperature	+95°C max.				
	Temperature coefficient	$\pm 0.02\%$ Per°C				
	Humidity	95%RH				
MTBF	>800,000 h @ 25°C (MIL-HDBK-217F)					
Physical	Dimension (L x W x H)	2.0 x 1.0 x 0.55 Inches (50.8 x 25.4x 14 mm)				
	Case Material	Six-side shielded Aluminum with Non-Conductive base				
	Weight	25 g				
	Cooling method	Free-air convection				

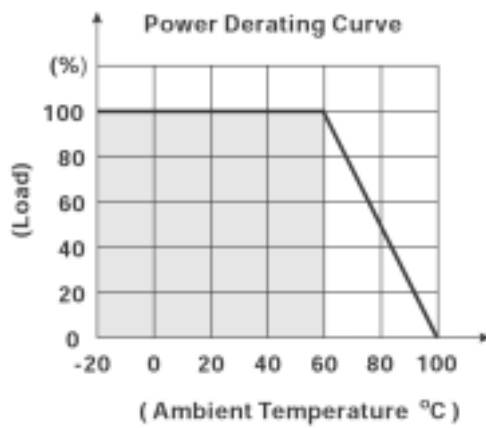
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

MECHANICAL DIMENSION (Top View)



PIN#	Single
1	-DC IN
2	+DC IN
3	-DC OUT
4	+DC OUT

DERATING



BLOCK DIAGRAM

Single Output

