

Low Power Low Offset Voltage Quad

LM339

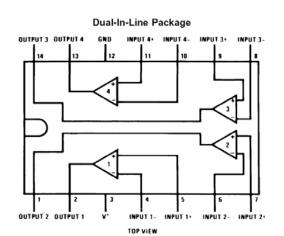
Comparators

Description	Features
The LM339 consists of four independent precision voltage comparators with an offset voltage specification as low as 2 mV max for all four comparators. These were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage. These comparators also have a unique characteristic in that the input common- mode voltage range includes ground, even though operated from a single power supply voltage.	 Wide supply voltage range LM339 is 2 to 36 VDC or - 1 to - 18 Very low supply current drain (0.8 mA) — independent of supply voltage Low input biasing current: 25 nA Low input offset current: - 5 nA n Offset voltage: - 3 mV Input common- mode voltage range includes GND

- Differential input voltage range equal to the • power supply voltage
- Low output saturation voltage: 250 mV at 4 • mA
- Output voltage compatible with TTL, DTL, • ECL, MOS and CMOS logic systems

Pin Connection

comparators.



Application areas include limit comparators, simple analog to

digital converters; pulse, squarewave and time delay

generators; wide range VCO; MOS clock timers;

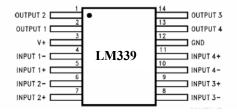
multivibrators and high voltage digital logic gates. The LM339

was designed to directly interface with TTL and CMOS. When

operated from both plus and minus power supplies, they will directly interface with MOS logic - where the low power drain of the LM339 is a distinct advantage over standard

Ordering Information

Devices	Package	Temp.
LM339M	SO-14	0 °C to 70 °C
LM339P	14-DIP	0 °C to 70 °C



Absolute Maximum Rating

Parameter	LM339	Unit
Supply Voltage	36	V
Differential Input Voltage	36	V
Input Voltage	-0.3 to 36	V
Input Current	50	mA
Storage Temperature	0 to 70	°C
Lead Temperature (solder 10 Second)	260	°C
ESD	250	V

Electrical Characteristics

 $(V_{CC} = 5V; T_J = 25^{\circ}C$, unless otherwise specified)

PARAMETER	ТІ	TEST CONDITIONS*			TYP	MAX	UNIT
V _{IO}	V _{CC} =5V to 30V		25 °C		2	5	mV
Input offset voltage	V _{IC} = V _{ICRmin} , V _O	=1.4V	Full range	İ		9	
l _{iO}	V _O =1.4V		25 °C		5	50	nA
Input offset current			Full range			150	1
I _{IB}	V _O =1.4V	V ₀ =1.4V			-25	-250	nA
Input bias current						-400	
V _{ICR}			25 °C	0 to Vcc-1.5			V
Common-mode input voltage range**			Full range	0 to Vcc-2			
A _{VD} Large-signal differential voltage amplification		V_{CC} =15V, V_0 =1.4V to 11.4V, $R_L \ge 15k\Omega$ to V_{CC}		50	200		V/mV
I _{ОН}	V _{OH} =5V, V _{ID} =1V	V _{OH} =5V, V _{ID} =1V			0.1	50	nA
High-level output current	V _{OH} =30V, V _{ID} =1V		Full range	Ì		1	μA
V _{OL}	I _{OL} =4mA, V _{ID} = -1V		25 °C		150	400	mV
Low-level output voltage						700	
I _{OL} Low-level output current	V _{OL} =1.5V, V _{ID} =	V_{OL} =1.5V, V_{ID} = -1V		6			mA
lcc	RL= ∞	V _{CC} =5V	25 °C		0.8	2	mA
Supply current		V _{CC} =30	/ Full range			2.5	

* Full range (MIN to MAX), for the LM339 is 0 °C to 70 °C. All characteristics are measured with zero common-mode input voltage unless otherwise specified.

** The voltage a tither input or common-mode should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is V_{CC} -1.5 V, but either or both inputs can go to 30 V without damage.

switching characteristics, Vcc=5V, T_A=25 °C

PARAMETER	TEST CONDITIONS		MIN	ТҮР	MAX	UNIT
Response time	R _L connected to 5V through	100-mV input step with 5-mV overdrive		1.3		μs
	5.1kΩ,	TTL-level input step		0.3		
	C _L =15pF* (See Note 1)					

* CL includes probe and jig capacitance.

NOTE 1: The response time specified is the interval between the input step function and the instant when the output crosses 1.4V.

Advance Information- These data sheets contain descriptions of products that are in development. The specifications are based on the engineering calculations, computer simulations and/ or initial prototype evaluation.

Preliminary Information- These data sheets contain minimum and maximum specifications that are based on the initial device characterizations. These limits are subject to change upon the completion of the full characterization over the specified temperature and supply voltage ranges.

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