



Single Output UHP Models

3.3 Volts @ 30 Amps 100 Watt, DC/DC Converters

Features

- Regulated 3.3V output @ 30 Amps
- Input range: 36V-75V
- Open frame: 2.3" x 2.4" x 0.47"
- Industry-standard package/pinout
- Light weight: 2.24 ounces (63.5g)
- Remose sense, Trim, On/Off Control
- High efficiency: 89%
- Fully isolated, 1500Vdc guaranteed
- Input under and overvoltage shutdown
- Output overvoltage protection
- Short circuit protection; thermal shutdown
- UL1950 and EN60950 safety approvals
- CE mark

DATEL's fully isolated UHP series of DC/DC converters affords users a practical solution for their low-voltage/high-current applications. With an input voltage range of 36 to 75 Volts, the UHP-3.3/30-D48 delivers 30 Amps of output current from a fully regulated 3.3V output. Using both surface-mount technology and planar magnetics, these converters are manufactured on a 2.3" x 2.4" open-frame package with an industry-standard pinout configuration.

UHP converters utilize a full-bridge, fixed-frequency topology along with synchronous output rectification to achieve a high efficiency of 89%. This efficiency, coupled with the open-frame package that allows unrestricted air flow, reduces internal component temperatures thereby allowing operation at elevated ambient temperatures.

These DC/DC's provide output trim, sense pins and primary side on/off control (available with positive or negative logic) or sync. Standard features also include input overvoltage and undervoltage shutdown circuitry, output overvoltage protection, output short-circuit and current limiting protection and thermal shutdown. All devices meet IEC950, UL1950 and EN60950 safety standards and carry the CE mark (meet LVD) requirements). CB reports are available on request.



XHP Series

Performance Specifications and Ordering Guide $^{\odot}$

			Output			Input						
	Vour		R/N (mVp-p) ^② Regu		Regulat	Regulation (Max.)		Bange	lin (4)	Efficiency		Package (Case.
Model	(Volts)	(Amps)	Тур.	Max.	Line	Load 3	(Volts)	(Volts)	(mA)	Min.	Тур.	Pinout)
UHP-3.3/30-D48	3.3	30	80	120	±1%	±1%	48	36-75	120/2340	86.5	88%	C27, P17

(1) Typical at TA = +25°C under nominal line voltage and full-load conditions.

2 Ripple/Noise (R/N) measured over a 20MHz bandwidth with 10µFtantalum and 1µF ceramic output capacitors.

③ Tested from no load to 100% load.

④ Nominal line voltage, no load/full load condition.

PART NUMBER STRUCTURE



Unipolar High-Power Series

> Nominal Output Voltage: 3.3 Volts

> > Maximum Output Current: 30 Amps

Add "N" or "S" suffix as desired

Input Voltage Range: D48 = 36-75 Volts (48V nominal)

Part Number Suffixes

UHP 30 Amp DC/DC's are designed so a negative logic on/off control ("N" suffix) or a Sync function ("S" suffix) can be added in the pin 3 position.

No Suffix On/Off Control function (positive polarity)

N Negative polarity on/off control

S Sync function

MECHANICAL SPECIFICATIONS



I/O Connections			
Pin	Function P17		
1	–Input		
2	No Pin		
3	On/Off Control		
4	+Input		
5	+Output		
6	+Sense		
7	Trim		
8	-Sense		
9	-Output		

Performance/Functional Specifications

Typical @ TA = +25°C under nominal line voltage, full-load conditions, unless noted.

Ir	nput		
Input Voltage Range	36-75 Volts (48V nominal)		
Overvoltage Shutdown	77-81 Volts (79V typical)		
Start-Up Threshold	34-36 Volts (35V typical)		
Undervoltage Shutdown	32.5-34.5 Volts (33.5V typical)		
Input Current: Normal Operating Conditions Standby Mode:	See Ordering Guide		
Off, OV, UV, Thermal Shutdown	TBD typical		
Input Reflected Ripple Current:			
Source Impedance	<0.1 Ω , no external input filtering TBD		
Internal Input Filter Type	Pi (0.01µF - TBDµH - 3.3µF)		
Reverse-Polarity Protection	1 minute duration, 5A maximum		
On/Off Control (Pin 3): ③ ④ ⑥ "N" Suffix Models	On = open or 2.5V to +VIN, I_{IN} = less than 50 μ A Off = 0-0.8V, IIN = 200 μ A @ 0V On = 0-0.8V, IIN = TBD @ 0V Off = open or TBD Volts		
Sync (Option, Pin 6): ③ ④ ⑥ Input Threshold	TBD Volts		
Input Voltage High	TBD Volts		
Input Resistance	TBD minimum		
Output High Voltage (100µA load)	TBD Volts		
Output Drive Current	TBD		
Input/Output Pulse Width	IBD nsec		
	itput		
Vour Accuracy.			
3 3V Output	+1.0% maximum		
3.3V Output	±1.0% maximum		
3.3V Output Minimum Loading Per Specification Binnle/Noice (20MHz BW) (5)	±1.0% maximum No load		
3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) (5)	±1.0% maximum No load See Ordering Guide See Ordering Guide		
3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) Line/Load Regulation	±1.0% maximum No load See Ordering Guide See Ordering Guide		
3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) (5) Line/Load Regulation Efficiency Output Voltage Space Pange	±1.0% maximum No load See Ordering Guide See Ordering Guide See Ordering Guide		
3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) (S) Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range (2)	±1.0% maximum No load See Ordering Guide See Ordering Guide +10% +10%		
3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) (5) Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range (2) Isolation Voltage	±1.0% maximum No load See Ordering Guide See Ordering Guide +10% ±10%, -20%		
3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) ⑤ Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range ② Isolation Voltage: Input-to-Output	±1.0% maximum No load See Ordering Guide See Ordering Guide \$ee Ordering Guide +10% ±10%, -20%		
3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range Isolation Voltage: Input-to-Output Isolation Resistance	±1.0% maximum No load See Ordering Guide See Ordering Guide +10% ±10%, -20% 1500Vdc minimum 100MΩ		
3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) (5) Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range (2) Isolation Voltage: Input-to-Output Isolation Resistance Isolation Capacitance	±1.0% maximum No load See Ordering Guide See Ordering Guide +10% ±10%, -20% 1500Vdc minimum 100MΩ 940pF		
3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) ⑤ Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range ② Isolation Voltage: Input-to-Output Isolation Resistance Isolation Capacitance Current Limit Inception:	±1.0% maximum No load See Ordering Guide See Ordering Guide +10% ±10%, -20% 1500Vdc minimum 100MΩ 940pF		
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3.3V Output 3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range Isolation Voltage: Input-to-Output Isolation Resistance Isolation Resistance Isolation Capacitance Current Limit Inception: 3.3V @ 98.5% Vout Short Circuit Current: 3.3V Output	±1.0% maximum No load See Ordering Guide See Ordering Guide +10% ±10%, -20% 1500Vdc minimum 100MΩ 940pF 33-35 Amps TBD Amps average current		
3.3V Output 3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) (S) Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range (2) Isolation Voltage: Input-to-Output Isolation Resistance Isolation Capacitance Current Limit Inception: 3.3V (@ 98.5% Vout Short Circuit Current: 3.3V Output Overvoltage Protection: 3.3V Output	±1.0% maximum No load See Ordering Guide See Ordering Guide ±10% ±10%, -20% 1500Vdc minimum 100MΩ 940pF 33-35 Amps TBD Amps average current Comparator, latching TBD Volts		
3.3V Output 3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) (S) Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range @ Isolation Voltage: Input-to-Output Isolation Resistance Isolation Resistance Isolation Capacitance Current Limit Inception: 3.3V @ 98.5% Vout Short Circuit Current: 3.3V Output Overvoltage Protection: 3.3V Output Maximum Capacitive Loading	±1.0% maximum No load See Ordering Guide See Ordering Guide \$ee Ordering Guide ±10% ±10%, -20% 1500Vdc minimum 100MΩ 940pF 33-35 Amps TBD Amps average current Comparator, latching TBD Volts TBD µF		
3.3V Output 3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) (S) Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range (2) Isolation Voltage: Input-to-Output Isolation Resistance Isolation Capacitance Current Limit Inception: 3.3V (2000) 98.5% Vout Short Circuit Current: 3.3V Output Overvoltage Protection: 3.3V Output Maximum Capacitive Loading Temperature Coefficient	±1.0% maximum No load See Ordering Guide See Ordering Guide See Ordering Guide +10% ±10%, -20% 1500Vdc minimum 100MΩ 940pF 33-35 Amps TBD Amps average current Comparator, latching TBD Volts TBD μF ±0.02% per °C		
3.3V Output 3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) (5) Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range (2) Isolation Voltage: Input-to-Output Isolation Resistance Isolation Resistance Isolation Capacitance Current Limit Inception: 3.3V (2000) 98.5% Vout Short Circuit Current: 3.3V Output Overvoltage Protection: 3.3V Output Overvoltage Protection: 3.3V Output Maximum Capacitive Loading Temperature Coefficient	±1.0% maximum No load See Ordering Guide See Ordering Guide See Ordering Guide +10% ±10%, -20% 1500Vdc minimum 100MΩ 940pF 33-35 Amps TBD Amps average current Comparator, latching TBD µF ±0.02% per °C haracteristics		
3.3V Output 3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) (5) Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range (2) Isolation Voltage: Input-to-Output Isolation Resistance Isolation Resistance Isolation Capacitance Current Limit Inception: 3.3V (20 98.5% Vour) Short Circuit Current: 3.3V Output Overvoltage Protection: 3.3V Output Overvoltage Protection: 3.3V Output Maximum Capacitive Loading Temperature Coefficient Dynamic C Dynamic Comparison	±1.0% maximum No load See Ordering Guide See Ordering Guide \$ee Ordering Guide ±10% ±10%, -20% 1500Vdc minimum 100MΩ 940pF 33-35 Amps TBD Amps average current Comparator, latching TBD µF ±0.02% per °C haracteristics TBDµsec maximum		
3.3V Output 3.3V Output Minimum Loading Per Specification Ripple/Noise (20MHz BW) (5) Line/Load Regulation Efficiency Output Voltage Sense Range Trim Range (2) Isolation Voltage: Input-to-Output Isolation Resistance Isolation Resistance Isolation Capacitance Current Limit Inception: 3.3V (20 98.5% Vour) Short Circuit Current: 3.3V Output Overvoltage Protection: 3.3V Output Overvoltage Protection: 3.3V Output Maximum Capacitive Loading Temperature Coefficient Dynamic C Dynamic Load Response: 3.3V (50-100% load step to 1% Vour) Start-Up Time:	±1.0% maximum No load See Ordering Guide See Ordering Guide \$\$ee Ordering Guide +10% ±10%, -20% 1500Vdc minimum 100MΩ 940pF 33-35 Amps TBD Amps average current Comparator, latching TBD Volts TBD µF ±0.02% per °C haracteristics TBDµsec maximum		
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DynaEnvironmental				
MTBF ⑦	Bellcore, ground fixed, full power 25°C ambient TBD million hours			
Operating Temperature (Ambient): 2				
Without Derating (200lfm)	+52°C			
With Derating	To +100°C (See Derating Curves)			
Thermal Shutdown	TBD			
Storage Temperature	-40 to +120°C			
Physical				
Dimensions	2.3" x 2.4" x 0.465" (58.4 x 61 x 11.8mm)			
Pin Material	Brass, solder coated			
Weight:	2.24 ounces (63.5 grams)			
Bular and the One and any large dation of a set	Our supplier set			

Primary to Secondary Insulation Level Operational

 $\odot~$ All models are specified with external 10 μF tantalum and 1 μF ceramic output capacitors.

② See Technical Notes/Graphs for details.

③ The On/Off Control function can be replaced with a Sync function. See Part Number Suffixes and Technical Notes for details.

 $\circledast\,$ Applying a voltage to On/Off Control (pin 3) when no input power is applied to the converter can cause permanent damage.

(5) Output noise may be further reduced with the installation of additional external output capacitors. See Technical Notes.

⑥ On/Off control is designed to be driven with open collector or by appropriate voltage levels. Voltages must be referenced to the –Input (pin 1).

Demonstrated MTBF available on request.

Absolute Maximum Ratings				
Input Voltage: Continuous: Transient (100msec):	81 Volts 100 Volts			
Input Reverse-Polarity Protection	Input Current must be <5A. 1 minute duration. Fusing recommended.			
Output Current	Current limited. Devices can withstand an indefinite output short circuit.			
On/Off Control (Pin 3) Max. Voltages Referenced to –Input (pin 1)	+Vin			
Storage Temperature	-40 to +120°C			
Lead Temperature (Soldering, 10 sec.)	+300°C			

These are stress ratings. Exposure of devices to any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied, nor recommended.

UHP Models

XHP Series

TECHNICAL NOTES

Trimming Output Voltages

UHP converters have a trim capability (pin 7) that allows users to adjust the output voltage 5%. Adjustments can be accomplished via a trim pot, Figure 2, or a single fixed resistor as shown in Figures 3 and 4. A single fixed resistor can increase or decrease the output voltage depending on its connection. Fixed resistors should have absolute TCR's less than 100ppm/°C to minimize sensitivity to changes in temperature.

A single resistor connected from the Trim (pin 7) to the +Sense (pin 6), see Figure 3, will increase the output voltage. A resistor connected from the Trim (pin 7) to the –Sense (pin 8) will decrease the output voltage. See Figure 4.

Trim adjustments greater than +10%/-20% can have an adverse effect on the converter's performance and are not recommended.



Figure 2. Trim Connections Using A Trim Pot



Figure 3. Trim Connections To Increase Output Voltages Using Fixed Resistors



Figure 4. Trim Connections To Decrease Output Voltages Using Fixed Resistors

Trim Up Equation

$$R_{T_{UP}}(k\Omega) = \frac{13.3(Vo - 1.226)}{Vo - 3.3} - 10.2$$

Trim Down Equation

$$R_{T_{\text{DOWN}}}(k\Omega) = \frac{16.31}{3.3 - \text{Vo}} - 10.2$$

Accuracy of adjustment is subject to tolerances or resistor values and factory-adjusted output accuracy. Vo = desired output voltage.



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ISO 9001 REGISTERED

DS0492 2/01

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