

Fast Turn-off Asymmetric Thyristor/Diode Module

Replaces April 1999 version, DS4200-4.0

DS4200-5.0 January 2000

APPLICATIONS

- High Frequency High Power Choppers And Inverters.
- Ultrasonic Generators.
- Welding.
- PWM Inverters.

$\begin{array}{ccc} \text{KEY PARAMETERS} \\ \text{V}_{\text{DRM}} & 1400\text{V} \\ \text{I}_{\text{TSM}} & 2000\text{A} \\ \text{I}_{\text{T(AV)}} \text{ per arm} & 110\text{A} \\ \text{V}_{\text{isol}} & 2500\text{V} \\ \text{t}_{\text{a}} & 10/12/15 \mu\text{s} \end{array}$

DESCRIPTION

The MAS 110S is a fast thyristor/diode module in an electrically isolated package. The semiconductors are are pressure contact mounted giving high resistance to thermal fatigue, and having excellent heat dissipation qualities.

Isolation medium is non-toxic alumina.

The MAS110S is recognised under the 'Recognised Component Program of Underwriters Laboratories Inc. USA. File number E151069.

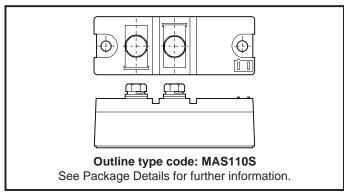


Fig.1 Package outline (not to scale)

VOLTAGE RATINGS

Type Number	Repetitive Peak Off-state Voltage V _{DRM} V	Conditions
MAS110S 14 MAS110S 12 MAS110S 10 MAS110S 08 MAS110S 06	1400 1200 1000 800 600	$T_{vj} = 125^{\circ}C,$ $I_{DRM} = 50mA,$ $V_{DSM} = V_{DRM} + 100V$

For full description of part number see 'Ordering Information'.

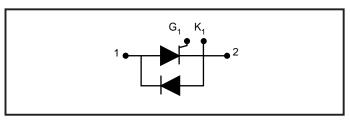


Fig.2 Single circuit

THYRISTOR CURRENT RATINGS

Symbol	Parameter	Conditions	Max.	Units
I _{T(AV)}	Mean forward current	Half wave resistive load, T _{case} = 75°C	110	А
I _{T(RMS)}	RMS value	T _{case} = 75°C	175	А

THYRISTOR SURGE RATINGS

Symbol	Parameter	Parameter Conditions		Units
I _{TSM}	Surge (non-repetitive) on-state current	10ms half sine; T _{case} = 125°C	2.0	kA
l ² t	I ² t for fusing	$V_R = 0\% V_{DRM}$	20.0 x 10 ³	A²s

THYRISTOR DYNAMIC CHARACTERISTICS

Symbol	Parameter	Conditions	i	Min.	Max.	Units
V _{TM}	Maximum on-state voltage	At 600A peak, T _{case} = 25°C		-	2.9	V
I _{DRM}	Peak off-state current	At V _{DRM} , T _{case} = 125°C		-	70	mA
dV/dt	Maximum linear rate of rise of off-state voltage	To 60% V_{DRM} $T_j = 125$ °C, Gate open circuit		-	1000	V/µs
dI/dt	Rate of rise of on-state current	From 67% V_{DRM} to 600A, Gate source 20V, 20 Ω $t_r = < 0.5 \mu s$, $T_j = 125 ^{\circ} C$	Repetitive 50Hz	-	500	A/μs
V _{T(TO)}	Threshold voltage	At T _{vj} = 125°C		-	1.6	V
r _T	On-state slope resistance	At T _{vj} = 125°C		-	1.4	mΩ
t _q	Turn-off time	$I_T = 100A, T_j = 125^{\circ}C,$	t _q code: W	-	10	μs
		$dI_R/dt = 30A/\mu s$, $V_{GK} = 0V$ $dV/dt = 20V/\mu s$ to 60%	t _q code: S	-	12	μs
		V_{DRM} , $V_{R} = 1V$.	t _q code: X	-	15	μs

THYRISTOR GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Conditions	Тур.	Max.	Units
V _{GT}	Gate trigger voltage	$V_{DRM} = 12V, T_{case} = 25^{\circ}C, R_{L} = 30\Omega$	-	4.0	\ \
I _{GT}	Gate trigger current	$V_{DRM} = 12V, T_{case} = 25^{\circ}C$	-	250	mA
V _{RGM}	Peak reverse gate voltage		-	7.0	V
I _{FGM}	Peak forward gate current	Anode positive with respect to cathode	-	10	А
P _{GM}	Peak gate power	-	-	50	W
P _{G(AV)}	Mean gate power	Average timing = 10ms	-	15	W

DIODE CURRENT RATINGS

Symbol	Parameter	Conditions	Max.	Units
I _{T(AV)}	Mean forward current	Half wave resistive load, T _{case} = 75°C	112	А
I _{T(RMS)}	RMS value	T _{case} = 75°C	175	А

DIODE SURGE RATINGS - PER ARM

Symbol	Parameter	Conditions	Max.	Units
I _{FSM}	Surge (non-repetitive) forward current	10ms half sine; T _{case} = 130°C	3.5	kA
l²t	I ² t for fusing	$V_R = 0\% V_{RRM}$	61.25 x 10 ³	A²s

DIODE DYNAMIC CHARACTERISTICS

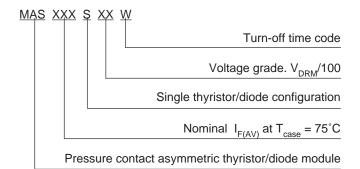
Symbol	Parameter	Conditions	Max.	Units
V _{FM}	Forward voltage	At 600A, T _{case} = 25°C.	2.65	V
I _{RRM}	Peak reverse current	At V _{RRM} , T _{case} = 125°C.	70	mA
t _{rr}	Reverse recovery time	$T_{case} = 125^{\circ}C, dI_{R}/dt = -50V/\mu s, I_{FM} = 200A$	1.3	μs
V _{TO}	Threshold voltage	At T _{vj} = 125°C.	1.6	V
r _T	Forward slope resistance	At $T_{vj} = 125^{\circ}C$.	1.5	mΩ

THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions	Min.	Max.	Units
R _{th(j-c)}	Thermal resistance - junction to case (Thyristor or diode)	dc	-	0.21	°C/W
R _{th(c-h)}	Thermal resistance - case to heatsink (Thyristor or diode)	Mounting force 6Nm with mounting compound.	-	0.07	°C/W
T _{vj}	Virtual junction temperature	-	-	125	°C
T _{op}	Operating temperature range	-	-40	125	°C
T _{stg}	Storage temperature range		-40	125	°C
V _{isol}	Isolation voltage	Commoned terminals to base plate. AC RMS, 1 min, 50Hz.	-	2.5	kV
-	Mounting torque		-	6.0	Nm

ORDERING INFORMATION

The module type number is made up as follows:



Examples:

MAS 110 S 12 W MAS 110 S 08 X

MODULE MOUNTING RECOMMENDATIONS

- Adequate heatsinking is required to maintain the base temperature at 75°C if full rated current is to be achieved. Power dissipation may be calculated by use of $V_{T(TO)}$ and r_T information and loss curves in accordance with standard formulae. We can provide assistance with calculations or choice of heatsink if required.
- The heatsink surface must be smooth and flat; a surface finish of N6 (32 μ in) and a flatness within 0.05mm (0.002") are recommended.
- Immediately prior to mounting, the heatsink surface should be lightly scrubbed with fine emery, Scotch Brite™ or a mild chemical etchant and then cleaned with a solvent to remove oxide build up and foreign material. Care should be taken to ensure no foreign particles remain.
- An even coating of thermal compound (eg. Unial) should be applied to both the heatsink and module mounting surfaces. This should ideally be 0.05mm (0.002") per surface to ensure optimum thermal performance.
- After application of thermal compound, place the module squarely over the mounting holes, (or 'T' slots) in the heatsink. Using a torque wrench, slowly tighten the recommended fixing bolts at each end, rotating each in turn no more than 1/4 of a revolution at a time. Continue until the required torque of 6Nm (55lb.ins) is reached at both ends.
- It is not acceptable to fully tighten one fixing bolt before starting to tighten the others. Such action may DAMAGE the module.

Curves

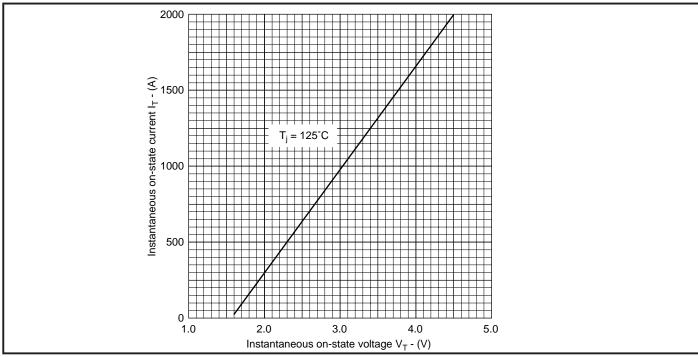


Fig.3 Maximum (limit) on-state characteristics (thyristor only)

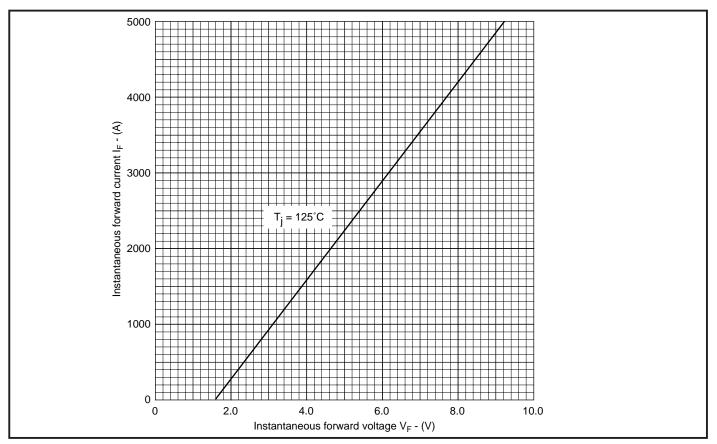


Fig.4 Maximum (limit) forward characteristics (diode only)

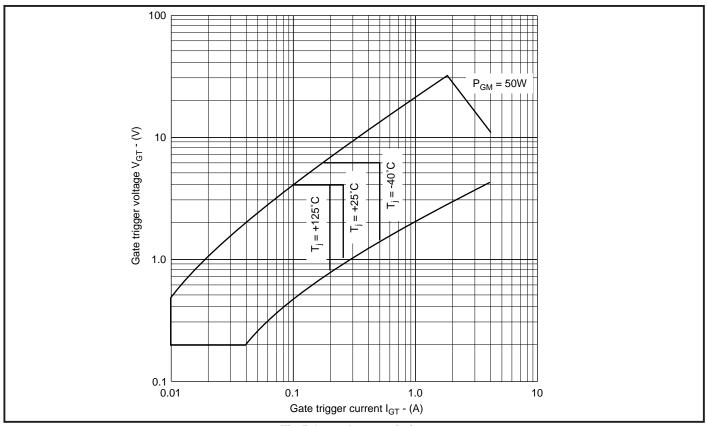


Fig.5 Gate characteristics

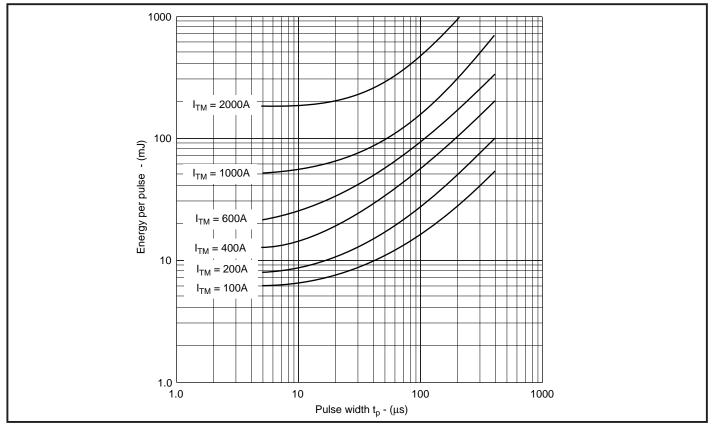


Fig.6 Sinusoidal energy per pulse (thyristor only)

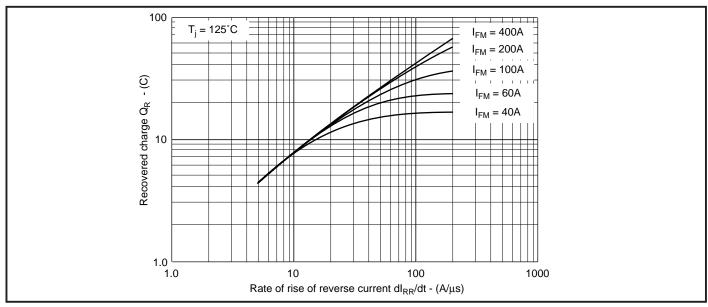


Fig.7 Recovered charge (diode only)

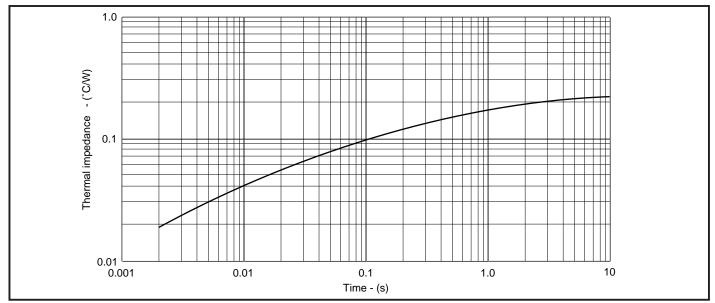
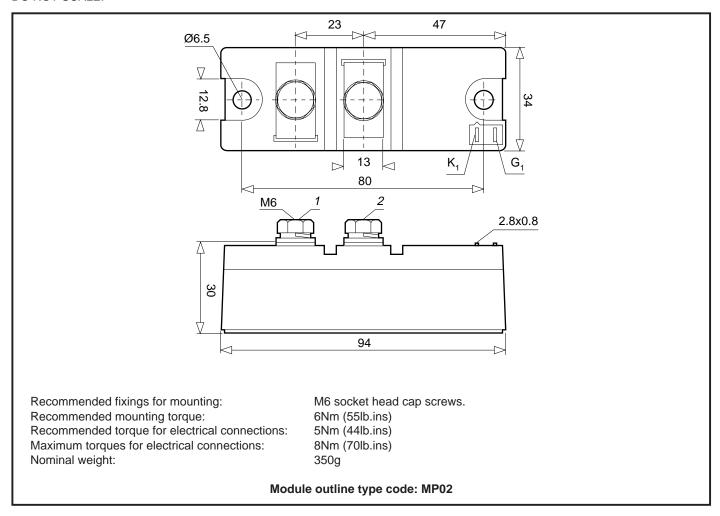


Fig.8 Maximum (limit) transient thermal impedance (thyristor only)

PACKAGE DETAILS

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink / clamping systems in line with advances in device types and the voltage and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the up to date CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete solution (PACs).

HEATSINKS

Power Assembly has it's own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance or our semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest Sales Representative or the factory.



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