

Phase Control Thyristor

Replaces January 2000 version, DS45253-4.0

DS4253-5.0 July 2001

FEATURES

■ High Surge Capability

APPLICATIONS

- High Power Drives
- High Voltage Power Supplies
- DC Motor Control
- Welding
- Battery Chargers

VOLTAGE RATINGS

| Type Number | Repetitive Peak Voltages V _{DRM} V _{RRM} V | Conditions |
|----------------------------------|---|--|
| TK18 12 M or K TK18 10 M or K | 1200 1000 | $ \begin{aligned} & T_{vj} = 0^{\circ} \text{ to } 125^{\circ}\text{C}, \\ & I_{\text{DRM}} = I_{\text{RRM}} = 100\text{mA}, \\ & V_{\text{DRM}}, V_{\text{RRM}} t_{\text{p}} = 10\text{ms}, \\ & V_{\text{DSM}} \& V_{\text{RSM}} = \\ & V_{\text{DRM}} \& V_{\text{RRM}} + 100V \\ & \text{respectively} \end{aligned} $ |

Lower voltage grades available.

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table, then:-

Add K to type number for 1/2" 20 UNF thread, e.g. TK18 12K.

or

Add M to type number for M12 thread, e.g. TK18 12M.

Note: Please use the complete part number when ordering and quote this number in any future correspondance relating to your order.

KEY PARAMETERS

 V_{DRM} 1200V $I_{T(AV)}$ 115A I_{TSM} 2000A $dVdt^*$ 200V/ μ s dI/dt 500A/ μ s

^{*}Higher dV/dt selections available

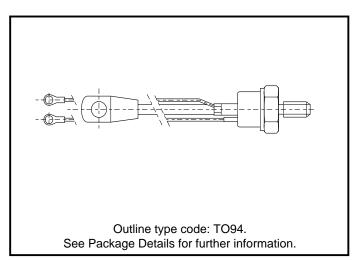


Fig. 1 Package outline



CURRENT RATINGS

$T_{case} = 60$ °C unless stated otherwise.

| Symbol | Parameter | Conditions | Max. | Units |
|---------------------|--------------------------------------|--------------------------|------|-------|
| I _{T(AV)} | Mean on-state current | Half wave resistive load | 152 | Α |
| I _{T(RMS)} | RMS value | - | 239 | А |
| I _T | Continuous (direct) on-state current | - | 206 | Α |

$T_{case} = 80$ °C unless stated otherwise.

| Symbol | Parameter | Conditions | Max. | Units |
|---------------------|--------------------------------------|--------------------------|------|-------|
| I _{T(AV)} | Mean on-state current | Half wave resistive load | 115 | А |
| I _{T(RMS)} | RMS value | - | 180 | А |
| Ι _τ | Continuous (direct) on-state current | - | 155 | Α |

SURGE RATINGS

| Symbol | Parameter | Conditions | Max. | Units |
|------------------|---|--|------------------------|------------------|
| I _{TSM} | Surge (non-repetitive) on-state current | 10ms half sine; T _{case} = 125°C | 1.6 | kA |
| l ² t | I ² t for fusing | V _R = 50% V _{RRM} - 1/4 sine | 12.8 x 10 ³ | A²s |
| I _{TSM} | Surge (non-repetitive) on-state current | 10ms half sine; T _{case} = 125°C | 2.0 | kA |
| l ² t | I ² t for fusing | V _R = 0 | 20.0 x 10 ³ | A ² s |

THERMAL AND MECHANICAL DATA

| Symbol | Parameter | Conditions | Min. | Max. | Units |
|----------------------|---------------------------------------|---|------|------|-------|
| R _{th(j-c)} | Thermal resistance - junction to case | dc | - | 0.24 | °C/W |
| R _{th(c-h)} | Thermal resistance - case to heatsink | Mounting torque 15.0Nm with mounting compound | - | 0.08 | °C/W |
| T _{vj} | Virtual junction temperature | On-state (conducting) | - | 125 | °C |
| | | Reverse (blocking) | - | 125 | °C |
| T _{stg} | Storage temperature range | | -40 | 150 | °C |
| - | Mounting torque | | 12.0 | 15.0 | Nm |



DYNAMIC CHARACTERISTICS

| Symbol | Parameter | Conditions | | Min. | Max. | Units |
|------------------------------------|--|--|-----------------|------|------|-------|
| V _{TM} | Maximum on-state voltage | At 300A peak, T _{case} = 25°C | | - | 1.5 | V |
| I _{RRM} /I _{DRM} | Peak reverse and off-state current | At V _{RRM} /V _{DRM} , T _{case} = 125°C | | - | 10 | mA |
| dV/dt | Maximum linear rate of rise of off-state voltage | To 60% V_{DRM} T_j = 125°C, Gate open circuit | | - | 200 | V/µs |
| -11/-14 | | Gate source 20V, 20Ω $t_r \le 0.5\mu s$, $T_j = 125^{\circ}C$ | Repetitive 50Hz | - | 500 | A/μs |
| dl/dt | Rate of rise of on-state current | | Non-repetitive | - | 800 | A/μs |
| V _{T(TO)} | Threshold voltage | At T _{vj} = 125°C | | - | 0.9 | V |
| r _T | On-state slope resistance | At T _{vj} = 125°C | | - | 2.0 | mΩ |
| t _{gd} | Delay time | $V_D = 300V$, $I_G = 1A$, $I_T = 50A$, $dI/dt = 50A/\mu s$, $dI_G/dt = 1A/\mu s$, $T_j = 25^{\circ}C$ | | - | 1.5 | μs |
| I _L | Latching current | $T_{j} = 25^{\circ}C, V_{D} = 12V$ | | - | - | mA |
| I _H | Holding current | $T_j = 25^{\circ}C, V_D = 12V, I_{TM} = 1A$ | | - | 50 | mA |

GATE TRIGGER CHARACTERISTICS AND RATINGS

| Symbol | Parameter | Conditions | Тур. | Max. | Units |
|-----------------------------|---------------------------|--|------|------|-------|
| V _{GT} | Gate trigger voltage | $V_{DRM} = 12V$, $T_{case} = 25^{\circ}C$, $R_{L} = 6\Omega$ | - | 3.0 | V |
| I _{GT} | Gate trigger current | $V_{DRM} = 12V$, $T_{case} = 25^{\circ}C$, $R_{L} = 6\Omega$ | - | 125 | mA |
| $V_{\sf GD}$ | Gate non-trigger voltage | At V_{DRM} $T_{case} = 125$ °C, $R_L = 12\Omega$ | - | 0.2 | V |
| V_{FGM} | Peak forward gate voltage | Anode positive with respect to cathode | - | 3.0 | V |
| V_{FGN} | Peak forward gate voltage | Anode negative with respect to cathode | - | 0.25 | V |
| V_{RGM} | Peak reverse gate voltage | | - | 5 | V |
| I _{FGM} | Peak forward gate current | Anode positive with respect to cathode | - | 4 | А |
| $P_{\scriptscriptstyle GM}$ | Peak gate power | - | - | 16 | W |
| $P_{G(AV)}$ | Mean gate power | | - | 3 | W |



CURVES

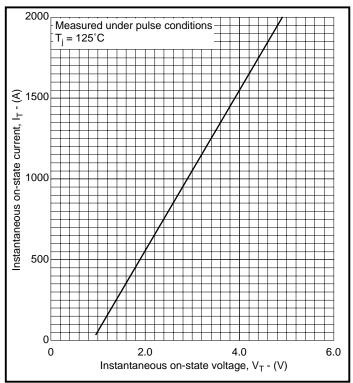


Fig.2 Maximum (limit) on-state characteristics

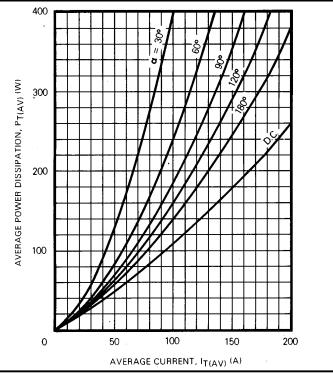
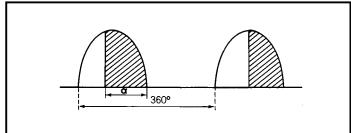
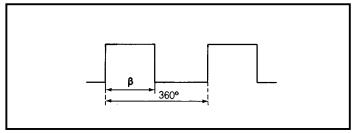


Fig.3 Maximum on-state power dissipation for sinusoidal current waveform

SINUSOIDAL CURRENT WAVEFORM



RECTANGULAR CURRENT WAVEFORM



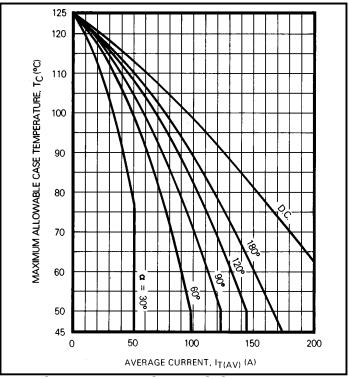
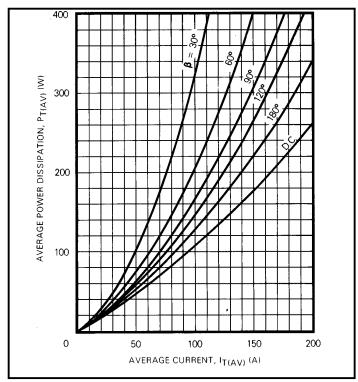


Fig.4 Maximum allowable case temperature for sinusoidal current waveform





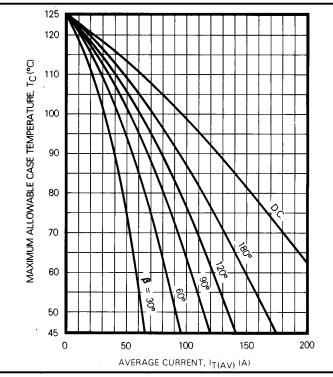
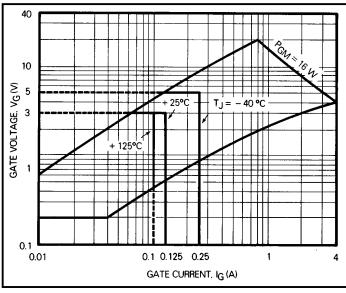
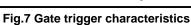


Fig.5 Maximum on-state power dissipation for rectangular current waveform

Fig.6 Maximum allowable case temperature for rectangular current waveform





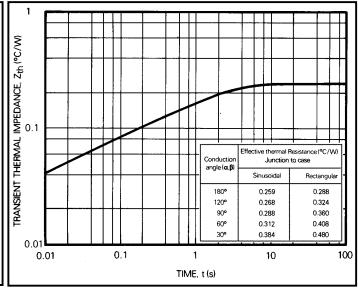
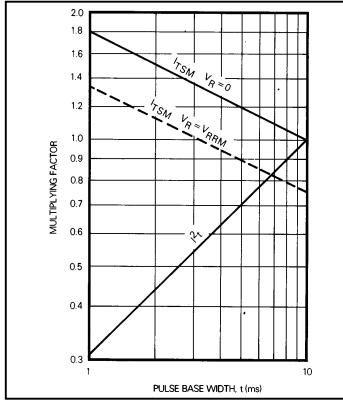


Fig.8 Transient thermal impedance - junction to case





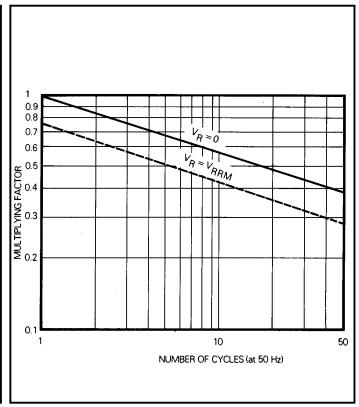


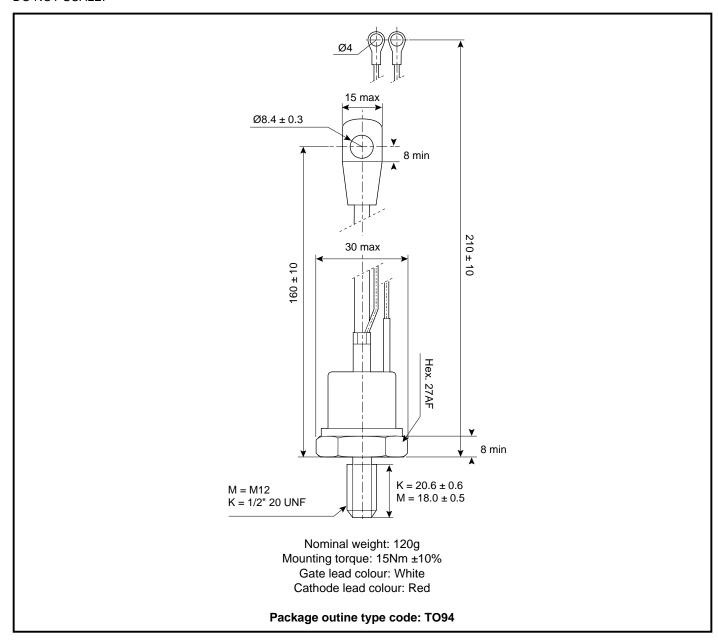
Fig.9 Multiplying factor for non-repetive sub-cycle surge onstate current and I^2t rating

Fig.10 Multiplying factor for non-repetive surge on-state current



PACKAGE DETAILS

For further package information, please contact your nearest Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor. and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

DEVICE CLAMPS

Disc devices require the correct clamping force to ensure their safe operation. The PACS range includes a varied selection of pre-loaded clamps to suit all of our manufactured devices. Types available include cube clamps for single side cooling of 'T' 23mm and 'E' 30mm discs, and bar clamps right up to 83kN for our 'Z' 100mm thyristors and diodes.

Clamps are available for single or double side cooling, with high insulation versions for high voltage assemblies.

Please refer to our application note on device clamping, AN4839

HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or customer service office.



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Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

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