GDU 90-20310



Gate Drive Unit

Replaces March 1998 version, DS4564-2.2

DS4564-3.0 January 2000

This datasheet should be used in conjunction with the application note AN4571, GDU9X-XXXXX Series, Gate Drive Unit.

APPLICATIONS KEY PARAMETERS

■ Used with Gate Turn-Off Thyristors in high current switching applications

 $\begin{array}{lll} I_{\text{FGM}} & 40\text{A} \\ I_{\text{G(ON)}} & 8\text{A} \\ \text{d}I_{\text{GQ}}/\text{d}t & 40\text{A}/\text{µs} \end{array}$

CONDITIONS - (UNLESS STATED OTHERWISE)

V ₁ = +5V	V ₂ = +15V		V ₃ = -15V	
Test circuit GTO		DG758BX		
GDU connection to GTO		500mm CO - AX cable type RC5327230		
Test circuit emitter and gate drive emitter		Honeywell sweetspot HFE 4020 - 013		
Test circuit emitter current		30mA		
Test circuit receiver		Honeywell sweetspot HFD 3029 - 002		
Gate drive unit receiver		Honeywell sweetspot HFD 3031 - 002		

ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I _{V1}	+5V PSU current	500Hz, 50% duty cycle	-	-	4.40	А
I _{V2}	+15V PSU current	500Hz	-	-	0.48	А
I _{V3}	-15V PSU current	500Hz, I _T = 3000A GTO T _j = 125°C	-	-	10.0	А
V _{1(Min)}	+5V PSU minimum	-	3.8	-	-	V
V _{2(Min)}	+15V PSU minimum	-	14.0	-	-	V
V _{3(Min)}	-15V PSU minimum	-	14.0	-	-	V
I _{FGM}	Peak forward gate current	-	40	-	-	А
I _{G(ON)}	On-state gate current	-	-	8	-	А
dl _{FG} /dt	Rate of rise of positive gate current	Measured 10 - 75% I _{FGM}	-	40	-	A/μs
dl _{GQ} /dt	Rate of rise of negative gate current $I_T = 3000A$, 90% $I_{G(ON)} - 50\%$ I_{GQM}		-	40	-	A/μs

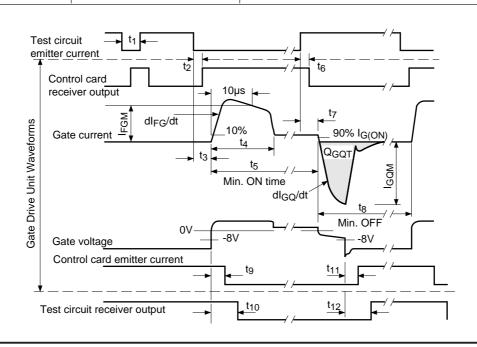
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TIMING CHARACTERISTICS

Symbol	Parameter			Conditions	Min.	Тур.	Max.	Units
t ₁ *†	No response pulse width of input signal		А	djustable by R81 + R82	2	-	3	μs
t ₂	Delay time emitter current to receiver o/p			-		-	0.8	μs
t ₃ *†	Turn-on delay emitter current to 10% I _{FGM}			-		-	6.2	μs
t ₄	I _{FGM} pulse width		-		-	16	-	μs
t ₅ *	Minimum on time 10% I _{FGM} to 90% I _{G(ON)}			Adjustable by R37		-	110	μs
t ₆	Receiver storage time			-		-	0.9	μs
t ₇	Turn-off delay. Emitter current to 90% I _{G(ON)}			-		-	2.3	μs
t ₈ *	Minimum off time 90% I _{G(ON)} to 10% I _{FGM}			Adjustable by R38		-	110	μs
t ₉	Delay time Gate volts to o/p emitter current			-	-	0.1	-	μs
t ₁₀	Turn-off delay Gate volts to test receiver o/p			-	-	0.7	-	μs
t ₁₁	Storage time Gate volts to o/p emitter current			Measured at low I _{GQM}	-	0.11	-	μs
t ₁₂	Turn-on delay Gate volts to test receiver o/p			Measured at low I _{GQM}	-	0.81	-	μs
* t.,t.,t. are factory settings. † Adjustment of t, alte			ers t	1. Varies with I. due to gate lead impedance.				

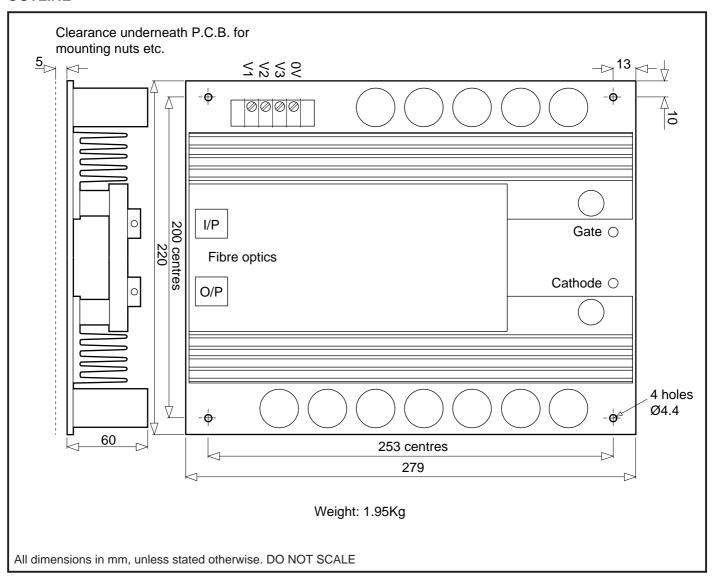
^{*} t_1, t_3, t_5, t_8 are factory settings.

^{1.} Varies with $I_{\tiny{\text{GQM}}}$ due to gate lead impedance.



[†] Adjustment of t₁ alters t₃.

OUTLINE



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POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink / clamping systems in line with advances in device types and the voltage and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the up to date CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete solution (PACs).

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Disc devices require the correct clamping force to ensure their safe operation. The PACs range offers a varied selection of preloaded clamps to suit all of our manufactured devices. This include cube clamps for single side cooling of 'T' 22mm

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Please refer to our application note on device clamping, AN4839

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