

Dual Diode Modules

Replaces December 1998 version, DS5101-3.0

DS5101-4.0 January 2000

FEATURES

- Dual Device Module
- Electrically Isolated Package
- Pressure Contact Construction
- International Standard Footprint
- Alumina (non-toxic) Isolation Medium

APPLICATIONS

- Rectifier Bridges
- DC Power Supplies
- Plating Rectifiers
- Traction Systems

VOLTAGE RATINGS

Type Number	Repetitive Peak Voltages V _{RRM}	Conditions
MP02/175 - 12	1200	T _{vj} = 150°C
MP02/175 - 10	1000	I _{RM} = 30mA
MP02/175 - 08	800	$V_{RSM} = V_{RRM} + 100V$

Lower voltage grades available. For full description of part number see "Ordering Instructions" on page 3.

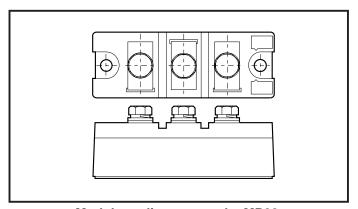
KEY PARAMETERS

 $\begin{array}{lll} V_{\text{RRM}} & 1200V \\ I_{\text{FSM}} & 5625A \\ I_{\text{F(AV)}} (\text{per arm}) & 170A \\ V_{\text{isol}} & 2500V \end{array}$

CIRCUIT OPTIONS

Code	Circuit	
НВ		
G		
GN		

PACKAGE OUTLINE



Module outline type code: MP02. See Package Details for further information

CURRENT RATINGS - PER ARM

Symbol	Parameter	Conditions		Max.	Units
	I _{F(AV)} Mean forward current	Halfwave, resistive load	T _{case} = 75°C	170	А
			T _{case} = 85°C	152	А
I _{F(AV)} M			T _{heatsink} = 75°C	149	Α
			T _{heatsink} = 85°C	133	А
I _{F(RMS)}	RMS value	T _{case} = 75°C		267	А

SURGE RATINGS - PER ARM

Symbol	Parameter	Conditions		Max.	Units
I _{FSM} Surge	Comment of the second state of the second stat	10ms half sine; T _j = 150°C	V _R = 0	5625	А
	Surge (non-repetitive) on-state current		V _R = 50% V _{RRM}	4500	А
I ² t I ² t for fusing	10ms half sine;	$V_R = 0$	158000	A ² s	
	T Clor lusing	T _j = 150°C	V _R = 50% V _{RRM}	100000	A ² s

THERMAL & MECHANICAL RATINGS

Symbol	Parameter	Conditions	Max.	Units
	dc	0.37	°C/W	
$R_{th(j-c)}$	Thermal resistance - junction to case	halfwave	0.38	°C/W
per Diode	3 phase	0.39	°C/W	
R _{th(c-hs)}	Thermal resistance - case to heatsink per Diode	Mounting torque = 6Nm with mounting compound	0.07	°C/W
T _{vj}	Virtual junction temperature		150	°C
T _{sto}	Storage temperature range		-40 to 150	°C
V _{isol}	Isolation voltage	Commoned terminals to base plate AC RMS, 1min, 50Hz	2.5	kV

CHARACTERISTICS

Symbol	Parameter	Conditions	Max.	Units
V _{FM}	Forward voltage	At 450A , T _{case} = 25°C	1.30	V
I _{RM}	Peak reverse current	At V_{RRM} , $T_j = 150^{\circ}C$	30	mA
V _{TO}	Threshold voltage	At T _{vj} = 150°C	0.81	V
r _T	On-state slope resistance	At T _{vj} = 150°C	0.84	mΩ

ORDERING INSTRUCTIONS

Part number is made up as follows:

MP02 HB 175 - 12

MP = Pressure contact module

02 = Outline type

HB = Circuit configuration code (see "circuit options" - front page)

175 = Nominal average current rating at T_{case} = 75°C

 $12 = V_{RRM}/100$

Examples:

MP02HB175 - 12 MP02G175 - 08 MP02GN175 - 10

Note: Prefered type is HB configuration. G and GN types are available for specific applications, only when requested.

MOUNTING RECOMMENDATIONS

- Adequate heatsinking is required to maintain the base temperature at 75°C if full rated current is to be achieved. Power dissipation may be calculated by use of V_(TO) and r_T information in accordance with standard formulae. We can provide assistance with calculations or choice of heatsink if required.
- The heatsink surface must be smooth and flat; a surface finish of N6 (32μin) and a flatness within 0.05mm (0.002") are recommended.
- Immediately prior to mounting, the heatsink surface should be lightly scrubbed with fine emery, Scotch Brite or a mild chemical etchant and then cleaned with a solvent to remove oxide build up and foreign material. Care should be taken to ensure no foreign particles remain.

- An even coating of thermal compound (eg. Unial) should be applied to both the heatsink and module mounting surfaces. This should ideally be 0.05mm (0.002") per surface to ensure optimum thermal performance.
- After application of thermal compound, place the module squarely over the mounting holes, (or 'T' slots) in the heatsink. Using a torque wrench, slowly tighten the recommended fixing bolts at each end, rotating each in turn no more than 1/4 of a revolution at a time. Continue until the required torque of 6Nm (55lb.ins) is reached at both ends.
- It is not acceptable to fully tighten one fixing bolt before starting to tighten the others. Such action may DAMAGE the module.

CURVES

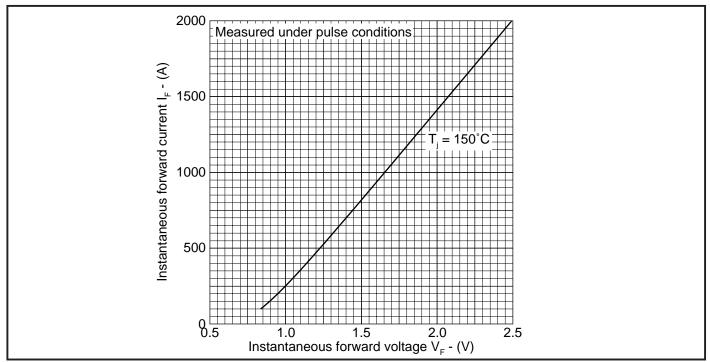


Fig. 1 Maximum (limit) forward characteristics (Per diode)

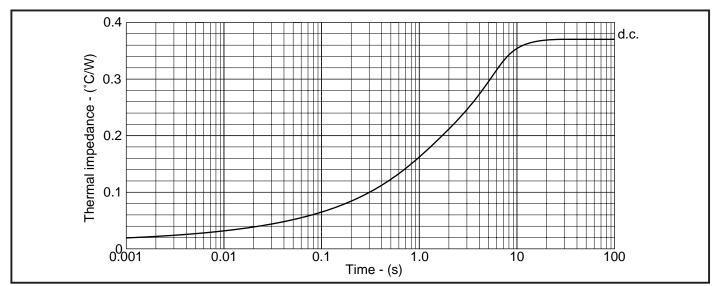


Fig. 2 Transient thermal impedance (DC) - (Per diode)

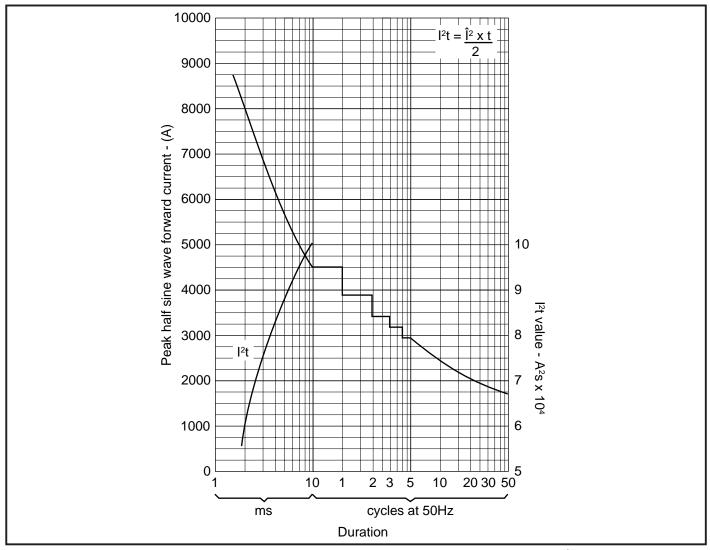


Fig. 3 Surge (non-repetitive) forward current vs time (with 0% V_{RRM}, T_{case} = 150°C)

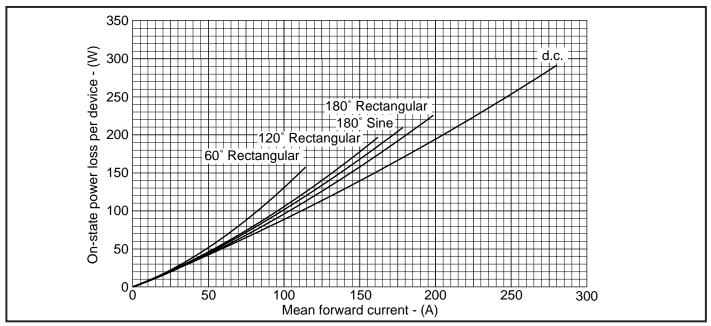


Fig. 4 On-state power loss per arm vs forward current at various conduction angles, 50/60Hz

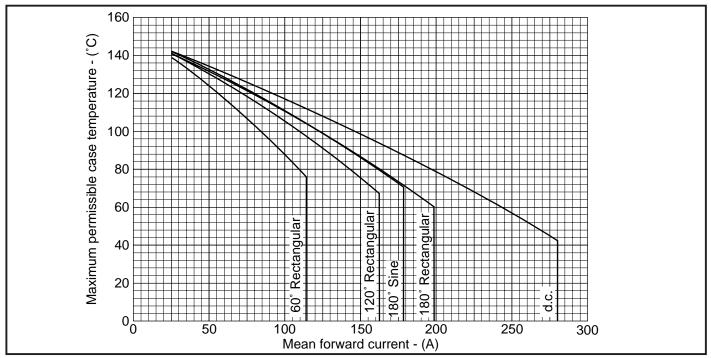


Fig. 5 Maximum permissible case temperature vs forward current per arm at various conduction angles, 50/60Hz

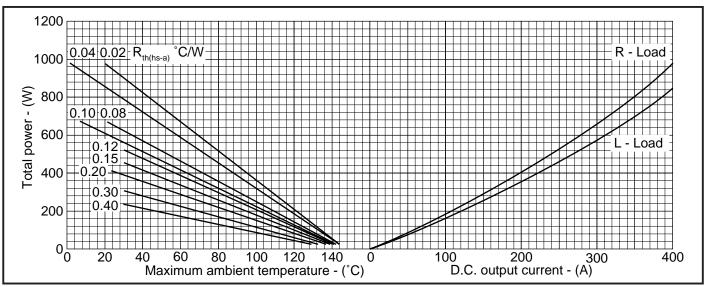


Fig. 6 50/60Hz single phase bridge dc output current vs power loss and maximum permissible ambient temperature for various values of heatsink thermal resistance.

(Note: $R_{th(hs:a)}$ values given above are true heatsink thermal resistances to ambient and already account for $R_{th(c:hs)}$ module contact thermal).

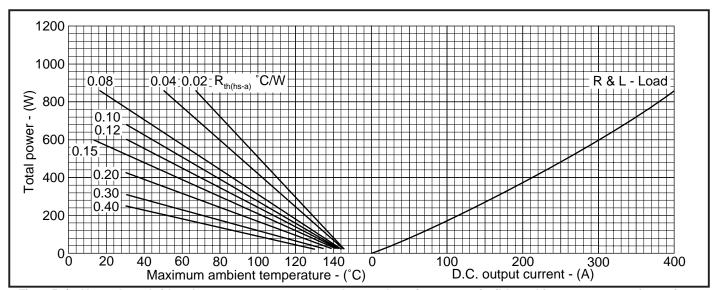
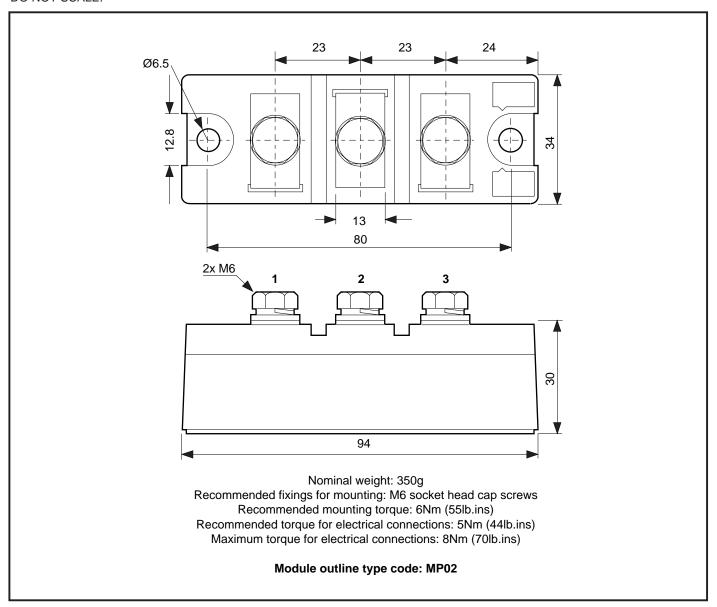


Fig. 7 50/60Hz 3- phase bridge dc output current vs power loss and maximum permissible ambient temperature for various values of heatsink thermal resistance.

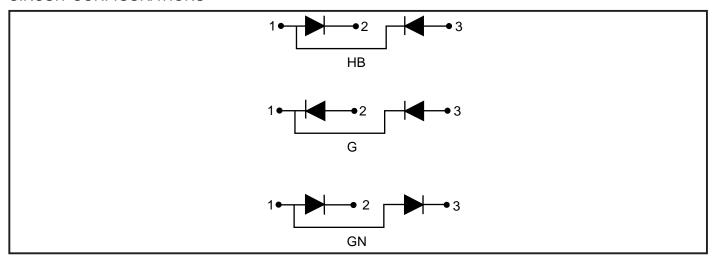
(Note: $R_{th(hs-a)}$ values given above are true heatsink thermal resistances to ambient and already account for $R_{th(c-hs)}$ module contact thermal).

PACKAGE DETAILS

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



CIRCUIT CONFIGURATIONS





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