

DIM400DCM17-A000

IGBT Chopper Module

Replaces September 2001, version DS5490-1.1

DS5490-2.0 March 2002

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Non Punch Through Silicon
- Isolated MMC Base with AIN Substrates

APPLICATIONS

- Power Supplies
- Motor Controllers
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual and single switch configurations covering voltages from 600V to 3300V and currents up to 2400A.

The DIM400DCM17-A000 is a 1700V, n channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) ensuring reliability in demanding applications. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM400DCM17-A000

Note: When ordering, please use the whole part number.

KEY PARAMETERS

| V _{CES} | | 1700\ |
|------------------------|-------|-------|
| V _{CE(sat)} * | (typ) | 2.7V |
| I _C | (max) | 400A |
| I _{C(BK)} | (max) | 800A |

*(measured at the power busbars and not the auxiliary terminals)

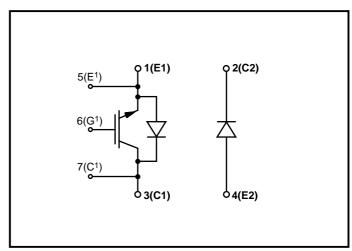


Fig. 1 Chopper circuit diagram

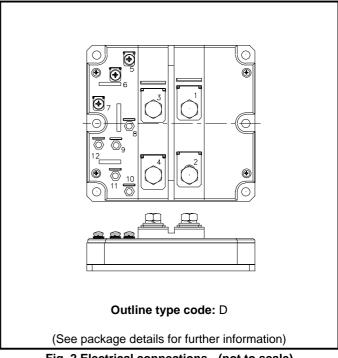


Fig. 2 Electrical connections - (not to scale)



ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

| Symbol | Parameter | Test Conditions | Max. | Units |
|--------------------|--|---|------|-------------------|
| V _{CES} | Collector-emitter voltage | $V_{GE} = 0V$ | 1700 | V |
| V _{GES} | Gate-emitter voltage | - | ±20 | V |
| I _c | Continuous collector current | T _{case} = 75°C | 400 | А |
| I _{C(PK)} | Peak collector current | 1ms, T _{case} = 105°C | 800 | А |
| P _{max} | Max. transistor power dissipation | $T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$ | 3470 | W |
| l²t | Diode I ² t value (IGBT arm) | $V_R = 0, t_p = 10 \text{ms}, T_{vj} = 125^{\circ}\text{C}$ | 30 | kA ² s |
| | Diode I ² t value (Diode arm) | | 120 | kA ² s |
| V _{isol} | Isolation voltage - per module | Commoned terminals to base plate. AC RMS, 1 min, 50Hz | 4000 | V |
| Q_{PD} | Partial discharge - per module | IEC1287. V ₁ = 1500V, V ₂ = 1100V, 50Hz RMS | 10 | рC |



THERMAL AND MECHANICAL RATINGS

Internal insulation material: AIN
Baseplate material: AISiC
Creepage distance: 20mm
Clearance: 10mm
CTI (Critical Tracking Index): 175

| Symbol | Parameter | Test Conditions | Min. | Тур. | Max. | Units |
|----------------------|---|-----------------------------|------|------|------|-------|
| R _{th(j-c)} | Thermal resistance - transistor (per arm) | Continuous dissipation - | - | - | 36 | °C/kW |
| | | junction to case | | | | |
| R _{th(j-c)} | Thermal resistance - diode (IGBT arm) | Continuous dissipation - | - | - | 80 | °C/kW |
| | Thermal resistance - diode (Diode arm) | junction to case | - | - | 40 | °C/kW |
| R _{th(c-h)} | Thermal resistance - case to heatsink | Mounting torque 5Nm | - | - | 8 | °C/kW |
| | (per module) | (with mounting grease) | | | | |
| T _j | Junction temperature | Transistor | - | - | 150 | °C |
| | | Diode | - | - | 125 | °C |
| T _{stg} | Storage temperature range | - | -40 | - | 125 | °C |
| - | Screw torque | Mounting - M6 | - | - | 5 | Nm |
| | | Electrical connections - M4 | - | - | 2 | Nm |
| | | Electrical connections - M8 | - | - | 10 | Nm |



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise.

| Symbol | Parameter | Test Conditions | Min. | Тур. | Max. | Units |
|------------------------|--|---|------|------|------|-------|
| I _{CES} | Collector cut-off current | $V_{GE} = 0V, V_{CE} = V_{CES}$ | - | - | 1 | mA |
| | | $V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}C$ | - | - | 12 | mA |
| I _{GES} | Gate leakage current | $V_{GE} = \pm 20V, V_{CE} = 0V$ | - | - | 2 | μΑ |
| $V_{\text{GE(TH)}}$ | Gate threshold voltage | $I_{\rm C}$ = 20mA, $V_{\rm GE}$ = $V_{\rm CE}$ | 4.5 | 5.5 | 6.5 | V |
| V _{CE(sat)} † | Collector-emitter saturation voltage | V _{GE} = 15V, I _C = 400A | - | 2.7 | 3.2 | V |
| | | $V_{GE} = 15V, I_{C} = 400A, , T_{case} = 125^{\circ}C$ | - | 3.4 | 4.0 | V |
| I _F | Diode forward current | DC | - | - | 400 | А |
| I _{FM} | Diode maximum forward current | t _p = 1ms | - | - | 800 | А |
| V _F † | Diode forward voltage (IGBT arm) | I _F = 400A | - | 2.2 | 2.5 | V |
| | Diode forward voltage (Diode arm) | | - | 1.8 | 2.1 | V |
| | Diode forward voltage (IGBT arm) | I _F = 400A, T _{case} = 125°C | - | 2.3 | 2.6 | V |
| | Diode forward voltage (Diode arm) | | - | 1.8 | 2.1 | V |
| C _{ies} | Input capacitance | V _{CE} = 25V, V _{GE} = 0V, f = 1MHz | - | 30 | - | nF |
| C _{res} | Reverse transfer capacitance | $V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$ | - | 2.5 | - | nF |
| L _M | Module inductance - per arm | - | - | 20 | - | nH |
| R _{INT} | Internal transistor resistance - per arm | - | - | 0.27 | - | mΩ |
| SC _{Data} | Short circuit. I _{sc} | $T_j = 125^{\circ}C, V_{cc} = 1000V,$ I_1 | - | 1850 | - | А |
| | | $t_p \le 10\mu s$, $V_{CE(max)} = V_{CES} - L^*$. di/dt I_2 | - | 1600 | - | А |
| | | IEC 60747-9 | | | | |

Note:

 $^{^{\}scriptscriptstyle \dagger}$ Measured at the power busbars and not the auxiliary terminals)

 $^{^{\}star}$ L is the circuit inductance + L $_{\rm M}$



ELECTRICAL CHARACTERISTICS

 $T_{case} = 25$ °C unless stated otherwise

| Symbol | Parameter | | Test Conditions | Min. | Тур. | Max. | Units |
|---------------------|--------------------------------|-----------|--------------------------------------|------|------|------|-------|
| t _{d(off)} | Turn-off delay time | | I _c = 400A | - | 1150 | - | ns |
| t _f | Fall time | | $V_{GE} = \pm 15V$ | - | 100 | - | ns |
| E _{OFF} | Turn-off energy loss | | V _{CE} = 900V | - | 120 | - | mJ |
| t _{d(on)} | Turn-on delay time | | $R_{G(ON)} = R_{G(OFF)} = 4.7\Omega$ | - | 250 | - | ns |
| t _r | Rise time | | L ~ 100nH | - | 250 | - | ns |
| E _{on} | Turn-on energy loss | | | - | 150 | - | mJ |
| Q_g | Gate charge | | | - | 4.5 | - | μС |
| Q _{rr} | Diode reverse recovery charge | Diode arm | I _F = 400A, | - | 250 | - | μС |
| | | IGBT arm | $V_R = 50\% V_{CES}$ | - | 100 | - | μС |
| I _{rr} | Diode reverse recovery current | Diode arm | dl _F /dt = 2000A/μs | - | 530 | - | Α |
| | | IGBT arm | | - | 230 | - | Α |
| E _{rec} | Diode reverse recovery energy | Diode arm | | - | 160 | - | mJ |
| | | IGBT arm | | - | 70 | - | mJ |



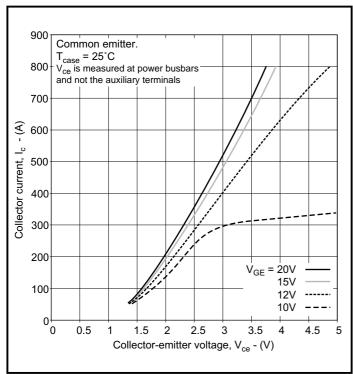
ELECTRICAL CHARACTERISTICS

 $T_{case} = 125$ °C unless stated otherwise

| Symbol | Parameter | | Test Conditions | Min. | Тур. | Max. | Units |
|---------------------|--------------------------------|-----------|--|------|------|------|-------|
| t _{d(off)} | Turn-off delay time | | I _C = 400A | - | 1400 | - | ns |
| t _f | Fall time | | $V_{GE} = \pm 15V$ | - | 130 | - | ns |
| E _{OFF} | Turn-off energy loss | | V _{CE} = 900V | - | 180 | - | mJ |
| t _{d(on)} | Turn-on delay time | | $R_{\text{G(ON)}} = R_{\text{G(OFF)}} = 4.7\Omega$ | - | 400 | - | ns |
| t _r | Rise time | | L ~ 100nH | - | 250 | - | ns |
| E _{on} | Turn-on energy loss | | | - | 170 | - | mJ |
| Q _{rr} | Diode reverse recovery charge | Diode arm | I _F = 400A, | - | 425 | - | μС |
| | | IGBT arm | $V_R = 50\% V_{CES}$ | - | 170 | - | μС |
| I _{rr} | Diode reverse recovery current | Diode arm | $dI_F/dt = 2000A/\mu s$ | - | 600 | - | А |
| | IGBT arm | | | - | 270 | - | А |
| E _{rec} | Diode reverse recovery energy | Diode arm | | - | 250 | - | mJ |
| | | IGBT arm | | - | 100 | - | mJ |



TYPICAL CHARACTERISTICS



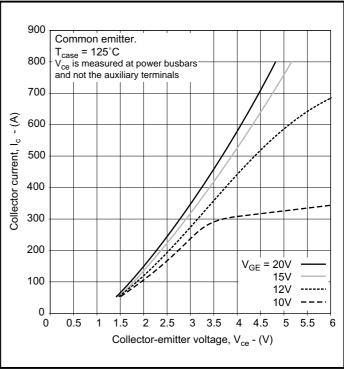
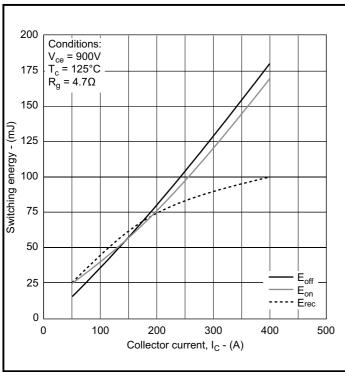
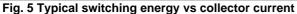


Fig. 3 Typical output characteristics

Fig. 4 Typical output characteristics





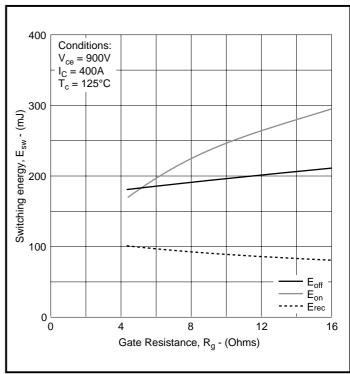
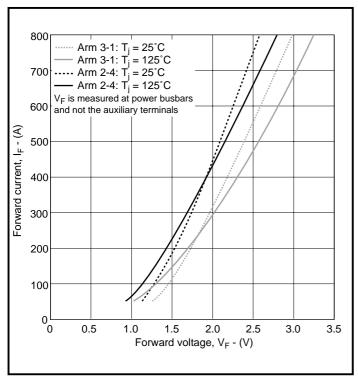


Fig. 6 Typical switching energy vs gate resistance





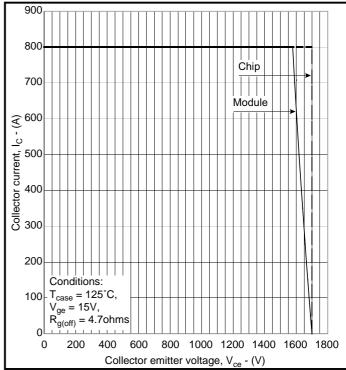


Fig. 7 Diode typical forward characteristics

Fig. 8 Reverse bias safe operating area

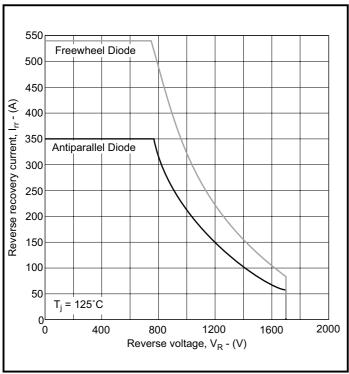


Fig. 9 Diode reverse bias safe operating area

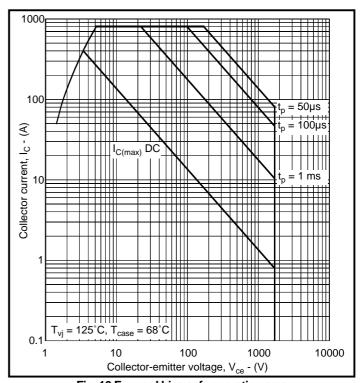
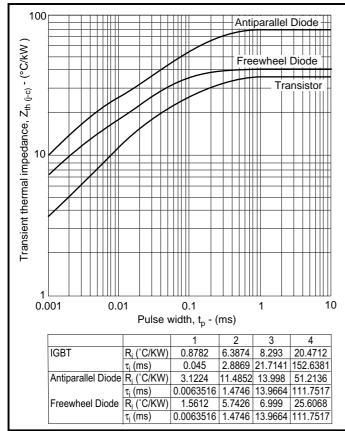


Fig. 10 Forward bias safe operating area





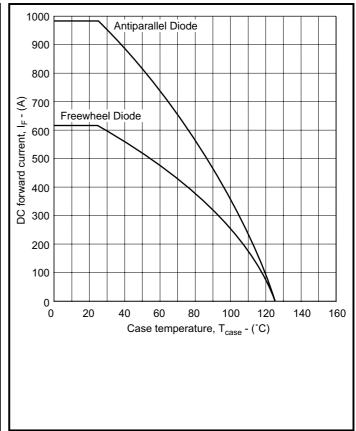


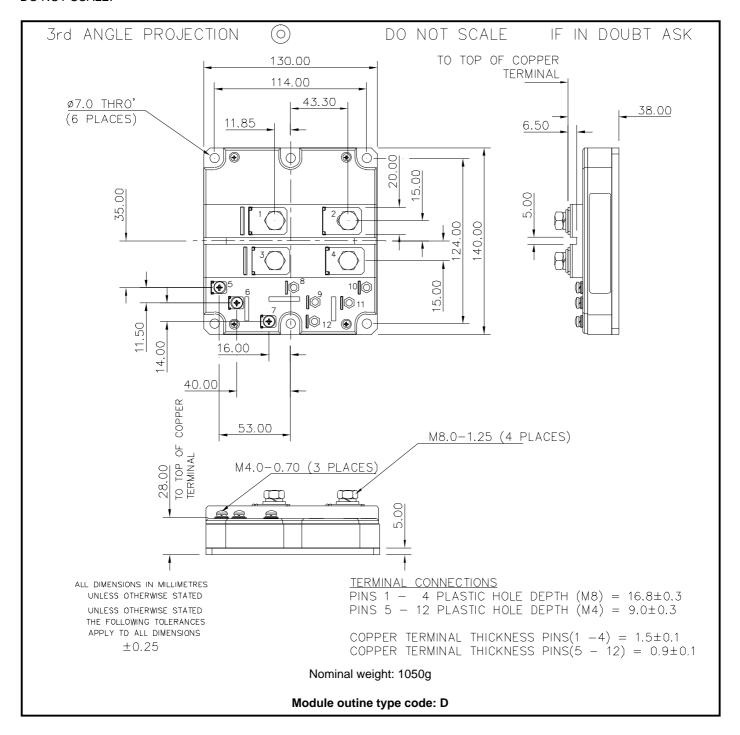
Fig. 11 Transient thermal impedance

Fig. 12 DC current rating vs case temperature



PACKAGE DETAILS

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