

April 1988 Revised August 1999

## 74F27

## **Triple 3-Input NOR Gate**

#### **General Description**

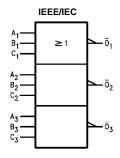
This device contains three independent gates, each of which performs the logic NOR function.

## **Ordering Code:**

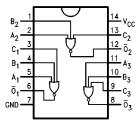
Order Number	Package Number	Package Description				
74F27SC	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow				
74F27SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide				
74F27PC	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide				

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code

### **Logic Symbol**



#### **Connection Diagram**



#### **Unit Loading/Fan Out**

Pin Names	Description	U.L.	Input I <sub>IH</sub> /I <sub>IL</sub>
		HIGH/LOW	Output I <sub>OH</sub> /I <sub>OL</sub>
A <sub>n</sub> , B <sub>n</sub> , C <sub>n</sub>	Data Inputs	1.0/1.0	20 μA/-0.6 mA
$\overline{O}_n$	Data Outputs	50/33.3	-1 mA/20 mA

#### **Function Table**

	Output		
A <sub>n</sub>	B <sub>n</sub>	C <sub>n</sub>	O <sub>n</sub>
L	L	L	Н
Х	Х	Н	L
Х	Н	Χ	L
Н	Х	Χ	L

- H = HIGH Voltage Level L = LOW Voltage Level X = Immaterial

#### Absolute Maximum Ratings(Note 1)

 $\begin{array}{lll} \mbox{Storage Temperature} & -65^{\circ}\mbox{C to } +150^{\circ}\mbox{C} \\ \mbox{Ambient Temperature under Bias} & -55^{\circ}\mbox{C to } +125^{\circ}\mbox{C} \\ \mbox{Junction Temperature under Bias} & -55^{\circ}\mbox{C to } +150^{\circ}\mbox{C} \\ \end{array}$ 

 $V_{CC}$  Pin Potential to Ground Pin -0.5V to +7.0V Input Voltage (Note 2) -0.5V to +7.0V Input Current (Note 2) -30 mA to +5.0 mA

Input Current (Note 2)
Voltage Applied to Output

in HIGH State (with  $V_{CC} = 0V$ )

Current Applied to Output

in LOW State (Max) twice the rated  $I_{OL}$  (mA)

# Recommended Operating Conditions

Free Air Ambient Temperature 0°C to +70°C Supply Voltage +4.5V to +5.5V

**Note 1:** Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

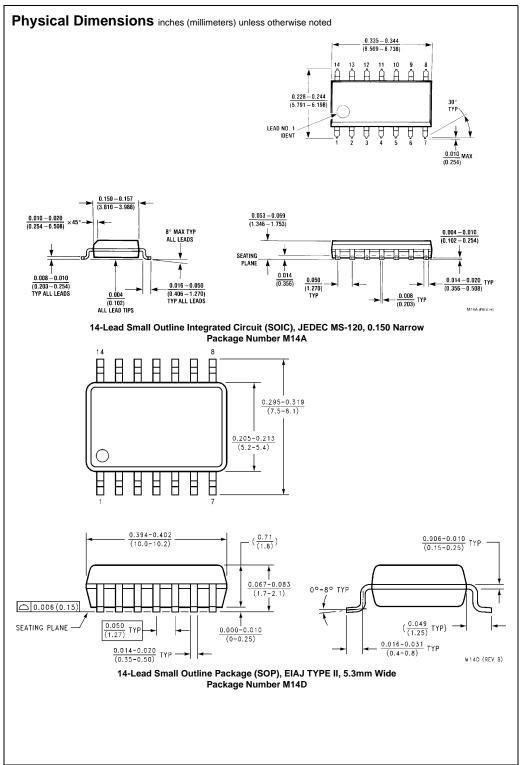
Note 2: Either voltage limit or current limit is sufficient to protect inputs.

#### **DC Electrical Characteristics**

Symbol	Parameter		Parameter Min		Тур	Max	Units	v <sub>cc</sub>	Conditions	
V <sub>IH</sub>	Input HIGH Voltage		2.0			V		Recognized as a HIGH Signal		
V <sub>IL</sub>	Input LOW Voltage				0.8	V		Recognized as a LOW Signal		
V <sub>CD</sub>	Input Clamp Diode Voltage				-1.2	V	Min	$I_{IN} = -18 \text{ mA}$		
V <sub>OH</sub>	Output HIGH 10% V <sub>CC</sub>		2.5			V	Min	I <sub>OH</sub> = -1 mA		
	Voltage	5% V <sub>CC</sub>	2.7					$I_{OH} = -1 \text{ mA}$		
V <sub>OL</sub>	Output LOW	10% V <sub>CC</sub>			0.5	V	Min	I <sub>OL</sub> = 20 mA		
	Voltage									
I <sub>IH</sub>	Input HIGH Current				5.0	μΑ	Max	V <sub>IN</sub> = 2.7V		
I <sub>BVI</sub>	Input HIGH Current				7.0	μΑ	Max	V <sub>IN</sub> = 7.0V		
	Breakdown Test									
I <sub>CEX</sub>	Output HIGH				50	μΑ	Max	$V_{OUT} = V_{CC}$		
	Leakage Current									
V <sub>ID</sub>	Input Leakage		4.75			V	0.0	I <sub>ID</sub> = 1.9 μA		
	Test							All Other Pins Grounded		
I <sub>OD</sub>	Output Leakage				3.75	μΑ	0.0	V <sub>IOD</sub> = 150 mV		
	Circuit Current							All Other Pins Grounded		
I <sub>IL</sub>	Input LOW Current				-0.6	mA	Max	V <sub>IN</sub> = 0.5V		
I <sub>OS</sub>	Output Short-Circuit Cu	irrent	-60		-150	mA	Max	V <sub>OUT</sub> = 0V		
I <sub>CCH</sub>	Power Supply Current			4.0	5.5	mA	Max	V <sub>O</sub> = HIGH		
I <sub>CCL</sub>	Power Supply Current			8.7	12.0	mA	Max	$V_O = LOW$		

#### **AC Electrical Characteristics**

	Parameter		T <sub>A</sub> = +25°C		$T_A = 0$ °C to +70°C $V_{CC} = +5.0V$ $C_L = 50 \text{ pF}$		Units
Symbol			$\text{V}_{\text{CC}} = +5.0\text{V}$				
Syllibol			$C_L = 50 \ pF$				
		Min	Тур	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay	2.0	3.8	6.0	1.5	6.5	ns



#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued) $\frac{0.740 - 0.770}{(18.80 - 19.56)}$ 0.090 (2.286) 14 13 12 11 10 9 14 13 12 $0.250 \pm 0.010$ (6.350 ± 0.254) PIN NO. 1 1 2 3 4 5 6 7 1 2 3 $\frac{0.092}{(2.337)}$ DIA $\frac{0.030}{(0.762)}$ MAX OPTION 1 OPTION 02 0.135 ± 0.005 $\frac{0.300 - 0.320}{(7.620 - 8.128)}$ $(3.429 \pm 0.127)$ 0.065 (1.651) (3.683 - 5.080) $\frac{0.008 - 0.016}{(0.203 - 0.406)} \text{ TYP}$ 95°±5 0.020 $\frac{0.125 - 0.150}{(3.175 - 3.810)}$ 0.075 ±0.015 (1.905 ±0.381) 0.280 (7.112)-MIN $\frac{0.014 - 0.023}{(0.356 - 0.584)}$ TYP $\frac{0.100 \pm 0.010}{(2.540 \pm 0.254)} \text{ TYP}$ $0.325 ^{\,+\,0.040}_{\,-\,0.015}$ $8.255 + 1.016 \\ -0.381$

14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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N14A (REV F)