

## 74VCX162245

# Low Voltage 16-Bit Bidirectional Transceiver with 3.6V Tolerant Inputs and Outputs and 26Ω Series Resistors in A Port Outputs

### General Description

The VCX162245 contains sixteen non-inverting bidirectional buffers with 3-STATE outputs and is intended for bus oriented applications. The device is byte controlled. Each byte has separate 3-STATE control inputs which can be shorted together for full 16-bit operation. The  $\overline{T/R}$  inputs determine the direction of data flow through the device. The  $\overline{OE}$  inputs disable both the A and B ports by placing them in a high impedance state.

The 74VCX162245 is designed for low voltage (1.65V to 3.6V)  $V_{CC}$  applications with I/O compatibility up to 3.6V. The 74VCX162245 is also designed with 26Ω series resistance in the A Port outputs. This design reduces line noise in applications such as memory address drivers, clock drivers, and bus transceivers/transmitters.

The 74VCX162245 is fabricated with an advanced CMOS technology to achieve high speed operation while maintaining low CMOS power dissipation.

### Features

- 1.65V–3.6V  $V_{CC}$  supply operation
- 3.6V tolerant inputs and outputs
- 26Ω series resistors in A port outputs
- $t_{PD}$  (B to A)
  - 3.4 ns max for 3.0V to 3.6V  $V_{CC}$
  - 4.3 ns max for 2.3V to 2.7V  $V_{CC}$
  - 8.6 ns max for 1.65V to 1.95V  $V_{CC}$
- Power-down high impedance inputs and outputs
- Supports live insertion/withdrawal (Note 1)
- Static Drive ( $I_{OH}/I_{OL}$  A outputs)
  - ±12 mA @ 3.0V  $V_{CC}$
  - ±8 mA @ 2.3V  $V_{CC}$
  - ±3 mA @ 1.65V  $V_{CC}$
- Uses patented noise/EMI reduction circuitry
- Latchup performance exceeds 300 mA
- ESD performance:
  - Human body model > 2000V
  - Machine model >200V

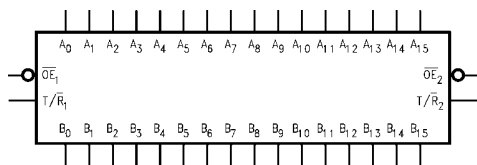
**Note 1:** To ensure the high-impedance state during power up or power down,  $\overline{OE}$  should be tied to  $V_{CC}$  through a pull-up resistor; the minimum value of the resistor is determined by the current-sourcing capability of the driver.

### Ordering Code:

Order Number	Package Number	Package Description
74VCX162245MTD	MTD48	48-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 6.1mm Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

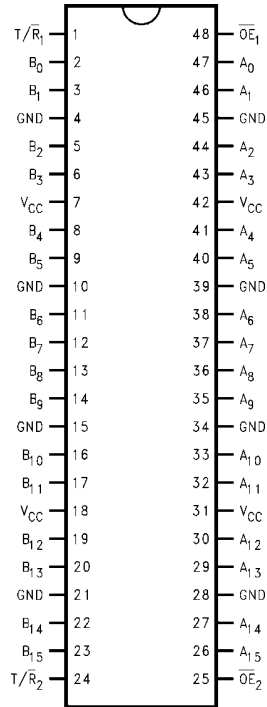
### Logic Symbol



### Pin Descriptions

Pin Names	Description
$\overline{OE}_n$	Output Enable Input
$\overline{T/R}_n$	Transmit/Receive Input
A <sub>0</sub> –A <sub>15</sub>	Side A Inputs or 3-STATE Outputs
B <sub>0</sub> –B <sub>15</sub>	Side B Inputs or 3-STATE Outputs

### Connection Diagram



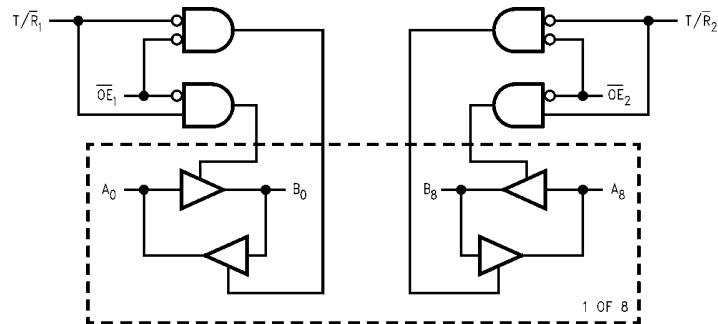
### Truth Tables

Inputs		Outputs
$\overline{OE}_1$	$T/R_1$	
L	L	Bus B <sub>0</sub> -B <sub>7</sub> Data to Bus A <sub>0</sub> -A <sub>7</sub>
L	H	Bus A <sub>0</sub> -A <sub>7</sub> Data to Bus B <sub>0</sub> -B <sub>7</sub>
H	X	HIGH Z State on A <sub>0</sub> -A <sub>7</sub> , B <sub>0</sub> -B <sub>7</sub>

Inputs		Outputs
$\overline{OE}_2$	$T/R_2$	
L	L	Bus B <sub>8</sub> -B <sub>15</sub> Data to Bus A <sub>8</sub> -A <sub>15</sub>
L	H	Bus A <sub>8</sub> -A <sub>15</sub> Data to Bus B <sub>8</sub> -B <sub>15</sub>
H	X	HIGH Z State on A <sub>8</sub> -A <sub>15</sub> , B <sub>8</sub> -B <sub>15</sub>

H = HIGH Voltage Level  
 L = LOW Voltage Level  
 X = Immaterial (HIGH or LOW, inputs and I/O's may not float)  
 Z = High Impedance

### Logic Diagram



Symbol	Parameter	Conditions	V <sub>CC</sub> (V)	Min	Max	Units
V <sub>IH</sub>	HIGH Level Input Voltage		2.7–3.6	2.0		V
V <sub>IL</sub>	LOW Level Input Voltage		2.7–3.6		0.8	V
V <sub>OH</sub>	HIGH Level Output Voltage A Outputs	I <sub>OH</sub> = –100 μA	2.7–3.6	V <sub>CC</sub> – 0.2		V
		I <sub>OH</sub> = –6 mA	2.7	2.2		
		I <sub>OH</sub> = –8 mA	3.0	2.4		
		I <sub>OH</sub> = –12 mA	3.0	2.2		
V <sub>OH</sub>	HIGH Level Output Voltage B Outputs	I <sub>OH</sub> = –100 μA	2.7–3.6	V <sub>CC</sub> – 0.2		V
		I <sub>OH</sub> = –12 mA	2.7	2.2		
		I <sub>OH</sub> = –18 mA	3.0	2.4		
		I <sub>OH</sub> = –24 mA	3.0	2.2		
V <sub>OL</sub>	LOW Level Output Voltage A Outputs	I <sub>OL</sub> = 100 μA	2.7–3.6		0.2	V
		I <sub>OL</sub> = 6 mA	2.7		0.4	
		I <sub>OL</sub> = 8 mA	3.0		0.55	
		I <sub>OL</sub> = 12 mA	3.0		0.8	
V <sub>OL</sub>	LOW Level Output Voltage B Outputs	I <sub>OL</sub> = 100 μA	2.7–3.6		0.2	V
		I <sub>OL</sub> = 12 mA	2.7		0.4	
		I <sub>OL</sub> = 18 mA	3.0		0.4	
		I <sub>OL</sub> = 24 mA	3.0		0.55	
I <sub>I</sub>	Input Leakage Current	0V ≤ V <sub>I</sub> ≤ 3.6V	2.7–3.6		±5.0	μA
I <sub>OZ</sub>	3-STATE Output Leakage	0V ≤ V <sub>O</sub> ≤ 3.6V V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>	2.7–3.6		±10	μA
I <sub>OFF</sub>	Power Off Leakage Current	0V ≤ (V <sub>I</sub> , V <sub>O</sub> ) ≤ 3.6V	0		10	μA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>I</sub> = V <sub>CC</sub> or GND	2.7–3.6		20	μA
		V <sub>CC</sub> ≤ (V <sub>I</sub> , V <sub>O</sub> ) ≤ 3.6V (Note 5)	2.7–3.6		±20	
ΔI <sub>CC</sub>	Increase in I <sub>CC</sub> per Input	V <sub>IH</sub> = V <sub>CC</sub> – 0.6V	2.7–3.6		750	μA

**Note 5:** Outputs disabled or 3-STATE only.

Absolute Maximum Ratings (Note 2)	
Supply Voltage (V <sub>CC</sub> )	–0.5V to +4.6V
DC Input Voltage (V <sub>I</sub> )	–0.5V to +4.6V
Output Voltage (V <sub>O</sub> )	
Outputs 3-State	–0.5V to +4.6V
Outputs Active (Note 3)	–0.5 to V <sub>CC</sub> + 0.5V
DC Input Diode Current (I <sub>IK</sub> ) V <sub>I</sub> < 0V	–50 mA
DC Output Diode Current (I <sub>OK</sub> )	
V <sub>O</sub> < 0V	–50 mA
V <sub>O</sub> > V <sub>CC</sub>	+50 mA
DC Output Source/Sink Current (I <sub>OH</sub> /I <sub>OL</sub> )	±50 mA
DC V <sub>CC</sub> or Ground Current per Supply Pin (I <sub>CC</sub> or Ground)	±100 mA
Storage Temperature Range (T <sub>STG</sub> )	–65°C to +150°C

### Recommended Operating Conditions (Note 4)

Power Supply	
Operating	1.65V to 3.6V
Data Retention Only	1.2V to 3.6V

Input Voltage	–0.3V to 3.6V
Output Voltage (V <sub>O</sub> )	
Output in Active States	0V to V <sub>CC</sub>
Output in 3-STATE	0.0V to 3.6V
Output Current in I <sub>OH</sub> /I <sub>OL</sub> -A Outputs	
V <sub>CC</sub> = 3.0V to 3.6V	±12 mA
V <sub>CC</sub> = 2.3V to 2.7V	±8 mA
V <sub>CC</sub> = 1.65V to 1.95V	±3 mA
Output Current in ± I <sub>OH</sub> /I <sub>OL</sub> -B Outputs	
V <sub>CC</sub> = 3.0V to 3.6V	±24 mA
V <sub>CC</sub> = 2.3V to 2.7V	±18 mA
V <sub>CC</sub> = 1.65V to 2.3V	±6 mA
Free Air Operating Temperature (T <sub>A</sub> )	–40°C to +85°C
Minimum Input Edge Rate (Δt/ΔV)	
V <sub>IN</sub> = 0.8V to 2.0V, V <sub>CC</sub> = 3.0V	10 ns/V

**Note 2:** The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the Absolute Maximum Ratings. The Recommended Operating Conditions tables will define the conditions for actual device operation.

**Note 3:** I<sub>O</sub> Absolute Maximum Rating must be observed.

**Note 4:** Floating or unused pins (inputs or I/O's) must be held HIGH or LOW.

### DC Electrical Characteristics (2.7V < V<sub>CC</sub> ≤ 3.6V)

<b>DC Electrical Characteristics (<math>2.3V \leq V_{CC} \leq 2.7V</math>)</b>						
Symbol	Parameter	Conditions	$V_{CC}$ (V)	Min	Max	Units
$V_{IH}$	HIGH Level Input Voltage		2.3–2.7	1.6		V
$V_{IL}$	LOW Level Input Voltage		2.3–2.7		0.7	V
$V_{OH}$	HIGH Level Output Voltage A Outputs	$I_{OH} = -100 \mu A$ $I_{OH} = -4 \text{ mA}$ $I_{OH} = -6 \text{ mA}$ $I_{OH} = -8 \text{ mA}$	2.3–2.7 2.3 2.3 2.3	$V_{CC} - 0.2$ 2.0 1.8 1.7		V
	HIGH Level Output Voltage B Outputs	$I_{OH} = -100 \mu A$ $I_{OH} = -6 \text{ mA}$ $I_{OH} = -12 \text{ mA}$ $I_{OH} = -18 \text{ mA}$	2.3–2.7 2.3 2.3 2.3	$V_{CC} - 0.2$ 2.0 1.8 1.7		V
$V_{OL}$	LOW Level Output Voltage A Outputs	$I_{OL} = 100 \mu A$ $I_{OL} = 6 \text{ mA}$ $I_{OL} = 8 \text{ mA}$	2.3–2.7 2.3 2.3		0.2 0.4 0.6	V
	LOW Level Output Voltage B Outputs	$I_{OL} = 100 \mu A$ $I_{OL} = 12 \text{ mA}$ $I_{OL} = 18 \text{ mA}$	2.3–2.7 2.3 2.3		0.2 0.4 0.6	V
$I_I$	Input Leakage Current	$0 \leq V_I \leq 3.6V$	2.3–2.7		$\pm 5.0$	$\mu A$
$I_{OZ}$	3-STATE Output Leakage	$0 \leq V_O \leq 3.6V$ $V_I = V_{IH}$ or $V_{IL}$	2.3–2.7		$\pm 10$	$\mu A$
$I_{OFF}$	Power Off Leakage Current	$0 \leq (V_I, V_O) \leq 3.6V$	0		10	$\mu A$
$I_{CC}$	Quiescent Supply Current	$V_I = V_{CC}$ or GND $V_{CC} \leq (V_I, V_O) \leq 3.6V$ (Note 6)	2.3–2.7 2.3–2.7		20 $\pm 20$	$\mu A$
<b>Note 6:</b> Outputs disabled or 3-STATE only.						
<b>DC Electrical Characteristics (<math>1.65V \leq V_{CC} &lt; 2.3V</math>)</b>						
Symbol	Parameter	Conditions	$V_{CC}$ (V)	Min	Max	Units
$V_{IH}$	HIGH Level Input Voltage		1.65–2.3	$0.65 \times V_{CC}$		V
$V_{IL}$	LOW Level Input Voltage		1.65–2.3		$0.35 \times V_{CC}$	V
$V_{OH}$	HIGH Level Output Voltage A Outputs	$I_{OH} = -100 \mu A$ $I_{OH} = -3 \text{ mA}$	1.65–2.3 1.65	$V_{CC} - 0.2$ 1.4		V
	HIGH Level Output Voltage B Outputs	$I_{OH} = -100 \mu A$ $I_{OH} = -6 \text{ mA}$	1.65–2.3 1.65	$V_{CC} - 0.2$ 1.25		V
$V_{OL}$	LOW Level Output Voltage A Outputs	$I_{OL} = 100 \mu A$ $I_{OL} = 3 \text{ mA}$	1.65–2.3 1.65		0.2 0.3	V
	LOW Level Output Voltage B Outputs	$I_{OL} = 100 \mu A$ $I_{OL} = 6 \text{ mA}$	1.65–2.3 1.65		0.2 0.3	V
$I_I$	Input Leakage Current	$0 \leq V_I \leq 3.6V$	1.65–2.3		$\pm 5.0$	$\mu A$
$I_{OZ}$	3-STATE Output Leakage	$0 \leq V_O \leq 3.6V$ $V_I = V_{IH}$ or $V_{IL}$	1.65–2.3		$\pm 10$	$\mu A$
$I_{OFF}$	Power Off Leakage Current	$0 \leq (V_I, V_O) \leq 3.6V$	0		10	$\mu A$
$I_{CC}$	Quiescent Supply Current	$V_I = V_{CC}$ or GND $V_{CC} \leq (V_I, V_O) \leq 3.6V$ (Note 7)	1.65–2.3 1.65–2.3		20 $\pm 20$	$\mu A$
<b>Note 7:</b> Outputs disabled or 3-STATE only.						

AC Electrical Characteristics (Note 8)								
Symbol	Parameter	$T_A = -40^\circ\text{C to } +85^\circ\text{C}, C_L = 30\text{ pF}, R_L = 500\Omega$						Units
		$V_{CC} = 3.3V \pm 0.3V$		$V_{CC} = 2.5 \pm 0.2V$		$V_{CC} = 1.8V \pm 0.15$		
		Min	Max	Min	Max	Min	Max	
$t_{PHL}, t_{PLH}$	Prop Delay, A to B	0.8	2.5	1.0	3.0	1.5	6.0	ns
$t_{PHL}, t_{PLH}$	Prop Delay, B to A	0.8	3.4	1.0	4.3	1.5	8.6	ns
$t_{PZL}, t_{PZH}$	Output Enable Time, A to B	0.8	3.8	1.0	4.9	1.5	9.3	ns
$t_{PZL}, t_{PZH}$	Output Enable Time, B to A	0.8	4.2	1.0	5.7	1.5	9.8	ns
$t_{PLZ}, t_{PHZ}$	Output Disable Time, A to B	0.8	3.7	1.0	4.2	1.5	7.6	ns
$t_{PLZ}, t_{PHZ}$	Output Disable Time, B to A	0.8	4.1	1.0	4.8	1.5	8.6	ns
$t_{OSHL}$	Output to Output		0.5		0.5		0.75	ns
$t_{OSLH}$	Skew (Note 9)							

**Note 8:** For  $C_L = 50\text{pF}$ , add approximately 300ps to the AC maximum specification.

**Note 9:** Skew is defined as the absolute value of the difference between the actual propagation delay for any two separate outputs of the same device. The specification applies to any outputs switching in the same direction, either HIGH-to-LOW ( $t_{OSHL}$ ) or LOW-to-HIGH ( $t_{OSLH}$ ).

### Dynamic Switching Characteristics

Symbol	Parameter	Conditions	$V_{CC}$ (V)	$T_A = +25^\circ\text{C}$	Units
				Typical	
$V_{OLP}$	Quiet Output Dynamic Peak $V_{OL}$ , A to B	$C_L = 30\text{ pF}, V_{IH} = V_{CC}, V_{IL} = 0V$	1.8 2.5 3.3	0.25 0.6 0.8	V
$V_{OLP}$	Quiet Output Dynamic Peak $V_{OL}$ , B to A	$C_L = 30\text{ pF}, V_{IH} = V_{CC}, V_{IL} = 0V$	1.8 2.5 3.3	0.15 0.25 0.35	V
$V_{OLV}$	Quiet Output Dynamic Valley $V_{OL}$ , A to B	$C_L = 30\text{ pF}, V_{IH} = V_{CC}, V_{IL} = 0V$	1.8 2.5 3.3	-0.25 -0.6 -0.8	V
$V_{OLV}$	Quiet Output Dynamic Valley $V_{OL}$ , B to A	$C_L = 30\text{ pF}, V_{IH} = V_{CC}, V_{IL} = 0V$	1.8 2.5 3.3	0.15 -0.25 -0.35	V
$V_{OHV}$	Quiet Output Dynamic Valley $V_{OH}$ , A to B	$C_L = 30\text{ pF}, V_{IH} = V_{CC}, V_{IL} = 0V$	1.8 2.5 3.3	1.5 1.9 2.2	V
$V_{OHV}$	Quiet Output Dynamic Valley $V_{OH}$ , B to A	$C_L = 30\text{ pF}, V_{IH} = V_{CC}, V_{IL} = 0V$	1.8 2.5 3.3	1.55 2.05 2.65	V

### Capacitance

Symbol	Parameter	Conditions	$T_A = +25^\circ\text{C}$	Units
$C_{IN}$	Input Capacitance	$V_{CC} = 1.8V, 2.5V, \text{ or } 3.3V, V_I = 0V \text{ or } V_{CC}$	6	pF
$C_{IO}$	Output Capacitance	$V_I = 0V, \text{ or } V_{CC}, V_{CC} = 1.8V, 2.5V \text{ or } 3.3V$	7	pF
$C_{PD}$	Power Dissipation Capacitance	$V_I = 0V \text{ or } V_{CC}, f = 10\text{ MHz}$ $V_{CC} = 1.8V, 2.5V \text{ or } 3.3V$	20	pF

### AC Loading and Waveforms

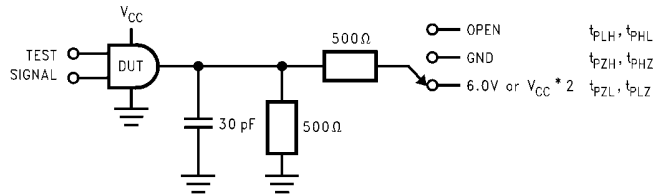


FIGURE 1. AC Test Circuit

TEST	SWITCH
$t_{PLH}, t_{PHL}$	Open
$t_{PZL}, t_{PLZ}$	6V at $V_{CC} = 3.3 \pm 0.3V$ ; $V_{CC} \times 2$ at $V_{CC} = 2.5 \pm 0.2V$ ; $1.8V \pm 0.15V$
$t_{PZH}, t_{PHZ}$	GND

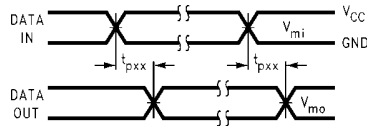


FIGURE 2. Waveform for Inverting and Non-inverting Functions

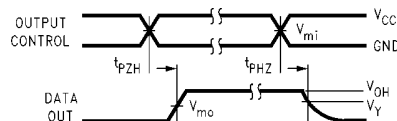


FIGURE 3. 3-STATE Output HIGH Enable and Disable Times for LOW Voltage Logic

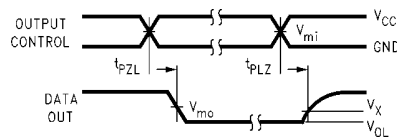


FIGURE 4. 3-STATE Output LOW Enable and Disable Times for LOW Voltage Logic

Symbol	$V_{CC}$		
	$3.3V \pm 0.3V$	$2.5V \pm 0.2V$	$1.8V \pm 0.15V$
$V_{mi}$	1.5V	$V_{CC}/2$	$V_{CC}/2$
$V_{mo}$	1.5V	$V_{CC}/2$	$V_{CC}/2$
$V_X$	$V_{OL} + 0.3V$	$V_{OL} + 0.15V$	$V_{OL} + 0.15V$
$V_Y$	$V_{OH} - 0.3V$	$V_{OH} - 0.15V$	$V_{OH} - 0.15V$

