

September 1986 Revised February 2000

DM74ALS09 Quad 2-Input AND Gate with Open Collector Outputs

General Description

This device contains four independent gates, each of which performs the logic AND function. The open-collector outputs require external pull-up resistors for proper logical operation.

Pull-Up Resistor Equations

$$\mathsf{R}_{\mathsf{MAX}} = \frac{\mathsf{V}_{\mathsf{CC}}\left(\mathsf{Min}\right) - \mathsf{V}_{\mathsf{OH}}}{\mathsf{N}_{\mathsf{1}}\left(\mathsf{I}_{\mathsf{OH}}\right) + \mathsf{N}_{\mathsf{2}}\left(\mathsf{I}_{\mathsf{IH}}\right)}$$

$$R_{MIN} = \frac{V_{CC} \left(Max \right) - V_{OL}}{I_{OL} - N_3 \left(I_{IL} \right)}$$

Where: N_1 (I_{OH}) = total maximum output HIGH current

for all outputs tied to pull-up resistor

 N_2 (I_{IH}) = total maximum input HIGH current

for all inputs tied to pull-up resistor N_3 (I_{IL}) = total maximum input LOW current for

all inputs tied to pull-up resistor

Features

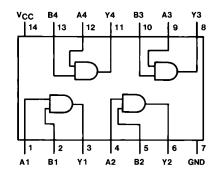
- Switching specifications at 50 pF
- \blacksquare Switching specifications guaranteed over full temperature and V_{CC} range
- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Functionally and pin for pin compatible with Schottky and low power Schottky TTL counterpart
- Improved AC performance over Schottky and low power Schottky counterparts

Ordering Code:

Order Number	Package Number	Package Description
DM74ALS09M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74ALS09N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0,300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

$$\mathbf{Y} = \mathbf{A}\mathbf{B}$$

Inp	uts	Output		
Α	В	Y		
L	L	L		
L	Н	L		
Н	L	L		
Н	Н	Н		

H = HIGH Logic Level L = LOW Logic Level

Absolute Maximum Ratings(Note 1)

Supply Voltage 7V Input Voltage 7V HIGH Level Output Voltage 7V Operating Free Air Temperature Range 0°C to $+70^{\circ}$ C

Storage Temperature Range Typical θ_{JA}

N Package 86.5°C/W M Package 116.0°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.5	5	5.5	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
V _{OH}	HIGH Level Output Voltage			5.5	V
I _{OL}	LOW Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range. All typical values are measured at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

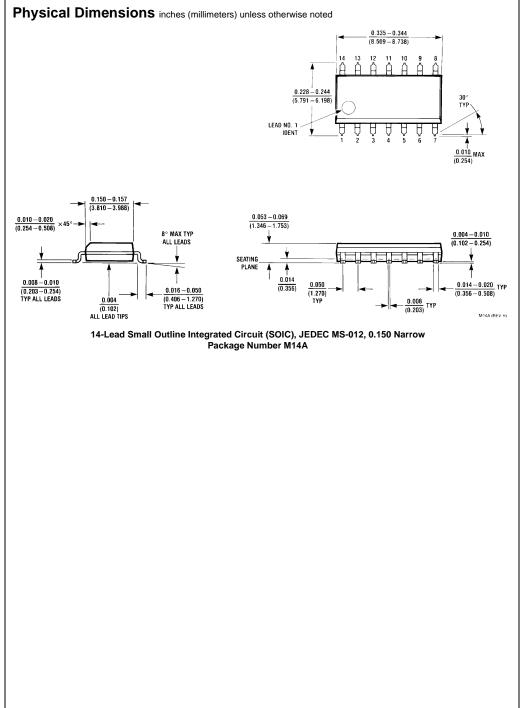
-65°C to +150°C

Symbol	Parameter	Conditions		Min	Тур	Max	Units
V _{IK}	Input Clamp Voltage	$V_{CC} = 4.5V$, $I_I = -18 \text{ mA}$	$V_{CC} = 4.5V$, $I_I = -18 \text{ mA}$			-1.5	V
I _{OH}	HIGH Level Output Current	$V_{CC} = 4.5V, V_{OH} = 5.5V$	V _{CC} = 4.5V, V _{OH} = 5.5V			100	μΑ
V _{OL}	LOW Level	V _{CC} = 4.5V	I _{OL} =4 mA		0.25	0.4	V
	Output Voltage		$I_{OL} = 8 \text{ mA}$		0.35	0.5	V
II	Input Current @ Max	$V_{CC} = 5.5V, V_{IH} = 7V$	V - F FV V - 7V			0.1	mA
	Input Voltage	VCC = 3.3 v, VIH = 7 v				0.1	IIIA
I _{IH}	HIGH Level Input Current	$V_{CC} = 5.5V, V_{IH} = 2.7V$				20	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = 5.5V, V_{IL} = 0.4V$				-0.1	mA
Icc	Supply Current	V _{CC} = 5.5V	Outputs HIGH		1.3	2.4	mA
			Outputs LOW		2.2	4	mA

Switching Characteristics

over recommended operating free air temperature range

Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time	V _{CC} = 4.5V to 5.5V	23	54	ns
	LOW-to-HIGH Level Output	$R_L = 2 k\Omega$, $C_L = 50 pF$	25	34	110
t _{PHL}	Propagation Delay Time		5	15	ns
	HIGH-to-LOW Level Output		3	13	115



Physical Dimensions inches (millimeters) unless otherwise noted (Continued) $\frac{0.740 - 0.770}{(18.80 - 19.56)}$ 0.090 (2.286) 14 13 12 11 10 9 14 13 12 0.250 ± 0.010 (6.350 ± 0.254 PIN NO. 1 1 2 3 4 5 6 7 1 2 3 $\frac{0.092}{(2.337)}$ DIA $\frac{0.030}{(0.762)}$ MAX OPTION 1 OPTION 02 0.135±0.005 $\frac{0.300 - 0.320}{(7.620 - 8.128)}$ (3.429 ± 0.127) 0.065 (1.651) (3.683 - 5.080) $\frac{0.008 - 0.016}{(0.203 - 0.406)} \text{ TYP}$ 0.020 95° ± 5 $\frac{0.125 - 0.150}{(3.175 - 3.810)}$ 0.075 ±0.015 (1.905 ±0.381) 0.280 (7.112)-MIN $\frac{0.014 - 0.023}{(0.356 - 0.584)}$ TYP $\frac{0.100 \pm 0.010}{(2.540 \pm 0.254)} \text{ TYP}$

14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

1.270 ± 0.010 (1.270 − 0.254)

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 $\frac{0.325 + 0.040 \\
 - 0.015}{(8.255 + 1.016) \\
 - 0.381)}$

N14A (REV F)

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