

March 1987 Revised February 2000

# DM74ALS645A **Octal Bus Transceivers**

### **General Description**

These octal bus transceivers are designed for asynchronous two-way communication between data busses. These devices transmit data from the A bus to the B bus or from the B bus to the A bus depending upon the level at the direction control (DIR) input. The enable input (G) can be used to disable the device so the busses are effectively isolated.

#### **Features**

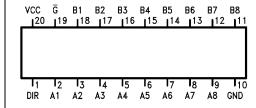
- Advanced Oxide-isolated Ion-implanted Schottky TTL process
- Switching performance is guaranteed over full temperature and  $V_{\mbox{\footnotesize CC}}$  supply range
- Switching performance specified at 50 pF
- PNP input design reduces input loading

## **Ordering Code:**

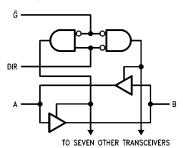
Order Number	Package Number	Package Description				
DM74ALS645AWM	M20B	20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide				
DM74ALS645AN	N20A	20-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide				

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

#### **Connection Diagram**



## **Logic Diagram**



#### **Function Table**

Control Inputs		Operation
G	DIR	
L	L	B Data to A Bus
L	Н	A Data to B Bus
Н	Х	Isolation

L = LOW Logic Level H = HIGH Logic Level

X = Either LOW or HIGH Logic Level

## **Absolute Maximum Ratings**(Note 1)

Supply Voltage 7V

Input Voltage;

 Control Inputs
 7V

 I/O Ports
 5.5V

Operating Free Air Temperature Range  $0^{\circ}\text{C to } +70^{\circ}\text{C}$ Storage Temperature Range  $-65^{\circ}\text{C to } +150^{\circ}\text{C}$ 

Typical  $\theta_{\text{JA}}$ 

N Package 53.0°C/W

M Package 72.0°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

## **Recommended Operating Conditions**

Symbol	Parameter	Min	Тур	Max	Units
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
Гон	HIGH Level Output Current			-15	mA
I <sub>OL</sub>	LOW Level Output Current			24	mA
T <sub>A</sub>	Operating Free Air Temperature Range	0		70	°C

### **Electrical Characteristics**

Over Recommended Free Air Temperature Range

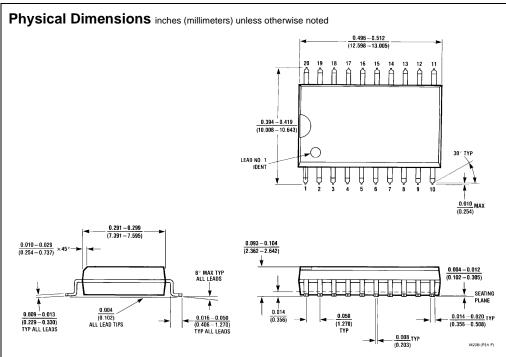
Symbol	Parameter	Test Conditions		Min	Тур	Max	Units	
V <sub>IC</sub>	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>I</sub> = -18 mA				-1.5	V	
V <sub>OH</sub>	HIGH Level Output Voltage	V <sub>CC</sub> = 4.5 to 5.5V	$I_{OH} = -0.4 \text{ mA}$	V <sub>CC</sub> - 2				
		V <sub>CC</sub> = Max	$I_{OH} = -3 \text{ mA}$	2.4	3.2		V	
			I <sub>OH</sub> = Max	2				
V <sub>OL</sub> LOW Level Output V	LOW Level Output Voltage	V <sub>CC</sub> = Min	I <sub>OL</sub> = 12 mA		0.25	0.4	V	
			I <sub>OL</sub> = 24 mA		0.35	0.5	V	
II	Input Current at	Current at V <sub>CC</sub> = Max				100		
	Maximum Input Voltage		Control Inputs, V <sub>I</sub> = 7V			100	μΑ	
I <sub>IH</sub>	HIGH Level Input Current	V <sub>CC</sub> = Max, V <sub>I</sub> = 2.7V (Note			20	μΑ		
I <sub>IL</sub>	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$ (Note			-100	μΑ		
Io	Output Drive Current	$V_{CC} = Max$ , $V_O = 2.25V$		-30		-112	mA	
Icc	Supply Current	V <sub>CC</sub> = Max	Outputs HIGH		30	45		
			Outputs LOW		36	55	mA	
			Outputs Disabled		38	58		

Note 2: For I/O ports, I<sub>IH</sub> and I<sub>IL</sub> parameters include the 3-STATE output current (I<sub>OZL</sub> and I<sub>OZH</sub>).

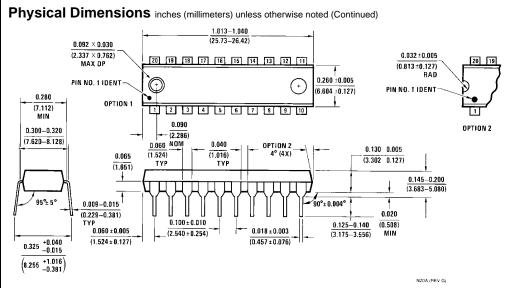
## **Switching Characteristics**

Over Recommended Operating Free Air Temperature Range

Symbol	Parameter	From (Input)	To (Output)	Conditions	Min	Max	Units
t <sub>PLH</sub>	Propagation Delay Time LOW-to-HIGH Level Output	A or B	B or A	$V_{CC} = 4.5 \text{ to } 5.5 \text{V},$ $C_L = 50 \text{ pF},$	3	10	ns
t <sub>PHL</sub>	Propagation Delay Time HIGH-to-LOW Level Output	A or B	B or A	R1 = R2 = 500Ω	3	10	ns
t <sub>PZH</sub>	Output Enable Time to HIGH Level Output	G	A or B		5	20	ns
t <sub>PZL</sub>	Output Enable Time to LOW Level Output	G	A or B		5	20	ns
t <sub>PHZ</sub>	Output Disable Time from HIGH Level Output	G	A or B		2	10	ns
t <sub>PLZ</sub>	Output Disable Time from LOW Level Output	G	A or B		4	15	ns



20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide Package Number M20B



20-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N20A

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