

August 1986 Revised March 2000

DM74LS05

Hex Inverters with Open-Collector Outputs

General Description

This device contains six independent gates each of which performs the logic INVERT function. The open-collector outputs require external pull-up resistors for proper logical operation.

Pull-Up Resistor Equations

$$R_{MAX} = \frac{V_{CC} (Min) - V_{OH}}{N_1 (I_{OH}) + N_2 (I_{IH})}$$

$$\mathsf{R}_{MIN} = \frac{\mathsf{V}_{CC} \left(\mathsf{Max}\right) - \mathsf{V}_{OL}}{\mathsf{I}_{OL} - \mathsf{N}_{3} \left(\mathsf{I}_{IL}\right)}$$

Where: N₁ (I_O

N₁ (I_{OH}) = total maximum output high current

for all outputs tied to pull-up resistor

 N_2 (I_{IH}) = total maximum input high current for all inputs tied to pull-up resistor

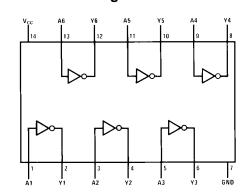
 $N_3 \ (I_{IL}) = total \ maximum \ input \ low \ current \ for \ all inputs tied to pull-up resistor$

Ordering Code:

Order Number	Package Number	Package Description			
DM74LS05M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow			
DM74LS05SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide			
DM74LS05N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide			

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

Y = A						
Input	Output					
Α	Y					
L	Н					
Н	L					

H = HIGH Logic Level L = LOW Logic Level

Absolute Maximum Ratings(Note 1)

 Supply Voltage
 7V

 Input Voltage
 7V

 Output Voltage
 7V

Operating Free Air Temperature Range 0°C to $+70^{\circ}\text{C}$ Storage Temperature Range -65°C to $+150^{\circ}\text{C}$

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
V _{OH}	HIGH Level Output Voltage			5.5	V
I _{OL}	LOW Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

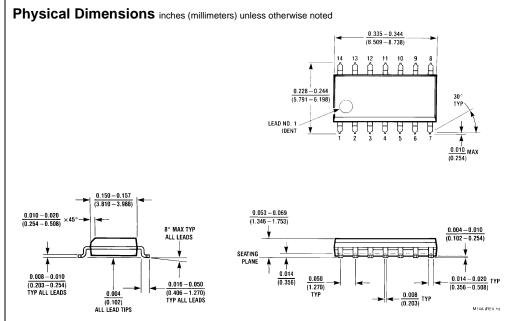
Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	V _{CC} = Min, I _I = -18 mA			-1.5	V
I _{CEX}	HIGH Level Output Current	$V_{CC} = Min, V_O = 5.5V$ $V_{IL} = Max$			100	μА
V _{OL}	LOW Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min$		0.35	0.5	V
		I _{OL} = 4 mA, V _{CC} = Min		0.25	0.4	
I _I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			0.1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.36	mA
I _{CCH}	Supply Current with Outputs HIGH	V _{CC} = Max		1.2	2.4	mA
I _{CCL}	Supply Current with Outputs LOW	V _{CC} = Max		3.6	6.6	mA

Note 2: All typicals are at $V_{CC} = 5V$, $T_A = 25$ °C.

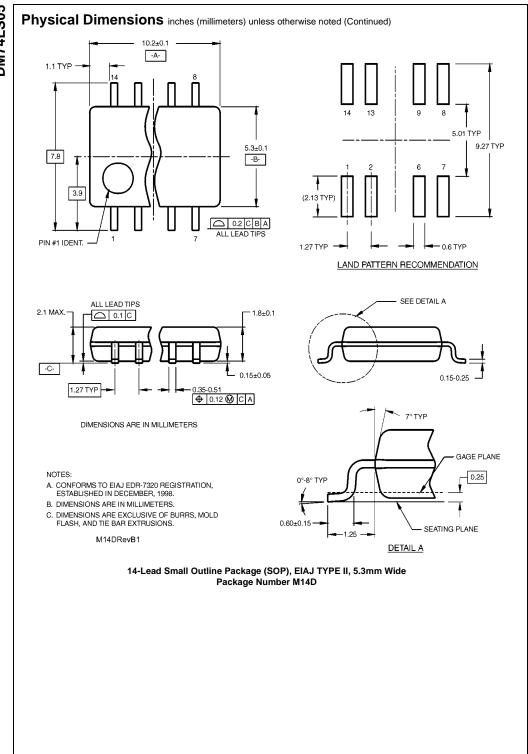
Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$

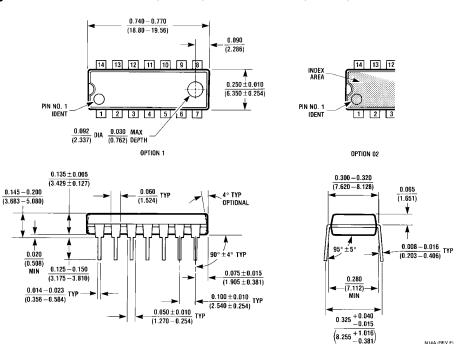
	Parameter					
Symbol		C _L = 15 pF		C _L = 50 pF		Units
		Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time	6	20	20	45	ns
	LOW-to-HIGH Level Output	0	20	20	45	113
t _{PHL}	Propagation Delay Time	3	15	4	20	ns
	HIGH-to-LOW Level Output					



14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow Package Number M14A



Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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