FAIRCHILD

SEMICONDUCTOR

DM74LS377 **Octal D-Type Flip-Flop with Common Enable and Clock**

General Description

Features

- The DM74LS377 is an 8-bit register built using advanced 8-bit high speed parallel registers low power Schottky technology. This register consists of
 - Positive edge-triggered D-type flip-flops
 - Fully buffered common clock and enable inputs

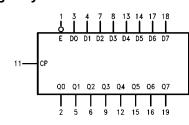
October 1988

Revised March 2000

the space-saving (0.3 inch row spacing) 20-pin package.

Ordering C	Code:	
Order Number	Package Number	Package Description
DM74LS377WM	M20B	20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide
DM74LS377N	N20A	20-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide
Devices also available	in Tape and Reel. Specify	by appending the suffix letter "X" to the ordering code.

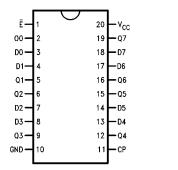
Logic Symbol



eight D-type flip-flops with a buffered common clock and a

buffered common input enable. The device is packaged in

Connection Diagram



DM74LS377 Octal D-Type Flip-Flop with Common Enable and Clock

V_{CC} = Pin 20 GND = Pin 10

Pin Descriptions

Truth Table

Pin Names	Description
E	Enable Input (Active LOW)
D0–D7	Data Inputs
CP	Clock Pulse Input (Active Rising Edge)
Q0–Q7	Flip-Flop Outputs

	Inputs		Output
Ē	СР	D _n	Q _n
Н	Х	Х	No Change
L	~	Н	н
L	~	L	L

L = LOW Voltage Level X = Immaterial

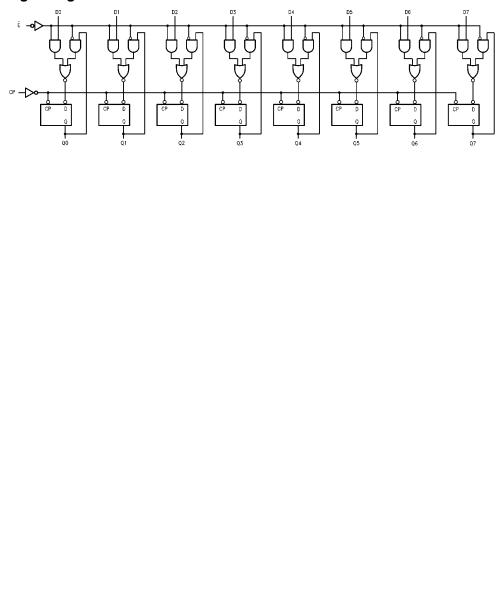
© 2000 Fairchild Semiconductor Corporation DS009831

Functional Description

The DM74LS377 consists of eight edge-triggered D flip-flops with individual D inputs and Q outputs. The Clock (CP) and Enable input (E) are common to all flip-flops.

When \overline{E} is LOW, new data is entered into the register on the next LOW-to-HIGH transition of CP. When \overline{E} is HIGH, the register will retain the present data independent of the CP.

Logic Diagram



Absolute Maximum Ratings(Note 1)

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	-65°C to +150°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

DM74LS377

Recommended Operating Conditions

Supply Voltage						
		4.75	5	5.25	;	V
HIGH Level Input Volta	ge	2				V
LOW Level Input Voltag	ge			0.8		V
HIGH Level Output Cur	rrent			-0.4	ļ.	mA
LOW Level Output Cur	rent			8		mA
Free Air Operating Terr	nperature	0		70		°C
Setup Time HIGH or LO	WC	10				
D _n to CP		10				ns
Hold Time HIGH or LO	W	5.0				
D _n to CP		5.0				ns
Setup Time HIGH or LO	WC	10				
E to CP		20				ns
Hold Time HIGH or LO	W	5.0				
E to CP		5.0				ns
CP Pulse Width HIGH	or LOW	20				
		20				ns
ended operating free air temperati Parameter		herwise noted) Conditions	Min	Typ	Max	Unit
· •			Min	Typ (Note 2)	Max	Units
· •	V _{CC} = Min, I _I :	Conditions	Min		Max -1.5	Unit: V
Parameter nput Clamp Voltage IIGH Level	$V_{CC} = Min, I_1$ $V_{CC} = Min, I_0$	Conditions	Min 2.7			
Parameter nput Clamp Voltage IIGH Level Dutput Voltage	$V_{CC} = Min, I_1 = V_{CC} = Min, I_0$ $V_{IL} = Max$	Conditions = -18 mA _H = Max		(Note 2)		V
Parameter nput Clamp Voltage IIGH Level Dutput Voltage OW Level	$V_{CC} = Min, I_{I}$ $V_{CC} = Min, I_{O}$ $V_{IL} = Max$ $V_{CC} = Min, I_{O}$	Conditions = -18 mA _H = Max		(Note 2)		v v
Parameter nput Clamp Voltage IIGH Level Dutput Voltage	$V_{CC} = Min, I_{I}$ $V_{CC} = Min, I_{O}$ $V_{IL} = Max$ $V_{CC} = Min, I_{O}$ $V_{IH} = Min$	Conditions = -18 mA _H = Max _L = Max		(Note 2) 3.4 0.35	-1.5	V
Parameter nput Clamp Voltage IIGH Level Dutput Voltage OW Level Dutput Voltage	$\label{eq:VCC} \begin{array}{ c c c } \hline V_{CC} = Min, I_l \\ \hline V_{CC} = Min, I_O \\ \hline V_{IL} = Max \\ \hline V_{CC} = Min, I_O \\ \hline V_{IH} = Min \\ \hline \hline I_{OL} = 4 \text{ mA, V} \end{array}$	Conditions = -18 mA _H = Max _L = Max _{CC} = Min		(Note 2)	-1.5	v v
Parameter Parameter IIGH Level Dutput Voltage OW Level Dutput Voltage Dutput Voltage Dutput Voltage Dutput Current @ Max	$V_{CC} = Min, I_1$ $V_{CC} = Min, I_0$ $V_{IL} = Max$ $V_{CC} = Min, I_0$ $V_{IH} = Min$ $I_{0L} = 4 mA, V$ $V_{CC} = Max, V$	Conditions = -18 mA _H = Max _L = Max _{CC} = Min		(Note 2) 3.4 0.35	-1.5	v v
Parameter Parameter IIGH Level Dutput Voltage OW Level Dutput Voltage Dutput Voltage nput Current @ Max nput Voltage	$V_{CC} = Min, I_1$ $V_{CC} = Min, I_0$ $V_{IL} = Max$ $V_{CC} = Min, I_0$ $V_{IH} = Min$ $I_{0L} = 4 mA, V$ $V_{CC} = Max, V$ $V_{II} = 10V$	Conditions = -18 mA $_{\text{H}} = \text{Max}$ $_{\text{L}} = \text{Max}$ $_{\text{CC}} = \text{Min}$ $_{\text{T}} = 7\text{V}$		(Note 2) 3.4 0.35	-1.5 0.5 0.4 0.1	V V V mA
Parameter Parameter IIGH Level Dutput Voltage OW Level Dutput Voltage Dutput Voltage Dutput Voltage Dutput Current @ Max	$V_{CC} = Min, I_1$ $V_{CC} = Min, I_0$ $V_{IL} = Max$ $V_{CC} = Min, I_0$ $V_{IH} = Min$ $I_{0L} = 4 mA, V$ $V_{CC} = Max, V$	Conditions = -18 mA $_{\text{H}} = \text{Max}$ $_{\text{L}} = \text{Max}$ $_{\text{CC}} = \text{Min}$ $_{\text{T}} = 7\text{V}$ $_{\text{T}} = 2.7\text{V}$		(Note 2) 3.4 0.35	-1.5 0.5 0.4	v v v
Parameter Parameter IIGH Level Dutput Voltage OW Level Dutput Voltage Dutput Voltage Input Current @ Max Input Voltage IIGH Level Input Current	$\label{eq:VCC} \begin{array}{ c c c } \hline V_{CC} = Min, I_1 \\ \hline V_{CC} = Min, I_0 \\ \hline V_{IL} = Max \\ \hline V_{CC} = Min, I_0 \\ \hline V_{IH} = Min \\ \hline I_{0L} = 4 mA, V \\ \hline V_{CC} = Max, V \\ \hline V_{I} = 10V \\ \hline V_{CC} = Max, V \end{array}$	Conditions = -18 mA $_{\text{H}} = \text{Max}$ $_{\text{L}} = \text{Max}$ $_{\text{CC}} = \text{Min}$ $_{\text{T}} = 7\text{V}$ $_{\text{T}} = 2.7\text{V}$ $_{\text{T}} = 0.4\text{V}$		(Note 2) 3.4 0.35	-1.5 0.5 0.4 0.1 20.0	V V V mA
-	Setup Time HIGH or LO D_n to CP Hold Time HIGH or LO D_n to CP Setup Time HIGH or LO \overline{E} to CP Hold Time HIGH or LO \overline{E} to CP Hold Time HIGH or LO \overline{E} to CP Hold Time HIGH or LO \overline{E} to CP CP Pulse Width HIGH	Hold Time HIGH or LOW D _n to CP Setup Time HIGH or LOW E to CP Hold Time HIGH or LOW	$\begin{tabular}{ c c c c c } \hline Setup Time HIGH or LOW & 10 \\ \hline D_n to CP & 10 \\ \hline Hold Time HIGH or LOW & 5.0 \\ \hline D_n to CP & 5.0 \\ \hline Setup Time HIGH or LOW & 10 \\ \hline E to CP & 20 \\ \hline Hold Time HIGH or LOW & 5.0 \\ \hline E to CP & 5.0 \\ \hline CP Pulse Width HIGH or LOW & 20 \\ \hline 20 \\ \hline \end{tabular}$	Setup Time HIGH or LOW 10 D _n to CP 10 Hold Time HIGH or LOW 5.0 D _n to CP 5.0 Setup Time HIGH or LOW 10 Ē to CP 20 Hold Time HIGH or LOW 5.0 Ē to CP 20 Hold Time HIGH or LOW 5.0 Ē to CP 5.0 CP Pulse Width HIGH or LOW 20 20 20	Setup Time HIGH or LOW 10 D _n to CP 10 Hold Time HIGH or LOW 5.0 D _n to CP 5.0 Setup Time HIGH or LOW 10 Ē to CP 20 Hold Time HIGH or LOW 5.0 Ē to CP 5.0 CP Pulse Width HIGH or LOW 5.0 ZO 20	Setup Time HIGH or LOW 10 D _n to CP 10 Hold Time HIGH or LOW 5.0 D _n to CP 5.0 Setup Time HIGH or LOW 10 E to CP 20 Hold Time HIGH or LOW 5.0 E to CP 20 Hold Time HIGH or LOW 5.0 E to CP 5.0 CP Pulse Width HIGH or LOW 20 20 20

