

KA723

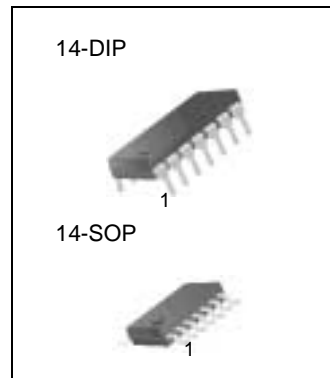
Precision Voltage Regulator

Features

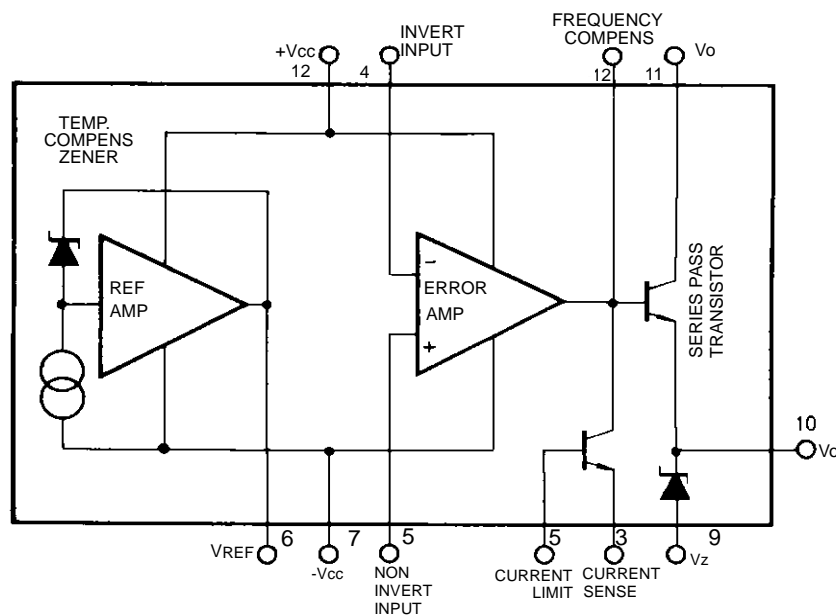
- Positive or Negative Supply Operation
- 0.01% line and load regulation
- Output voltage adjustable from 2V to 37 V
- Output current to 150mA without external pass transistor

Description

The KA723 are monolithic integrated Circuit voltage regulators featuring high ripple rejection, excellent output and load regulation, excellent temperature stability, and low standby current. The KA723 are also useful in a wide range of other applications such as a shunt regulator, a current regulator or a temperature controller.



Internal Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Pulse Voltage from V+ to V- (50ms)	V _{I(P)}	50	V _{PEAK}
Continuous Voltage from V+ to V-	V _I	40	V
Input-Output Voltage Differential	V _I - V _O	40	V
Maximum Output Current	I _O	150	mA
Differential Input Voltage	V _{ID}	±5	V
Voltage Between Non-Inverting Input and V-	V _{IE}	8	V
Current from V _Z	I _Z	25	mA
Current from V _{REF}	I _{REF}	15	mA
Power Dissipation	P _D	1000	mW
Operating Temperature Range	T _{OPR}	0 ~ +70	°C
Storage Temperature Range	T _{STG}	-65 ~ + 150	°C

Electrical Characteristics

(Unless otherwise specified, T_A = 25°C, V_{IN} = V⁺ = V_C = 12V, V⁻ = 0, V_{OUT} = 5V, I_L = 1mA, R_{SC} = 0, C_I = 100pF, C_{REF} = 0 and divider impedance as seen by error amplifier ≤ 10KΩ connected as shown in figure 1)

Parameter	Symbol	Conditions	Min	Typ.	Max.	Unit
Line Regulation	ΔV _O	V _I = 12V to 15V V _I = 12V to 40V	-	0.01 0.1	0.1 0.5	%
		T _{MIN} ≤ T _A ≤ T _{MAX} V _I = 12V to 15V	-	-	0.3	
Load Regulation	ΔV _O	I _O = 1mA to 50mA	-	0.03	0.2	%
		T _{MIN} ≤ T ≤ T _{MAX} I _O = 1 to 50mA	-	-	0.6	
Ripple Rejection	dB	f = 100Hz to 10KHz, C _{REF} = 0	-	74	-	dB
		f = 100Hz to 10KHz, C _{REF} = 5μF	-	86	-	
Average Temperature Coefficient of Output Voltage	ΔV _O /ΔT	T _{MIN} ≤ T ≤ T _{MAX}	-	0.003	0.015	%/°C
Short Circuit Current Limit	I _{SC}	R _{SC} = 10Ω, V _O = 0	-	65	-	mA
Reference Voltage	V _{REF}	-	6.80	7.15	7.50	V
Output Noise Voltage	V _N	f = 100Hz to 10KHz, C _{REF} = 0	-	20	-	μVrms
		f = 100Hz to 10KHz, C _{REF} = 5μF	-	2.5	-	
Long-term Stability	ST	-	-	0.1	-	%/ 1000HR
Standby Current Drain	I _D	I _L = 0, V _I = 30V	-	2.0	4.0	mA
Input Voltage Range	V _I	-	9.5	-	40	V
Output Voltage Range	V _O	-	2.0	-	37	V
Input-Output Voltage Differential	V _D	-	3.0	-	38	V

Notes:

1. Line and load regulation specifications are given for the condition of constant chip temperature.
2. Temperature drifts must be taken into account separately for high dissipation conditions.

Typical Application

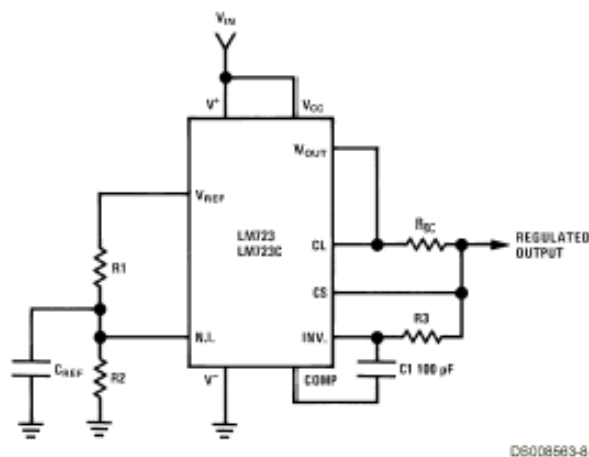


Figure 1. Basic Low Voltage Regulator
(V_{OUT} = 2 to 7Volts)

Note: $R3 = \frac{R1R2}{R1 + R2}$ for minimum temperature drift

Typical Performance

Regulated Output Voltage 5V

Line regulation ($\Delta V_{IN} = 3V$) 0.5mV

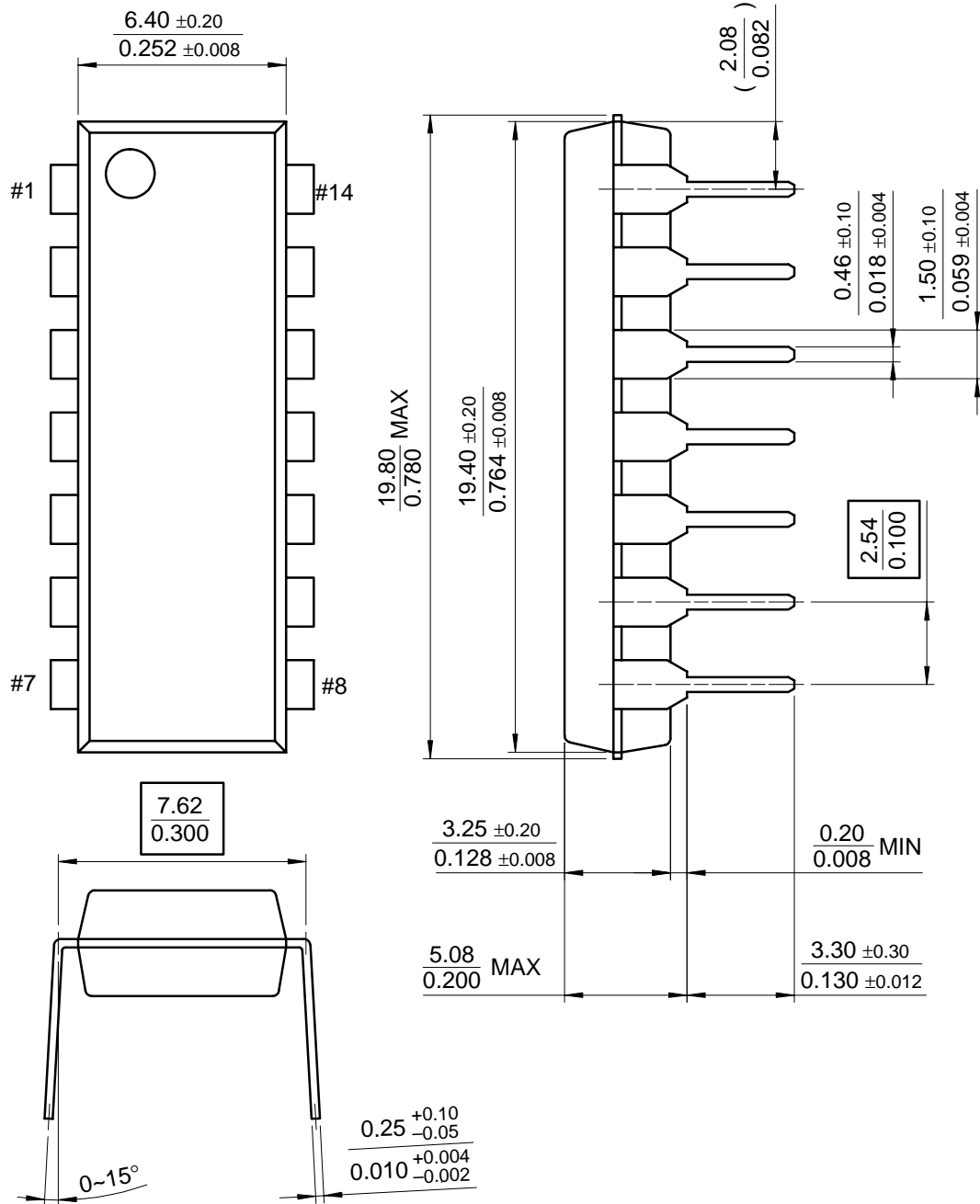
Load Regulation ($\Delta V_L = 50V$) 1.5mV

Mechanical Dimensions

Package

Dimensions in millimeters

14-DIP

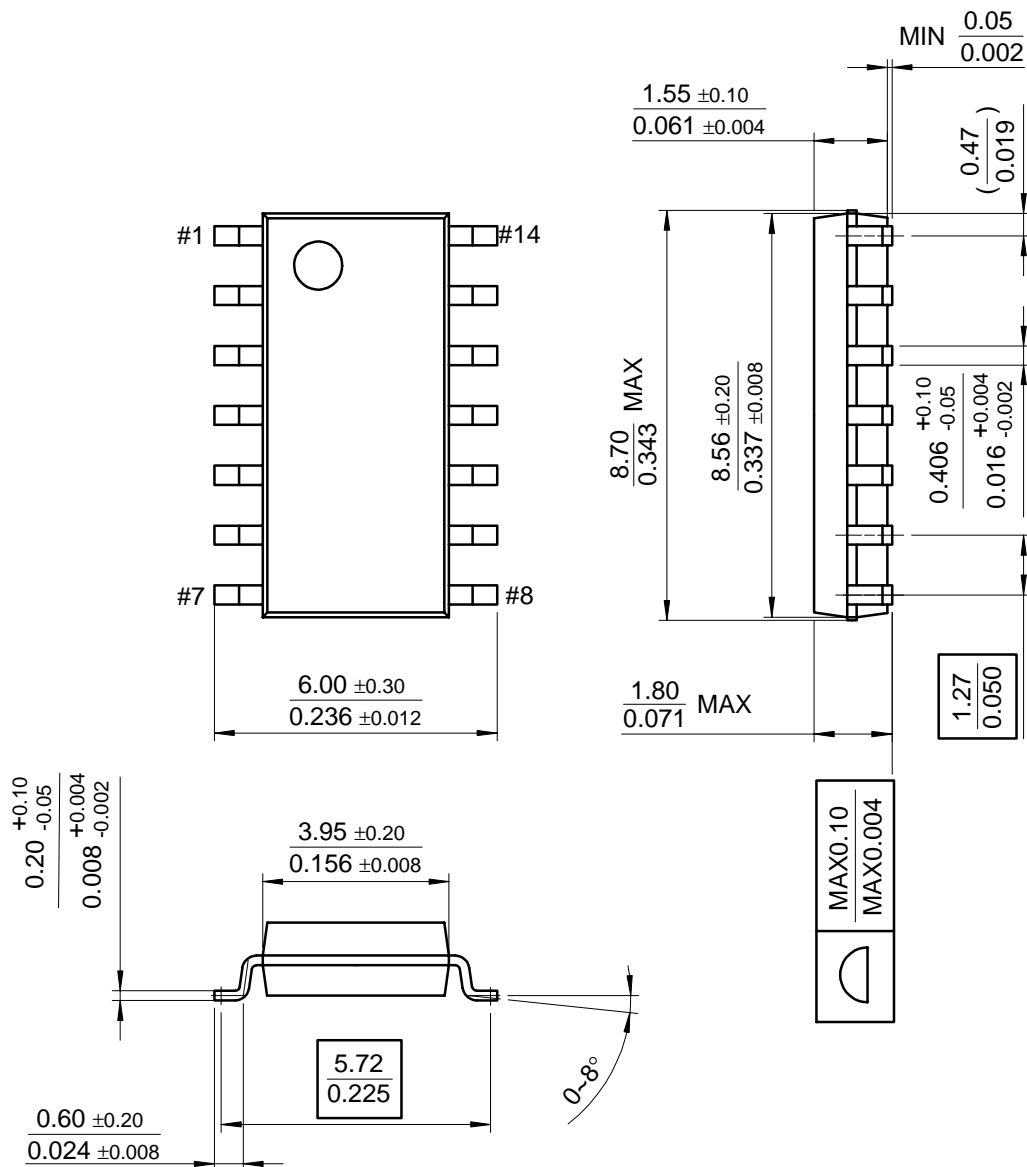


Mechanical Dimensions (Continued)

Package

Dimensions in millimeters

14-SOP



Ordering Information

Product Number	Package	Operating Temperature
KA723	14-DIP	0 ~ +70°C
KA723D	14-SOP	

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