

**FPN560**  
**FPN560A**



**NPN Low Saturation Transistor**

These devices are designed for high current gain and low saturation voltage with collector currents up to 3.0 A continuous. Sourced from Process NA.

**Absolute Maximum Ratings\*** TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CEO</sub>	Collector-Emitter Voltage	60	V
V <sub>CBO</sub>	Collector-Base Voltage	80	V
V <sub>EBO</sub>	Emitter-Base Voltage	5.0	V
I <sub>C</sub>	Collector Current - Continuous	3.0	A
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

\*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

**NOTES:**

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

**Thermal Characteristics** TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		FPN560 / FPN560A	
P <sub>D</sub>	Total Device Dissipation	1.0	W
R <sub>θJC</sub>	Thermal Resistance, Junction to Case	50	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient	125	°C/W

## NPN Low Saturation Transistor

(continued)

FPN560 / FPN560A

### Electrical Characteristics

$T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Max	Units
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#### OFF CHARACTERISTICS

$BV_{CEO}$	Collector-Emitter Breakdown Voltage	$I_C = 10\text{ mA}, I_B = 0$	60		V
$BV_{CBO}$	Collector-Base Breakdown Voltage	$I_C = 100\ \mu\text{A}, I_E = 0$	80		V
$BV_{EBO}$	Emitter-Base Breakdown Voltage	$I_E = 100\ \mu\text{A}, I_C = 0$	5.0		V
$I_{CBO}$	Collector Cutoff Current	$V_{CB} = 30\text{ V}, I_E = 0$ $V_{CB} = 30\text{ V}, I_E = 0, T_A = 100^\circ\text{C}$		100 10	nA $\mu\text{A}$
$I_{EBO}$	Emitter Cutoff Current	$V_{EB} = 4.0\text{ V}, I_C = 0$		100	nA

#### ON CHARACTERISTICS\*

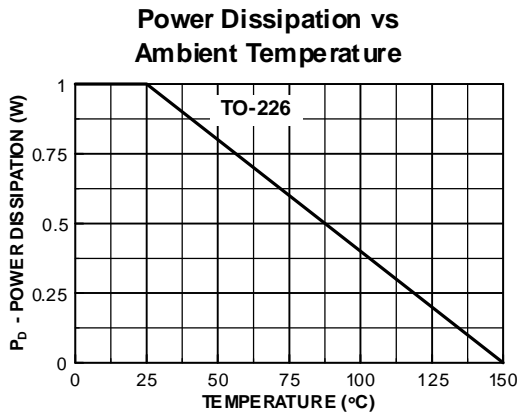
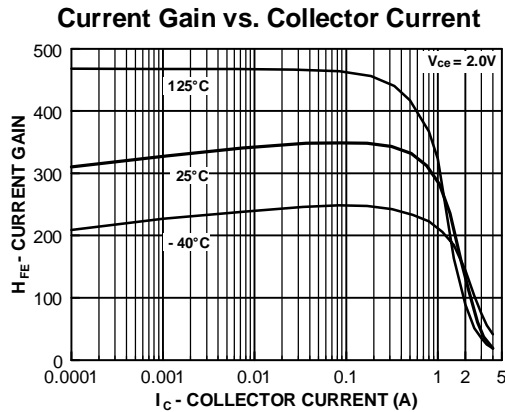
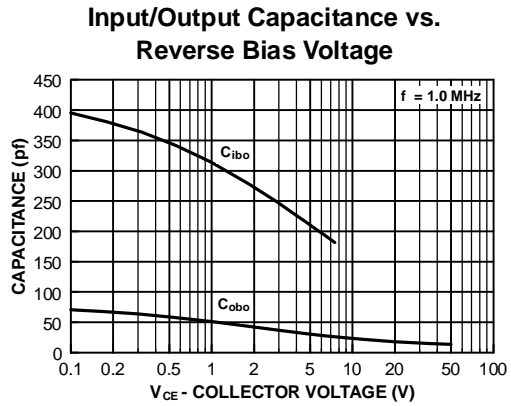
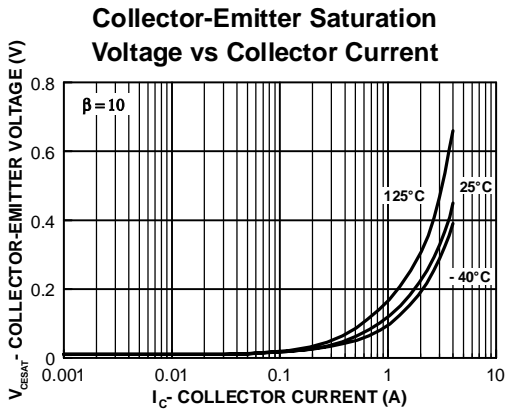
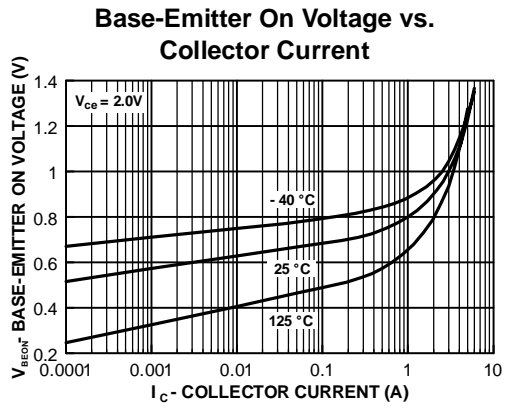
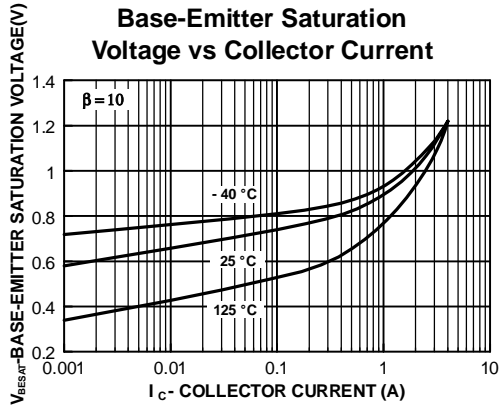
$h_{FE}$	DC Current Gain	$I_C = 100\text{ mA}, V_{CE} = 2.0\text{ V}$ $I_C = 500\text{ mA}, V_{CE} = 2.0\text{ V}$  $I_C = 1.0\text{ A}, V_{CE} = 2.0\text{ V}$ $I_C = 2.0\text{ A}, V_{CE} = 2.0\text{ V}$	  <b>560</b> <b>560A</b>	70 100 250 80 40	300 550	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = 1.0\text{ A}, I_B = 100\text{ mA}$ $I_C = 2.0\text{ A}, I_B = 200\text{ mA}$	<b>560</b> <b>560A</b>		300 350 300	mV mV mV
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = 1.0\text{ A}, I_B = 100\text{ mA}$			1.25	V
$V_{BE(on)}$	Base-Emitter Saturation Voltage	$I_C = 1.0\text{ A}, V_{CE} = 2.0\text{ V}$			1.0	V

#### SMALL SIGNAL CHARACTERISTICS

$C_{obo}$	Output Capacitance	$V_{CB} = 10\text{ V}, I_E = 0, f = 1.0\text{ MHz}$		30	pF
$F_T$	Transition Frequency	$I_C = 100\text{ mA}, V_{CE} = 5.0\text{ V},$ $f = 100\text{ MHz}$	75		MHz

\*Pulse Test: Pulse Width  $\leq 300\ \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$

Typical Characteristics



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