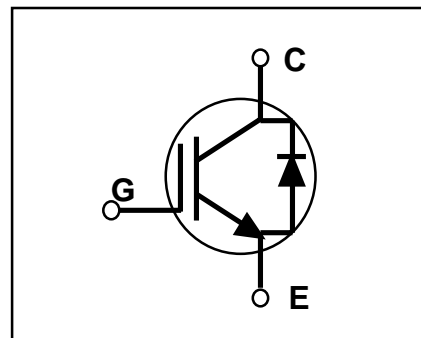
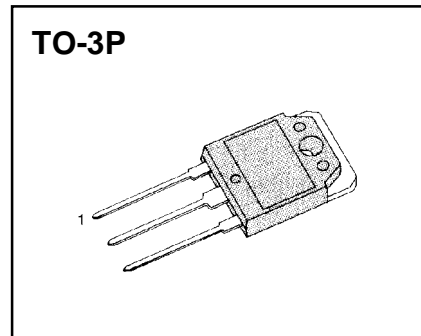


FEATURES

- * High Speed Switching
- * Low Saturation Voltage
: $V_{CE(sat)} = 1.95\text{ V}$ (@ $I_c=6.5\text{A}$)
- * High Input Impedance
- * CO-PAK, IGBT with FRD
: $T_{rr} = 37\text{nS}$ (typ.)

APPLICATIONS

- * AC & DC Motor controls
- * General Purpose Inverters
- * Robotics , Servo Controls
- * Power Supply
- * Lamp Ballast



ABSOLUTE MAXIMUM RATINGS

Symbol	Characteristics	Rating	Units
V_{CES}	Collector-Emitter Voltage	600	V
V_{GES}	Gate-Emitter Voltage	± 20	V
I_C	Collector Current @ $T_c = 25^\circ\text{C}$	13	A
	Collector Current @ $T_c = 100^\circ\text{C}$	6.5	A
$I_{CM(1)}$	Pulsed Collector Current	52	A
I_F	Diode Continuous Forward Current @ $T_c = 100^\circ\text{C}$	8	A
I_{FM}	Diode Maximum Forward Current	56	A
P_D	Maximum Power Dissipation @ $T_c = 25^\circ\text{C}$	60	W
	Maximum Power Dissipation @ $T_c = 100^\circ\text{C}$	25	W
T_j	Operating Junction Temperature	-55 ~ 150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55 ~ 150	$^\circ\text{C}$
T_L	Maximum Lead Temp. For Soldering	300	$^\circ\text{C}$
	Purposes, 1/8" from case for 5 seconds		

Notes:(1) Repetitive rating : Pulse width limited by max. junction temperature

ELECTRICAL CHARACTERISTICS (IGBT PART) (T_c=25°C, Unless Otherwise Specified)

Symbol	Characteristics	Test Conditions	Min	Typ	Max	Units
BV _{CES}	C - E Breakdown Voltage	V _{GE} = 0V , I _C = 250uA	600	-	-	V
ΔV _{CES} / ΔT _J	Temperature Coeff. of Breakdown Voltage	V _{GE} = 0V , I _C = 1mA	-	0.6	-	V/°C
V _{GE(th)}	G - E threshold voltage	I _C = 6.5mA , V _{CE} = V _{GE}	4.0	5.5	7.5	V
I _{CES}	Collector cutoff Current	V _{CE} = V _{CES} , V _{GE} = 0V	-	-	250	uA
I _{GES}	G - E leakage Current	V _{GE} = V _{GES} , V _{CE} = 0V	-	-	100	nA
V _{CE(sat)}	Collector to Emitter saturation voltage	I _C =6.5A, V _{GE} = 15V	-	1.95	2.6	V
		I _C =13A, V _{GE} = 15V	-	2.6	-	V
Cies	Input capacitance	V _{GE} = 0V , f = 1MHz	-	375	-	pF
Co _{es}	Output capacitance	V _{CE} = 30V	-	63	-	pF
Cr _{es}	Reverse transfer capacitance		-	13	-	pF
td(on)	Turn on delay time	V _{CC} = 300V , I _C = 6.5A	-	15	-	nS
tr	Turn on rise time	V _{GE} = 15V	-	26	-	nS
td(off)	Turn off delay time	R _G = 50Ω	-	50	80	nS
tf	Turn off fall time	Inductive Load	-	110	220	nS
E _{on}	Turn on Switching Loss		-	0.1	-	mJ
E _{off}	Turn off Switching Loss		-	0.1	-	mJ
E _{ts}	Total Switching Loss		-	0.2	0.3	mJ
Q _g	Total Gate Charge	V _{CC} = 300V	-	25	37	nC
Q _{ge}	Gate-Emitter Charge	V _{GE} = 15V	-	7	11	nC
Q _{gc}	Gate-Collector Charge	I _C = 6.5A	-	8	12	nC
Le	Internal Emitter Inductance	Measured 5mm from PKG	-	14	-	nH

ELECTRICAL CHARACTERISTICS (DIODE PART)

(T_c=25°C, Unless Otherwise Specified)

Symbol	Characteristics	Test Conditions		Min	Typ	Max	Units
V _{FM}	Diode Forward Voltage	I _F =8.0A	T _c =25°C	-	1.4	1.7	V
			T _c =100°C	-	1.3	-	
T _{rr}	Diode Reverse Recovery Time	I _F =8.0A, V _R =200V -di/dt=200A/uS	T _c =25°C	-	37	55	nS
			T _c =100°C	-	55	-	
I _{rr}	Diode Peak Reverse Recovery Current		T _c =25°C	-	3.5	5.0	A
			T _c =100°C	-	4.5	-	
Q _{rr}	Diode Reverse Recovery Charge		T _c =25°C	-	65	138	nC
			T _c =100°C	-	124	-	

THERMAL RESISTANCE

Symbol	Characteristics	Min	Typ	Max	Units
R _{θJC}	Junction-to-Case (IGBT)	-	-	2.0	°C/W
R _{θJC}	Junction-to-Case (DIODE)	-	-	3.5	°C/W
R _{θJA}	Junction-to-Ambient	-	-	40	°C/W
R _{θCS}	Case-to-Sink	-	0.24	-	°C/W

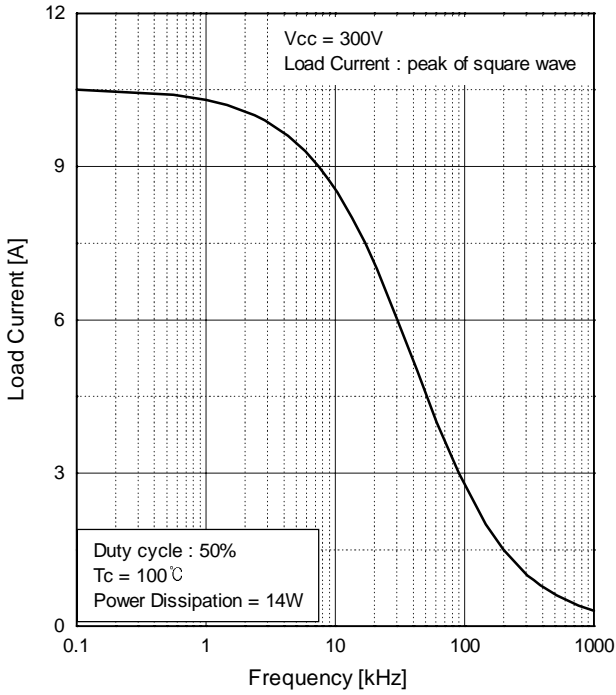


Fig.1 Typical Load Current vs. Frequency

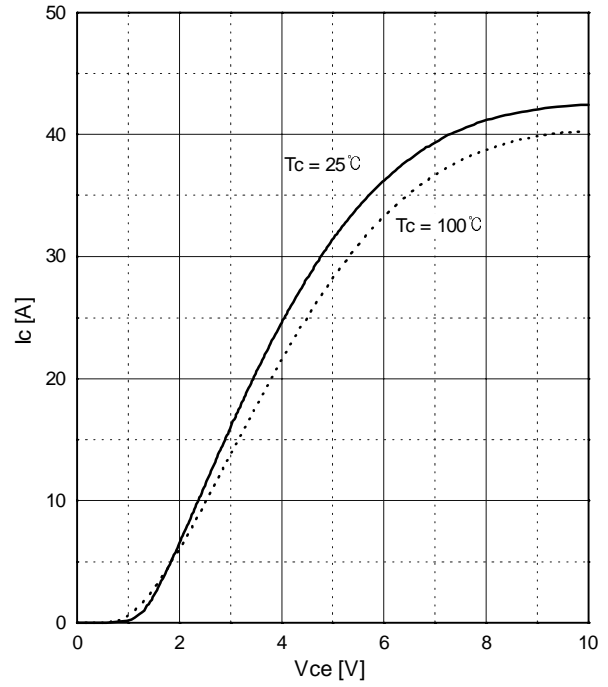


Fig.2 Typical Output Characteristics

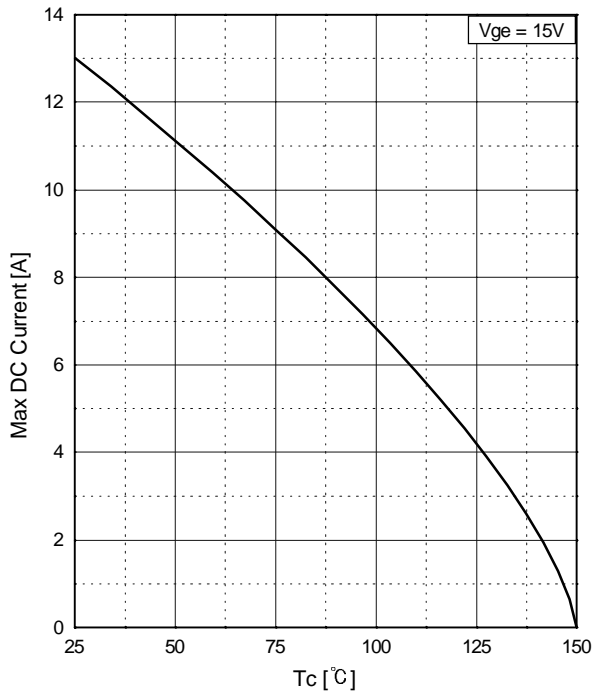


Fig.3 Maximum Collector Current vs. Case Temperature

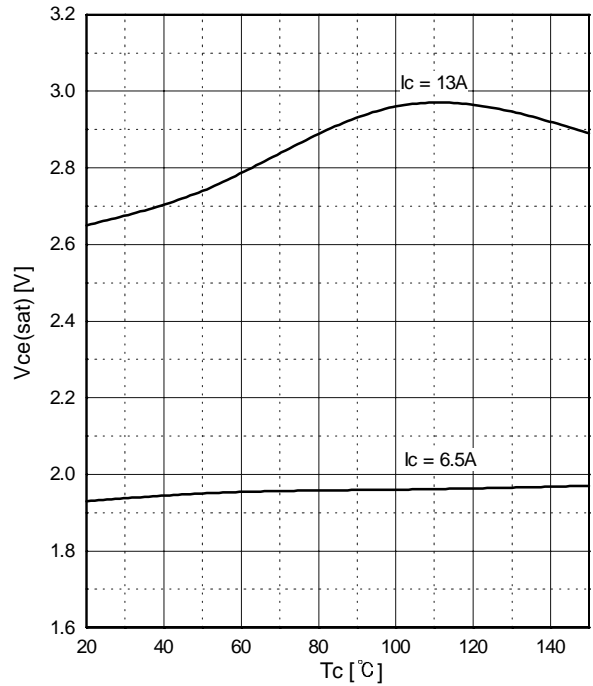


Fig.4 Collector to Emitter Voltage vs. Case Temperature

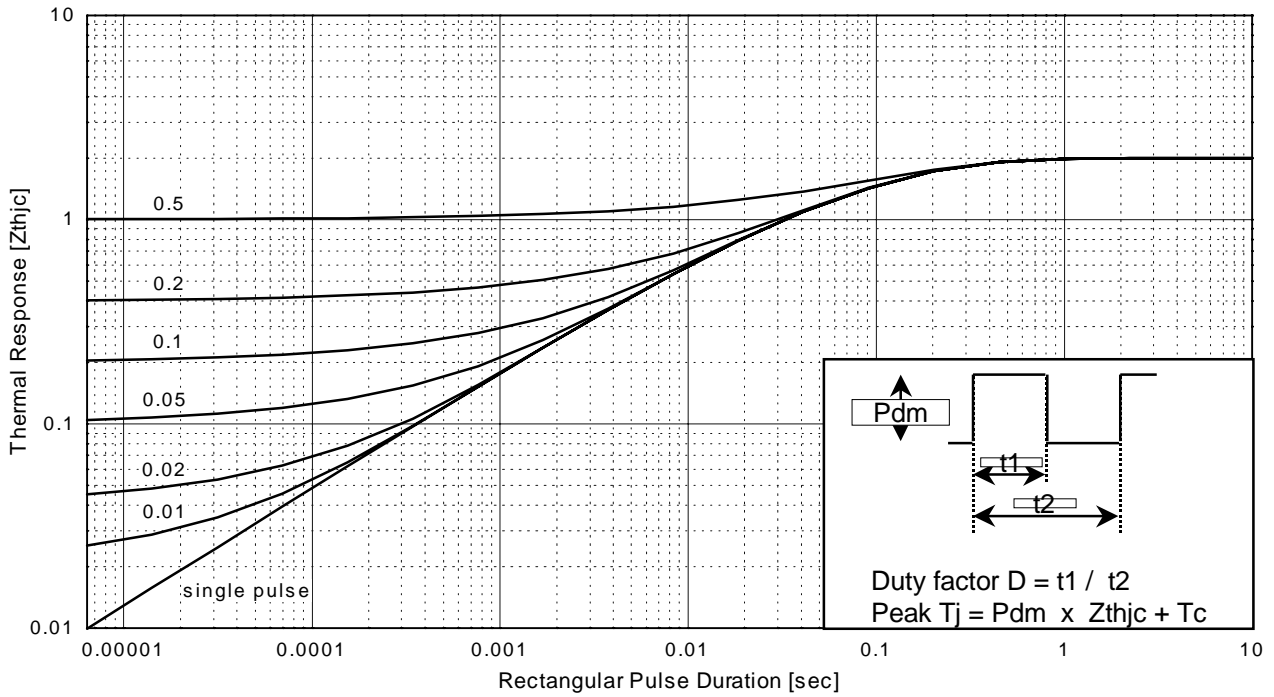


Fig.5 Maximum Effective Transient Thermal Impedance, Junction to Case

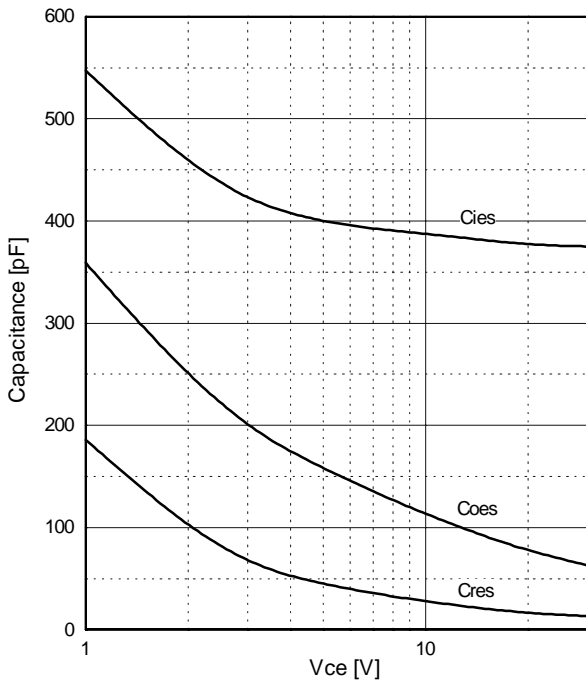


Fig.6 Typical Capacitance vs. Collector to Emitter Voltage

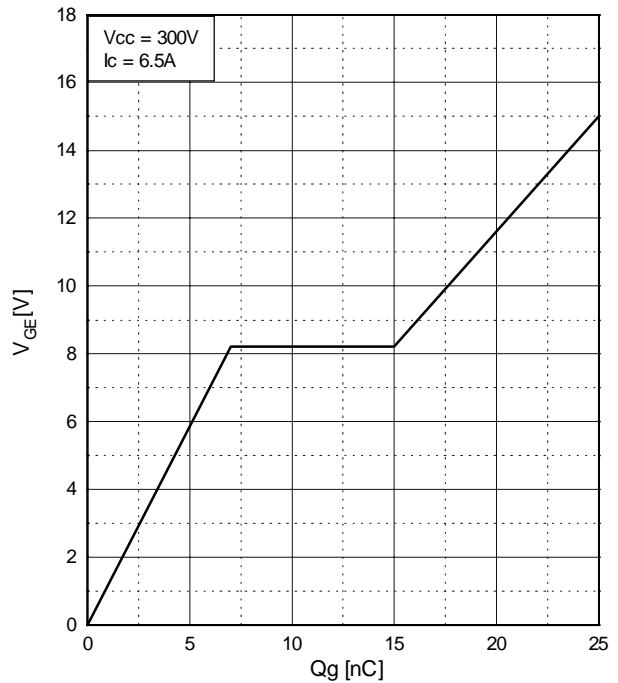


Fig.7 Typical Gate Charge vs. Gate to Emitter Voltage

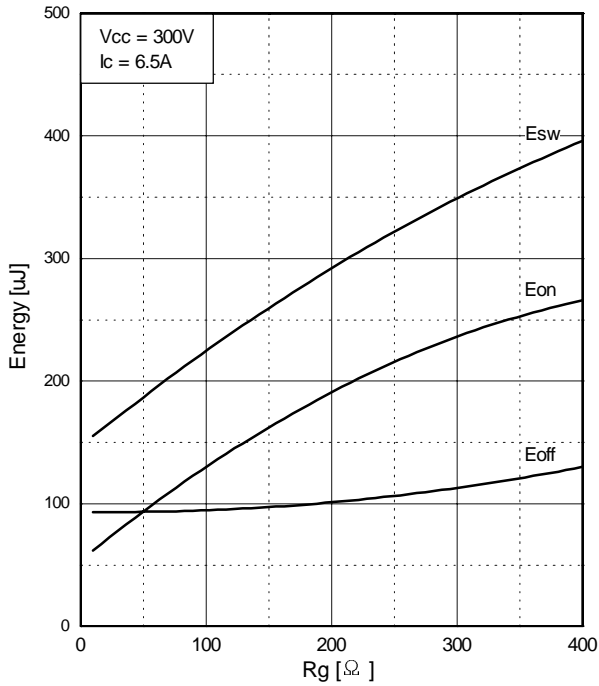


Fig.8 Typical Switching Loss vs. Gate Resistance

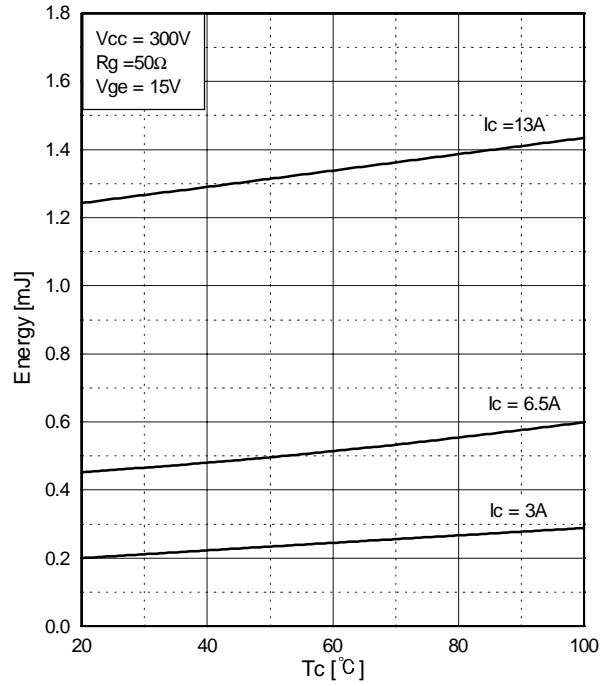


Fig.9 Typical Switching Loss vs. Case Temperature

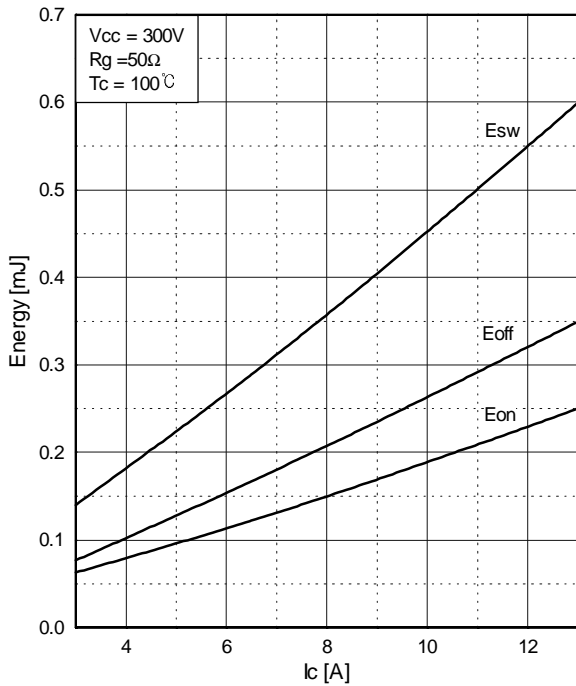


Fig.10 Typical Switching loss vs. Collector to Emitter Current

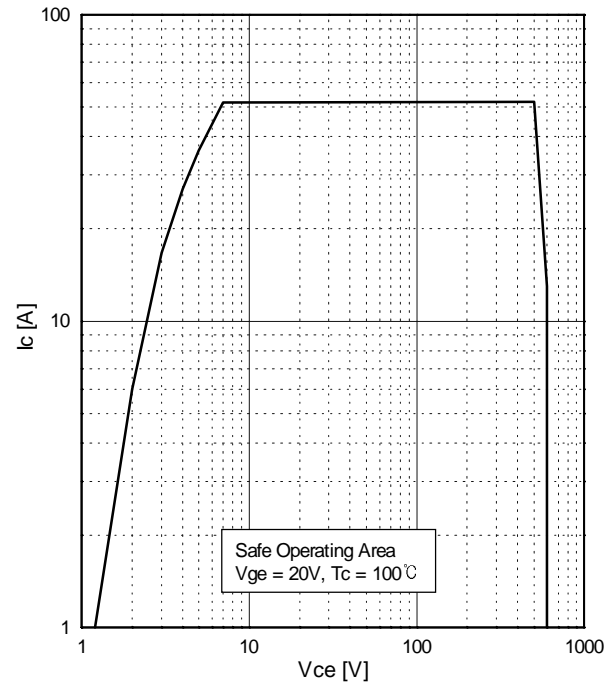


Fig.11 Turn-off SOA

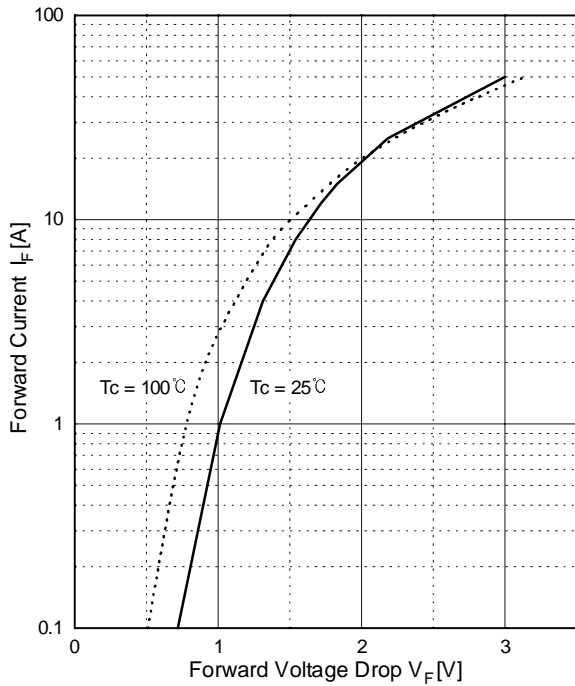


Fig.12 Typical Forward Voltage Drop vs. Forward Current

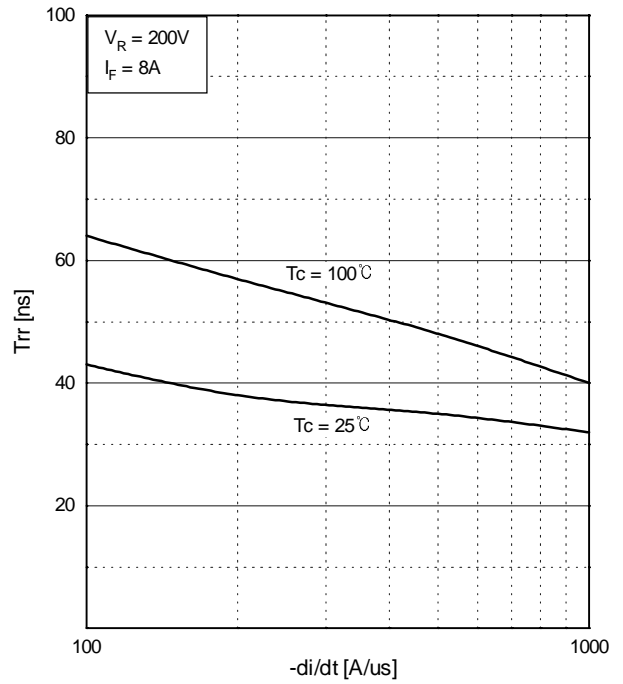


Fig.13 Typical Reverse Recovery Time vs. di/dt

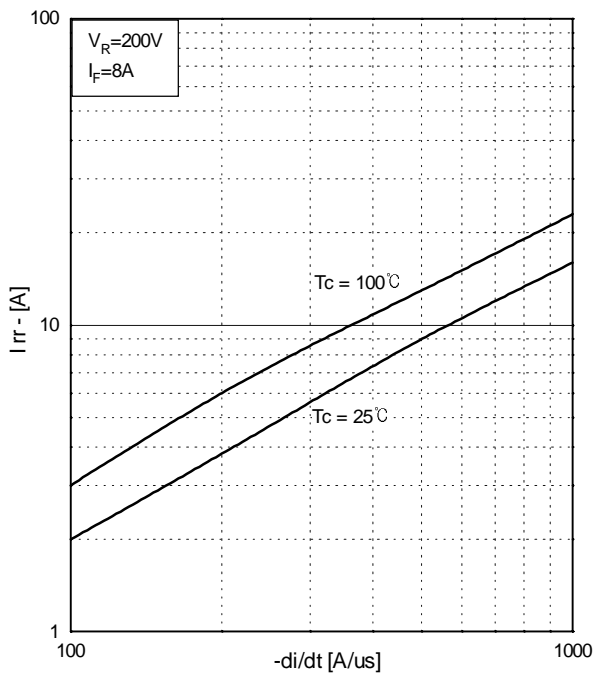


Fig.14 Typical Reverse Recovery Current vs. di/dt

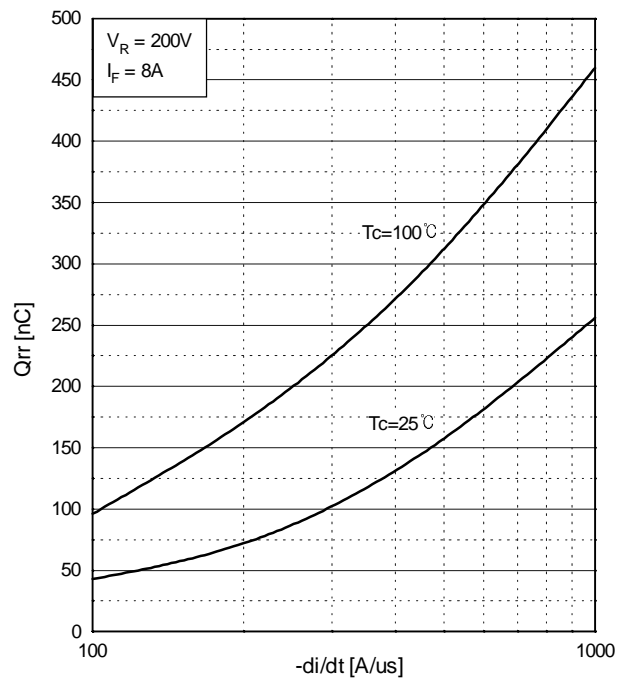


Fig.15 Typical Stored Charge vs. di/dt

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