

Si4963DY

Dual P-Channel 2.5V Specified PowerTrench® MOSFET

General Description

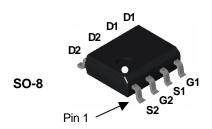
This P-Channel 2.5V specified MOSFET is a rugged gate version of Fairchild Semiconductor's advanced PowerTrench process. It has been optimized for power management applications with a wide range of gate drive voltage (2.5V - 12V).

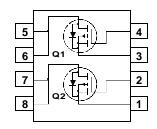
Applications

- Load switch
- Motor drive
- DC/DC conversion
- Power management

Features

- -6.2 A, -20 V, $R_{DS(ON)} = 33$ m Ω @ $V_{GS} = -4.5$ V $R_{DS(ON)} = 50$ m Ω @ $V_{GS} = -2.5$ V
- Extended V_{GSS} range (±12V) for battery applications
- Low gate charge
- High performance trench technology for extremely low $R_{DS(ON)}$
- High power and current handling capability





Absolute Maximum Ratings TA=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units	
V _{DSS}	Drain-Source Voltage		-20	V	
V _{GSS}	Gate-Source Voltage		±12	V	
I _D	Drain Current - Continuous	(Note 1a)	-6.2	Α	
	- Pulsed		-40		
P _D	Power Dissipation for Dual Operation		2	W	
	Power Dissipation for Single Operation	(Note 1a)	1.6		
		(Note 1b)	1		
		(Note 1c)	0.9		
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to +175	°C	

Thermal Characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient	(Note 1a)	78	°C/W
R _{0JC}	Thermal Resistance, Junction-to-Case	(Note 1)	40	°C/W

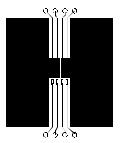
Package Marking and Ordering Information

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Device Marking	Device	Reel Size	Tape width	Quantity	
4963	Si4963DY	13"	12mm	2500 units	

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	acteristics				ı	I.
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	-20			V
<u>ΔBV DSS</u> ΔT _J	Breakdown Voltage Temperature Coefficient	$I_D = -250 \mu\text{A}$, Referenced to 25°C		-16		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$			-1	μΑ
I _{GSSF}	Gate-Body Leakage, Forward	$V_{GS} = -12 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
I _{GSSR}	Gate-Body Leakage, Reverse	$V_{GS} = 12 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA
On Char	acteristics (Note 2)					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.6	-1.0	-1.5	V
$\Delta V_{GS(th)} \over \Delta T_J$	Gate Threshold Voltage Temperature Coefficient	I_D = -250 μA, Referenced to 25°C		3		mV/°C
R _{DS(on)}	Static Drain–Source On–Resistance	$V_{GS} = -4.5 \text{ V}, I_D = -6.2 \text{ A}$ $V_{GS} = -2.5 \text{ V}, I_D = -5 \text{ A}$ $V_{GS} = -4.5 \text{ V}, I_D = -6.2 \text{A},$ $T_J = 125^{\circ}\text{C}$		23 34 45	33 50 56	mΩ
I _{D(on)}	On–State Drain Current	$V_{GS} = -4.5 \text{ V}, V_{DS} = -5 \text{ V}$	-15			Α
g FS	Forward Transconductance	$V_{DS} = -5 \text{ V}, \qquad I_D = -6.2 \text{ A}$		19		S
Dvnamio	Characteristics					
C _{iss}	Input Capacitance	V 40V V 0V		1456		pF
Coss	Output Capacitance	$V_{DS} = -10 \text{ V}, \qquad V_{GS} = 0 \text{ V},$		300		pF
C _{rss}	Reverse Transfer Capacitance	f = 1.0 MHz		150		pF
Switchin	g Characteristics (Note 2)					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = -10 \text{ V}, \qquad I_D = -1 \text{ A},$		15	27	ns
t _r	Turn-On Rise Time	$V_{GS} = -4.5 \text{ V}, \qquad R_{GEN} = 6 \Omega$		11	20	ns
t _{d(off)}	Turn-Off Delay Time	1		57	91	ns
t _f	Turn-Off Fall Time	1		37	59	ns
Qg	Total Gate Charge	$V_{DS} = -10 \text{ V}, \qquad I_D = -6.2 \text{ A},$		14	20	nC
Q _{gs}	Gate-Source Charge	$V_{GS} = -4.5 \text{ V}$		3		nC
Q _{gd}	Gate-Drain Charge]		5		nC
Drain-S	ource Diode Characteristics	and Maximum Ratings				
Is	Maximum Continuous Drain-Source				-1.3	Α
V _{SD}	Drain–Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = -1.3 \text{ A} \text{(Note 2)}$		-0.7	-1.2	V

Notes

1. R_{QJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{QJC} is guaranteed by design while R_{QCA} is determined by the user's board design.



a) 78°C/W when mounted on a 0.5in² pad of 2 oz copper



125°C/W when mounted on a 0.02 in² pad of 2 oz copper



135°C/W when mounted on a minimum pad.

Scale 1:1 on letter size paper

2. Pulse Test: Pulse Width < 300μ s, Duty Cycle < 2.0%

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