HD74AC86/HD74ACT86

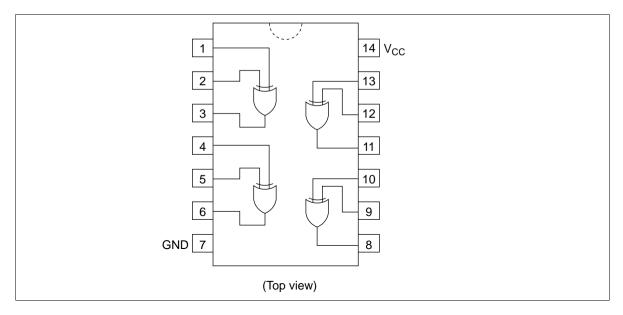
Quad 2-Input Exclusive-OR-Gate

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Features

- Outputs Source/Sink 24 mA
- HD74ACT86 has TTL-Compatible Inputs

Pin Arrangement



DC Characteristics (unless otherwise specified)

Item	Symbol	Max	Unit	Condition
Maximum quiescent supply current	I _{cc}	40	μA	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5$ V, Ta = Worst case
Maximum quiescent supply current	I _{cc}	4.0	μA	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5$ V, Ta = 25°C
Maximum I _{cc} /input (HD74ACT86)	I _{CCT}	1.5	mA	$V_{IN} = V_{CC} - 2.1 \text{ V}, V_{CC} = 5.5 \text{ V},$ Ta = Worst case



HD74AC86/HD74ACT86

AC Characteristics: HD74AC86

			Ta = +25°C C _∟ = 50 pF		Ta = −40°C to +85°C C _∟ = 50 pF			
ltem	Symbol	V _{cc} (V)* ¹	Min	Тур	Max	Min	Max	Unit
Propagation delay	t _{PLH}	3.3	1.0	9.5	12.5	1.0	14.0	ns
		5.0	1.0	7.5	10.0	1.0	11.0	
Propagation delay	t _{PHL}	3.3	1.0	8.5	11.5	1.0	13.0	ns
		5.0	1.0	6.5	9.0	1.0	10.0	

Note: 1. Voltage Range 3.3 is $3.3 \text{ V} \pm 0.3 \text{ V}$ Voltage Range 5.0 is $5.0 \text{ V} \pm 0.5 \text{ V}$

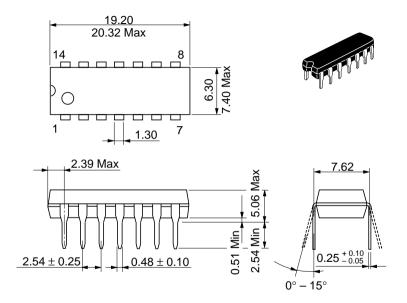
AC Characteristics: HD74ACT86

				Ta = +25°C C _∟ = 50 pF		Ta = −40°C to +85°C C _∟ = 50 pF		
ltem	Symbol	V _{cc} (V)* ¹	Min	Тур	Max	Min	Max	Unit
Propagation delay	t _{PLH}	5.0	1.0	8.5	11.0	1.0	12.0	ns
Propagation delay	t _{PHL}	5.0	1.0	7.0	10.0	1.0	11.0	ns

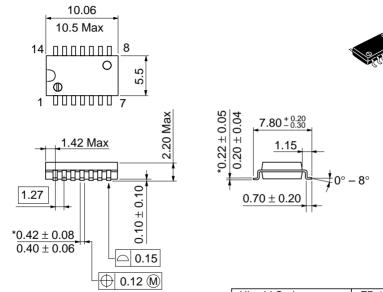
Note: 1. Voltage Range 5.0 is 5.0 V \pm 0.5 V

Capacitance

Item	Symbol	Тур	Unit	Condition
Input capacitance	CIN	4.5	pF	$V_{cc} = 5.5 V$
Power dissipation capacitance	C_{PD}	4.5	pF	$V_{cc} = 5.0 V$

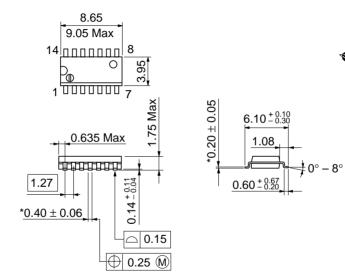


Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g



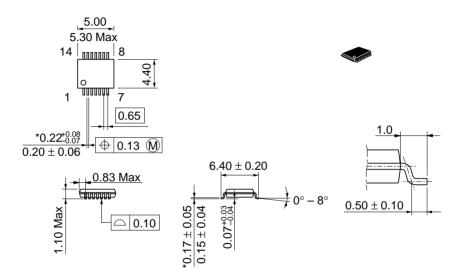
*Dimension including the plating thickness Base material dimension

Hitachi Code	FP-14DA
JEDEC	—
EIAJ	Conforms
Weight (reference value)	0.23 g



Hitachi Code	FP-14DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.13 g

*Pd plating



*Dimension including the plating thickness Base material dimension

Hitachi Code	TTP-14D
JEDEC	
EIAJ	
Weight (reference value)	0.05 g

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Semiconductor & Integrated Circuits. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109 NorthAmerica URL http:semiconductor.hitachi.com/ http://www.hitachi-eu.com/hel/ecg Europe http://www.has.hitachi.com.sg/grp3/sicd/index.htm http://www.hitachi.com.tw/E/Product/SICD_Frame.htm Asia (Singapore) Asia (Taiwan) Asia (HongKong) http://www.hitachi.com.hk/eng/bo/grp3/index.htm http://www.hitachi.co.jp/Sicd/indx.htm Japan For further information write to: Hitachi Semiconductor Hitachi Europe GmbH Hitachi Asia Pte. Ltd. (America) Inc. Electronic components Group 16 Collyer Quay #20-00 179 East Tasman Drive, Dornacher Stra§e 3 Hitachi Tower San Jose,CA 95134 D-85622 Feldkirchen, Munich Singapore 049318 Tel: <1> (408) 433-1990 Fax: <1>(408) 433-0223 Germany Tel: 535-2100 Tel: <49> (89) 9 9180-0 Fax: 535-1533 Fax: <49> (89) 9 29 30 00

 Fax: <49> (89) 9 29 30 00
 Hita

 Hitachi Europe Ltd.
 Hita

 Electronic Components Group.
 Taip

 Whitebrook Park
 3F,

 Lower Cookham Road
 Tun

 Maidenhead
 Tel:

 Berkshire SL6 8YA, United Kingdom
 Fax

 Tel: <44> (1628) 585000

 Fax: <44> (1628) 778322

Hitachi Asia Ltd. Taipei Branch Office 3F, Hung Kuo Building. No.167, Tun-Hwa North Road, Taipei (105) Tel: <886> (2) 2718-3666 Fax: <886> (2) 2718-8180

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Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Tsim Sha Tsui, Kowloon, Hong Kong Tel: <852> (2) 735 9218 Fax: <852> (2) 730 0281 Telex: 40815 HITEC HX

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