# HD74AC126/HD74ACT126

Quad Buffer/Line Driver with 3-State Output

# **HITACHI**

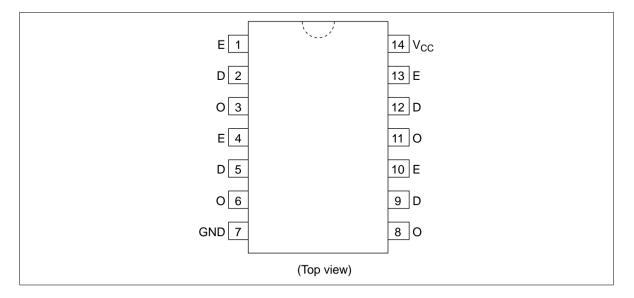
### **Description**

The HD74AC126/HD74ACT126 is an quad buffer and line driver designed to be employed as a memory address driver, clock driver and bus oriented transmitter/receiver which provides improved PC board density.

#### **Features**

- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- Outputs Source/Sink 24 mA
- HD74ACT126 has TTL-Compatible Inputs

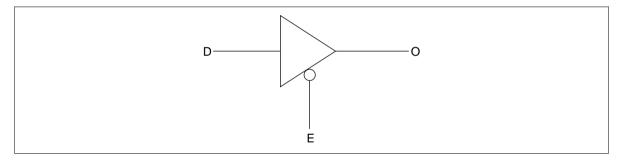
## Pin Arrangement





## HD74AC126/HD74ACT126

## Logic Symbol



### Pin Names

D Data Inputs

E 3-State Output Enable Inputs (Active High)

O Outputs

### **Truth Table**

#### Inputs

Е	D	- Output
Н	L	L
Н	Н	Н
L	X	Z

H: High Voltage Level
L: Low Voltage Level

X : Immaterial

Z: High Impedance

## **DC Characteristics** (unless otherwise specified)

Item	Symbol	Max	Unit	Condition
Maximum Quiescent Supply Current	I <sub>cc</sub>	80	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$ , Ta = Worst case
Maximum Quiescent Supply Current	I <sub>cc</sub>	8.0	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$ , $Ta = 25^{\circ}\text{C}$
Maximum I <sub>cc</sub> /Input (HD74ACT126)	I <sub>CCT</sub>	1.5	mA	$V_{IN} = V_{CC} - 2.1 \text{ V}, V_{CC} = 5.5 \text{ V}$ Ta = Worst case

AC Characteristics: HD74AC126

			Ta = +25°C C <sub>L</sub> = 50 pF			Ta = $-40^{\circ}$ C to $+85^{\circ}$ C C <sub>L</sub> = 50 pF		
Item	Symbol	V <sub>cc</sub> (V)*1	Min	Тур	Max	Min	Max	Unit
Propagation Delay	t <sub>PLH</sub>	3.3	1.0	6.5	9.0	1.0	10.0	ns
		5.0	1.0	5.5	7.0	1.0	7.5	
Propagation Delay	t <sub>PHL</sub>	3.3	1.0	6.5	9.0	1.0	10.0	
		5.0	1.0	5.0	7.0	1.0	7.5	
Enable Time	t <sub>PZL</sub>	3.3	1.0	6.5	12.5	1.0	13.0	<del></del>
		5.0	1.0	5.5	9.0	1.0	9.5	<del></del>
Enable Time	t <sub>PHZ</sub>	3.3	1.0	7.0	12.0	1.0	13.0	<del></del>
		5.0	1.0	5.5	9.0	1.0	9.5	<del></del>
Disable Time	t <sub>PLZ</sub>	3.3	1.0	8.0	12.0	1.0	12.5	_
		5.0	1.0	6.5	10.0	1.0	10.5	_
Disable Time	t <sub>PZH</sub>	3.3	1.0	7.0	12.5	1.0	13.5	_
		5.0	1.0	6.0	10.0	1.0	10.5	_

Note: 1. Voltage Range 3.3 is  $3.3 \text{ V} \pm 0.3 \text{ V}$ Voltage Range 5.0 is  $5.0 \text{ V} \pm 0.5 \text{ V}$ 

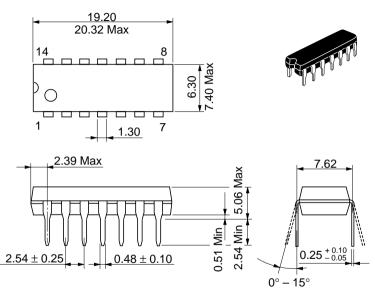
**AC Characteristics: HD74ACT125** 

		Ta = +25°C C <sub>L</sub> = 50 pF		Ta = $-40^{\circ}$ C to $+85^{\circ}$ C C <sub>L</sub> = 50 pF				
Item	Symbol	V <sub>cc</sub> (V)*1	Min	Тур	Max	Min	Max	Unit
Propagation Delay	t <sub>PLH</sub>	5.0	1.0	6.5	9.0	1.0	10.0	ns
Propagation Delay	t <sub>PHL</sub>	5.0	1.0	7.0	9.0	1.0	10.0	<del></del>
Enable Time	t <sub>PZH</sub>	5.0	1.0	6.0	9.0	1.0	10.0	<del></del>
Enable Time	t <sub>PZL</sub>	5.0	1.0	7.0	10.0	1.0	11.0	<del></del>
Disable Time	t <sub>PHZ</sub>	5.0	1.0	8.0	10.5	1.0	11.5	<del></del>
Disable Time	t <sub>PLZ</sub>	5.0	1.0	7.0	10.5	1.0	11.5	<del></del>

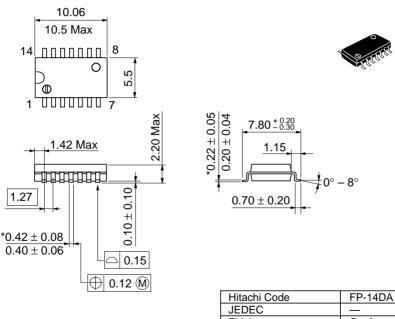
Note: 1. Voltage Range 5.0 is 5.0 V  $\pm$  0.5 V

## Capacitance

Item	Symbol	Тур	Unit	Condition	
Input Capacitance	C <sub>IN</sub>	4.5	pF	$V_{CC} = 5.5 \text{ V}$	
Power Dissipation Capacitance	C <sub>PD</sub>	45.0	pF	V <sub>cc</sub> = 5.0 V	



Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g

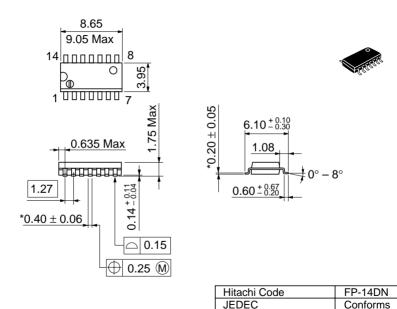


\*Dimension including the plating thickness
Base material dimension

\*Dimension including the plating thickness

Base material dimension

\*United States of The 14-57 of The 14



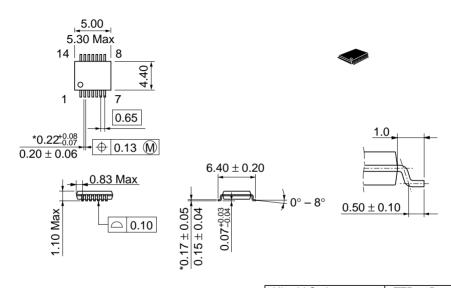
EIAJ

Weight (reference value)

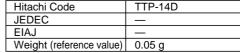
Conforms

0.13 g

\*Pd plating



\*Dimension including the plating thickness
Base material dimension



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