2SK1618(L), 2SK1618(S)

Silicon N-Channel MOS FET

HITACHI

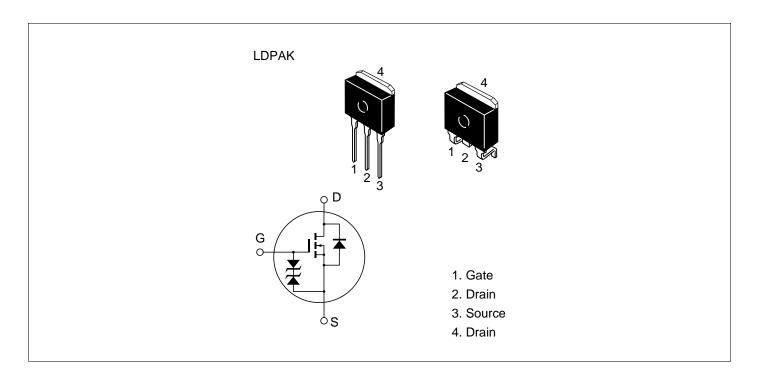
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

Outline





2SK1618(L), 2SK1618(S)

Absolute Maximum Ratings $(Ta = 25^{\circ}C)$

| Item | Symbol | Ratings | Unit |
|---|------------------------------|-------------|------|
| Drain to source voltage | $V_{\scriptscriptstyle DSS}$ | 600 | V |
| Gate to source voltage | V_{GSS} | ±30 | V |
| Drain current | I _D | 3 | A |
| Drain peak current | l _{D(pulse)} *1 | 6 | A |
| Body to drain diode reverse drain current | I _{DR} | 3 | A |
| Channel dissipation | Pch*2 | 30 | W |
| Channel temperature | Tch | 150 | °C |
| Storage temperature | Tstg | -55 to +150 | °C |

Notes 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at $T_c = 25^{\circ}C$

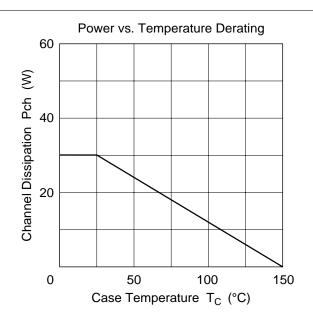
Electrical Characteristics ($Ta = 25^{\circ}C$)

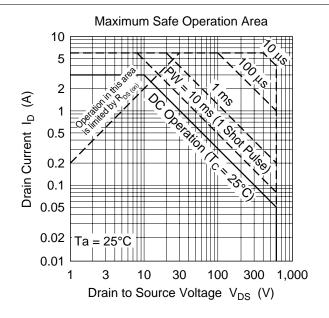
| Item | Symbol | Min | Тур | Max | Unit | Test conditions |
|--|---------------------------------|-----|-----|-----|------|---|
| Drain to source breakdown voltage | $V_{(BR)DSS}$ | 600 | _ | _ | V | $I_{D} = 10 \text{ mA}, V_{GS} = 0$ |
| Gate to source breakdown voltage | $V_{(BR)GSS}$ | ±30 | _ | _ | V | $I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$ |
| Gate to source leak current | I _{GSS} | | _ | ±10 | μΑ | $V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$ |
| Zero gate voltage drain current | $I_{	exttt{DSS}}$ | _ | _ | 250 | μΑ | $V_{DS} = 500 \text{ V}, V_{GS} = 0$ |
| Gate to source cutoff voltage | $V_{\text{GS(off)}}$ | 2.0 | _ | 3.0 | V | $I_{D} = 1 \text{ mA}, V_{DS} = 10 \text{ V}$ |
| Static Drain to source on state resistance | $R_{\scriptscriptstyle DS(on)}$ | _ | 3.8 | 5.0 | Ω | $I_D = 1 \text{ A}, V_{GS} = 10 \text{ V}^{*1}$ |
| Forward transfer admittance | yfs | 1.2 | 2.0 | _ | S | $I_D = 1 \text{ A}, V_{DS} = 10 \text{ V}^{*1}$ |
| Input capacitance | Ciss | _ | 295 | _ | pF | $V_{DS} = 10 \text{ V}, V_{GS} = 0,$ |
| Output capacitance | Coss | _ | 70 | _ | pF | f = 1 MHz |
| Reverse transfer capacitance | Crss | _ | 12 | _ | pF | _ |
| Turn-on delay time | t _{d(on)} | | 8 | _ | ns | $I_D = 1 \text{ A}, V_{GS} = 10 \text{ V},$ |
| Rise time | t _r | _ | 25 | _ | ns | $R_L = 30 \Omega$ |
| Turn-off delay time | $t_{\text{d(off)}}$ | _ | 65 | _ | ns | _ |
| Fall time | t _f | _ | 30 | _ | ns | _ |
| Body to drain diode forward voltage | V_{DF} | _ | 0.9 | _ | V | $I_F = 2 A, V_{GS} = 0$ |
| Body to drain diode reverse recovery time | t _{rr} | | 220 | | ns | $I_F = 2 \text{ A}, V_{GS} = 0,$ $di_F/dt = 100 \text{ A/}\mu\text{s}$ |

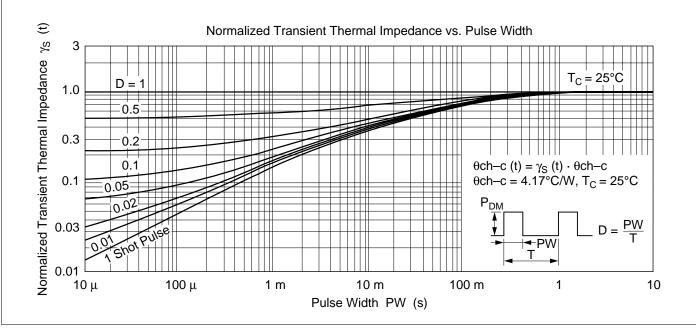
Note 1. Pulse test

See characteristic curves of 2SK1572.

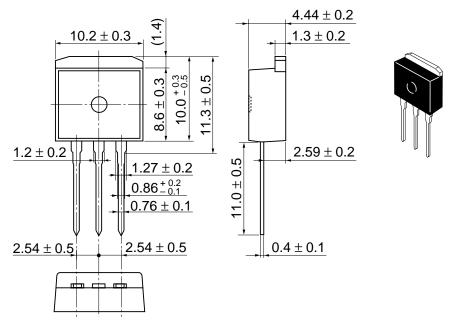
2SK1618(L), 2SK1618(S)







Unit: mm



| Hitachi Code | LDPAK (L) |
|--------------------------|-----------|
| JEDEC | _ |
| EIAJ | _ |
| Weight (reference value) | 1.4 g |

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