HAT2024R

Silicon N Channel Power MOS FET
High Speed Power Switching

HITACHI

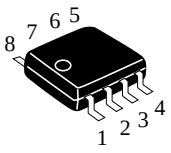
ADE-208-494 C (Z)
4th. Edition
July 1997

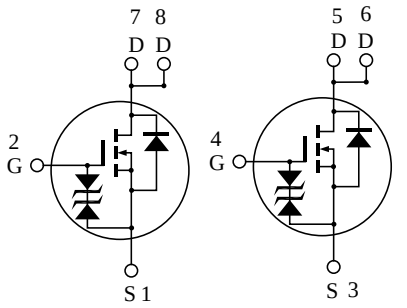
Features

- Low on-resistance
- Capable of 4 V gate drive
- Low drive current
- High density mounting

Outline

SOP–8





1, 3	S
2, 4	G
5, 6, 7, 8	Dra

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Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	30	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	5.5	A
Drain peak current	$I_{D(pulse)}$*[1]	44	A
Body to drain diode reverse drain current	I_{DR}	5.5	A
Channel dissipation	Pch*[2]	2	W
Channel dissipation	Pch *[3]	3	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10µs, duty cycle ≤ 1 %
2. 1 Drive operation : When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW≤ 10s
3. 2 Drive operation : When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW≤ 10s

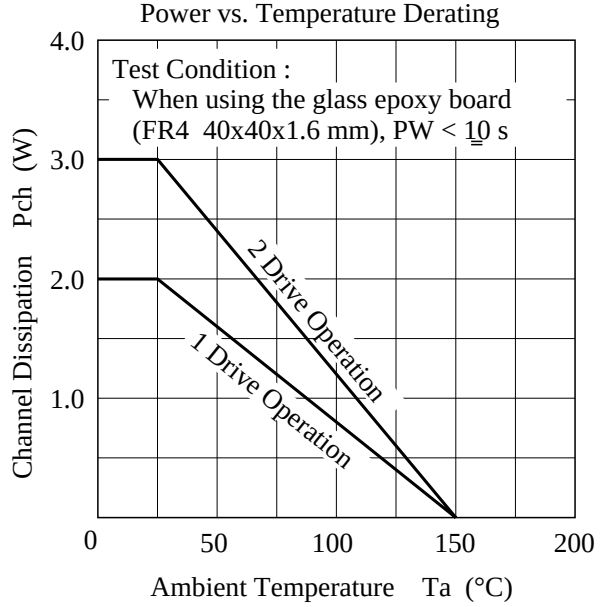
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	—	—	V	I_D = 10mA, V_{GS} = 0
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	—	V	I_G = ±100µA, V_{DS} = 0
Gate to source leak current	I_{GSS}	—	—	±10	µA	V_{GS} = ±16V, V_{DS} = 0
Zero gate voltege drain current	I_{DSS}	—	—	10	µA	V_{DS} = 30 V, V_{GS} = 0
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	—	2.0	V	V_{DS} = 10V, I_D = 1mA
Static drain to source on state	$R_{DS(on)}$	—	0.05	0.065	Ω	I_D = 3A, V_{GS} = 10V*[1]
resistance	$R_{DS(on)}$	—	0.078	0.11	Ω	I_D = 3A, V_{GS} = 4V*[1]
Forward transfer admittance	$\|y_{fs}\|$	3.5	5.5	—	S	I_D = 3A, V_{DS} = 10V*[1]
Input capacitance	Ciss	—	310	—	pF	V_{DS} = 10V
Output capacitance	Coss	—	220	—	pF	V_{GS} = 0
Reverse transfer capacitance	Crss	—	100	—	pF	f = 1MHz
Turn-on delay time	$t_{d(on)}$	—	17	—	ns	V_{GS} = 4V, I_D = 3A
Rise time	t_r	—	190	—	ns	V_{DD} ≅ 10V
Turn-off delay time	$t_{d(off)}$	—	25	—	ns	
Fall time	t_f	—	60	—	ns	
Body to drain diode forward voltage	V_{DF}	—	0.9	1.4	V	IF = 5.5A, V_{GS} = 0*[1]
Body to drain diode reverse recovery time	t_{rr}	—	50	—	ns	IF = 5.5A, V_{GS} = 0 diF/ dt =20A/µs

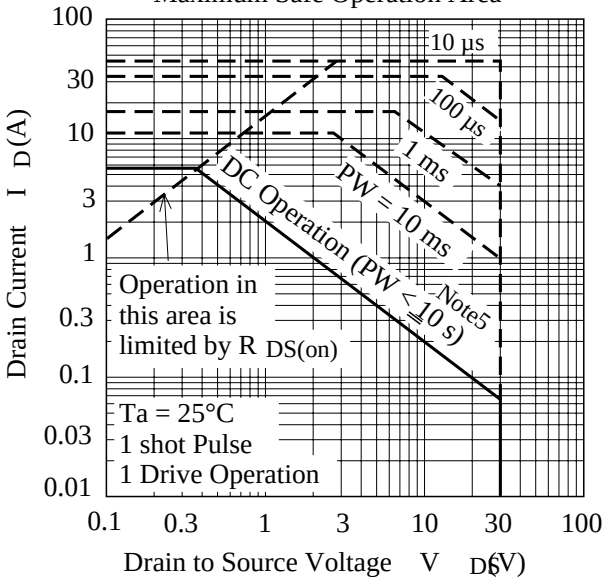
Note: 1. Pulse test

Main Characteristics

Power vs. Temperature Derating

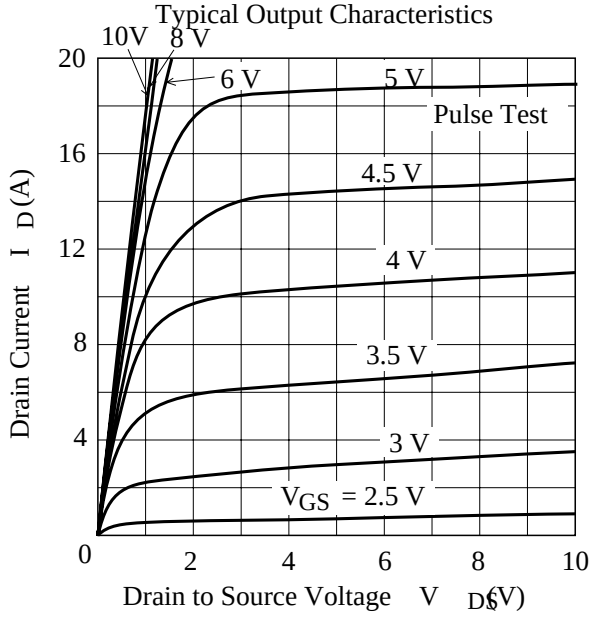


Maximum Safe Operation Area

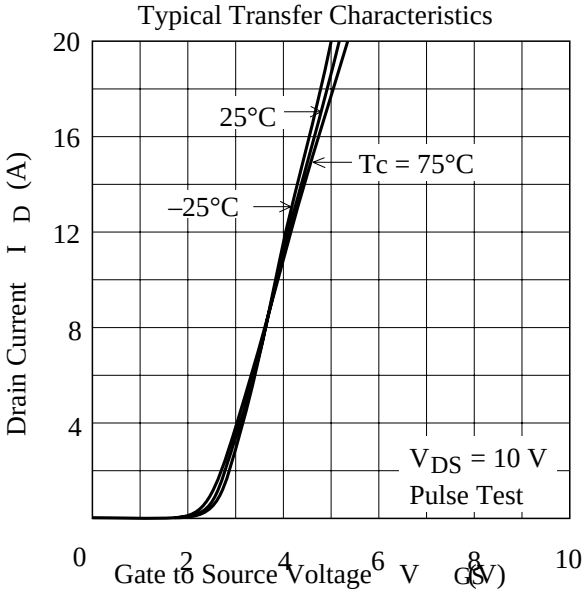


Note 5 :
When using the glass epoxy board
(FR4 40x40x1.6 mm)

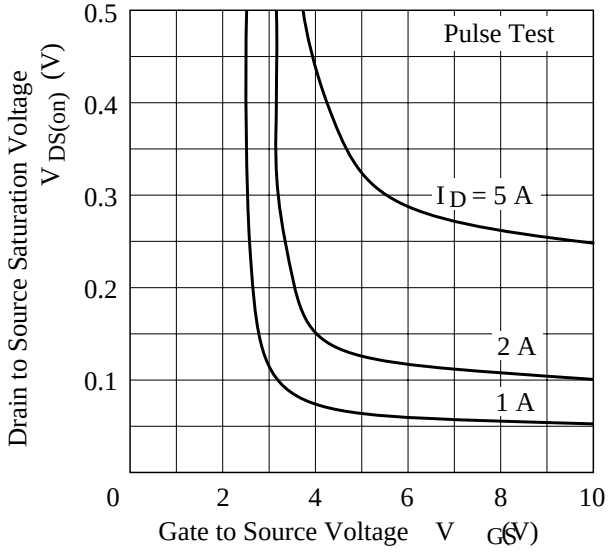
Typical Output Characteristics



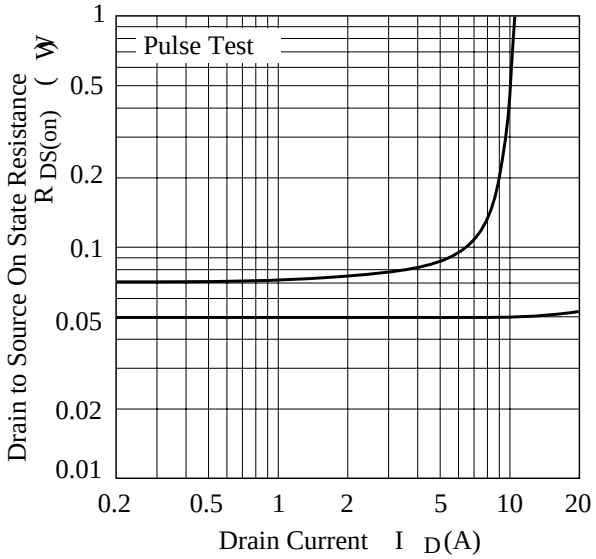
Typical Transfer Characteristics



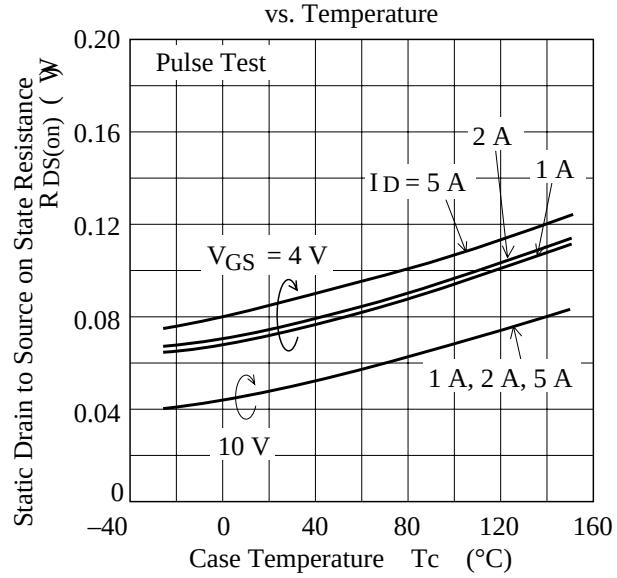
Drain to Source Saturation Voltage vs.
Gate to Source Voltage



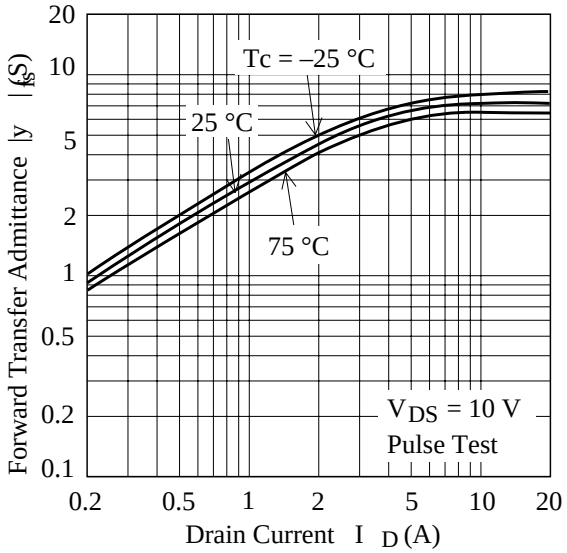
Static Drain to Source on State Resistance
vs. Drain Current



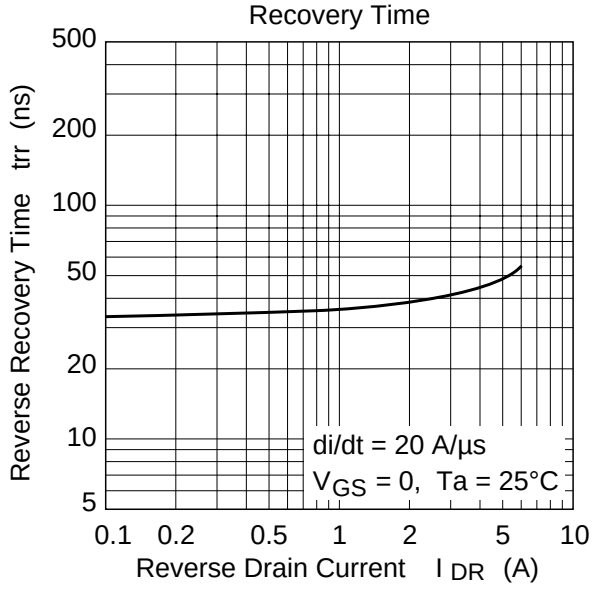
Static Drain to Source on State Resistance
vs. Temperature



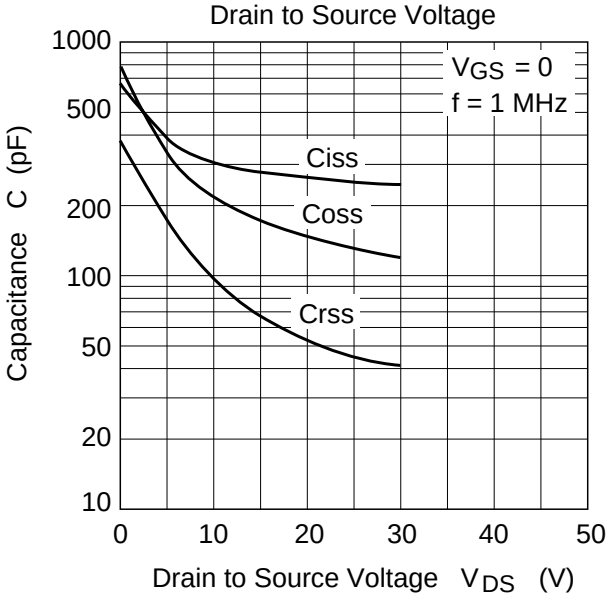
Forward Transfer Admittance vs.
Drain Current



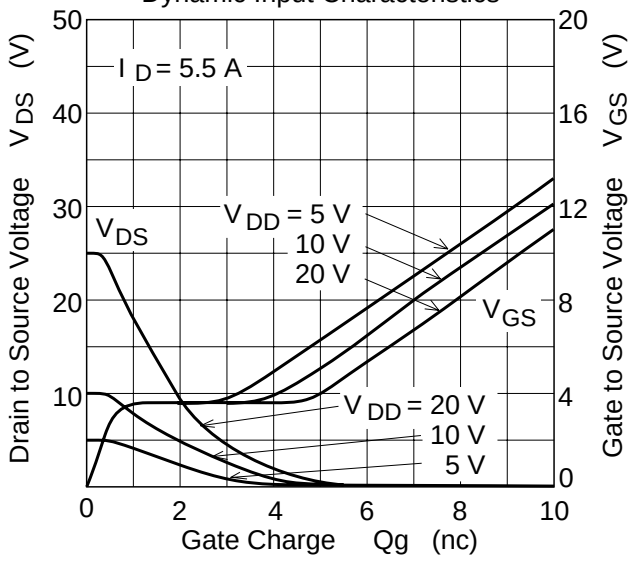
Body–Drain Diode Reverse Recovery Time



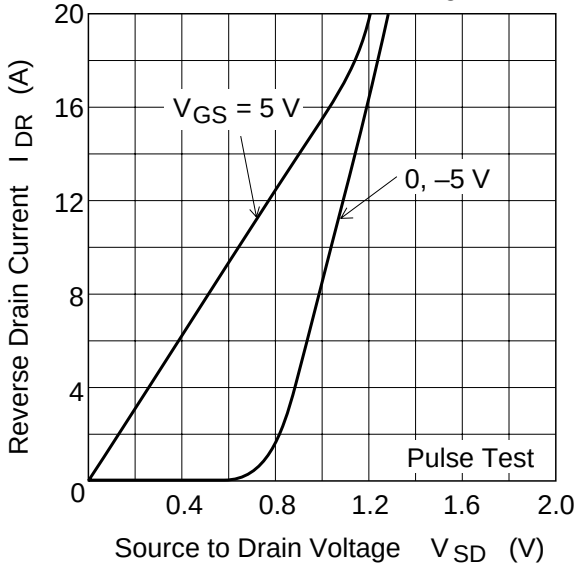
Typical Capacitance vs. Drain to Source Voltage



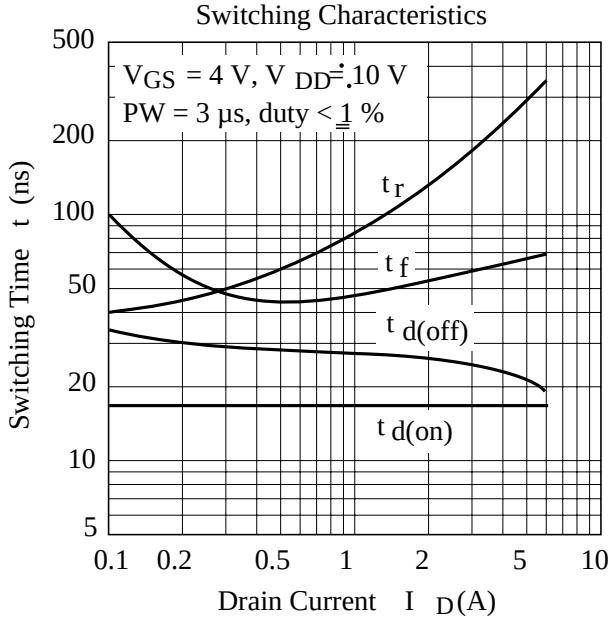
Dynamic Input Characteristics



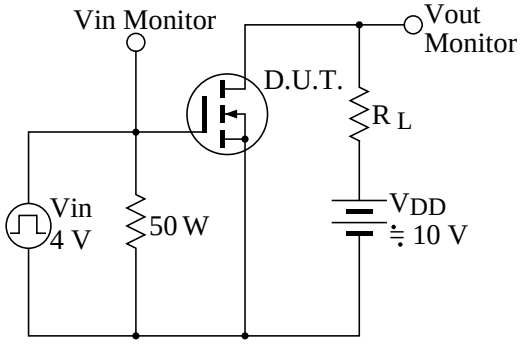
Reverse Drain Current vs. Souece to Drain Voltage



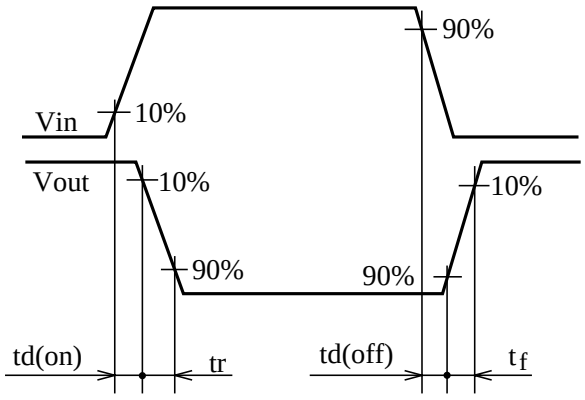
Switching Characteristics



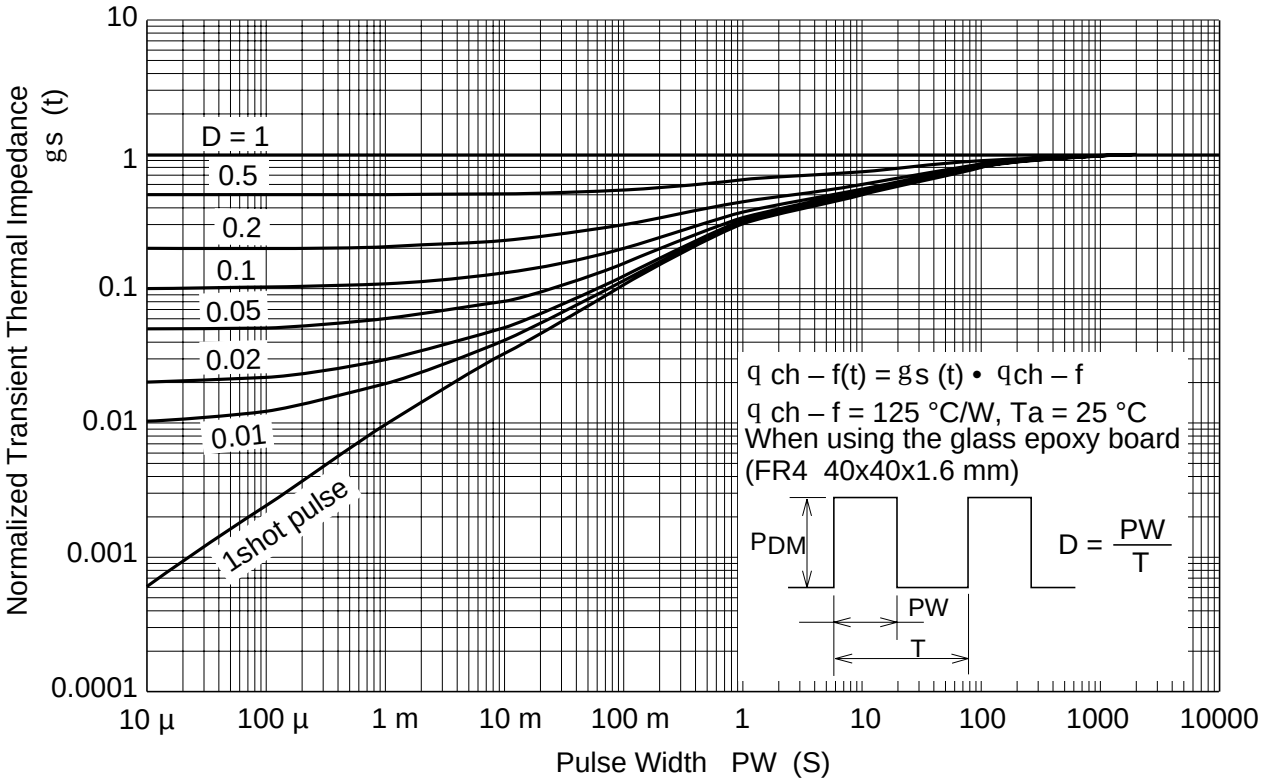
Switching Time Test Circuit



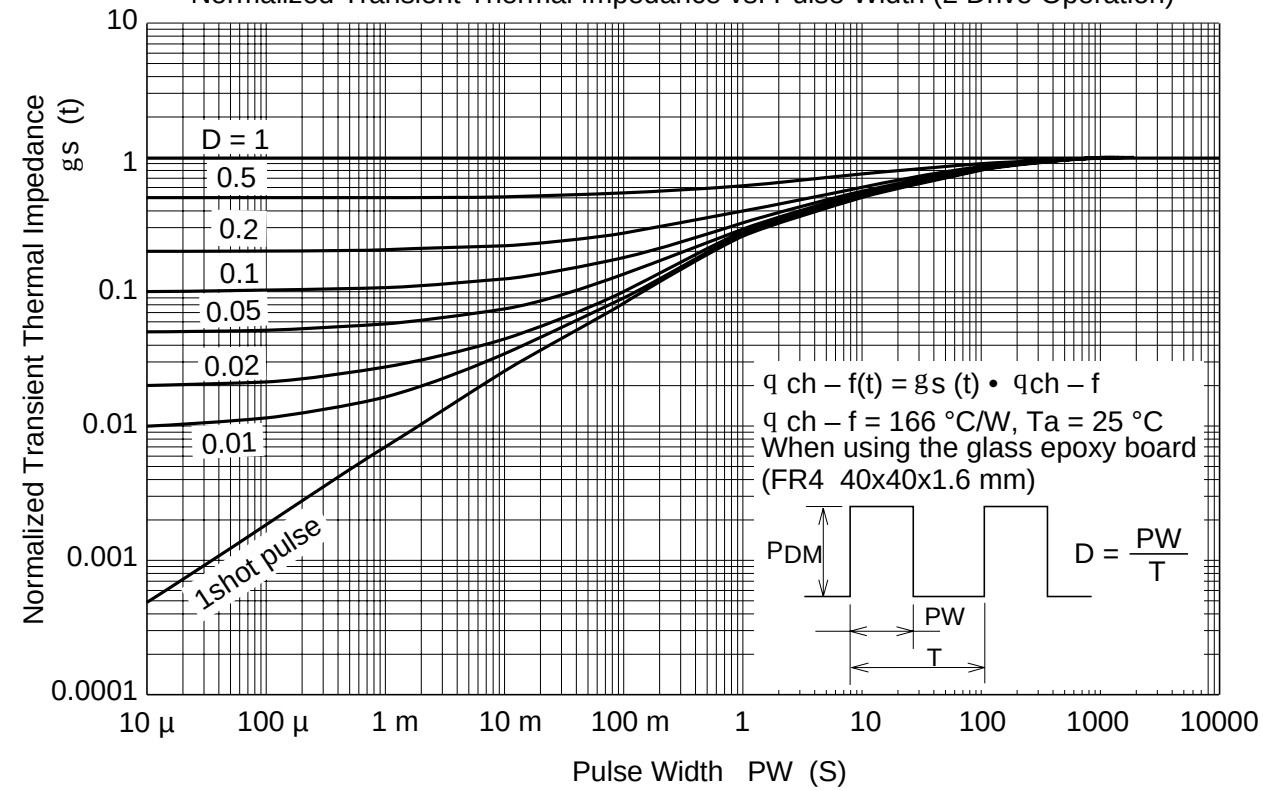
Switching Time Waveform



Normalized Transient Thermal Impedance vs. Pulse Width (1 Drive Operation)

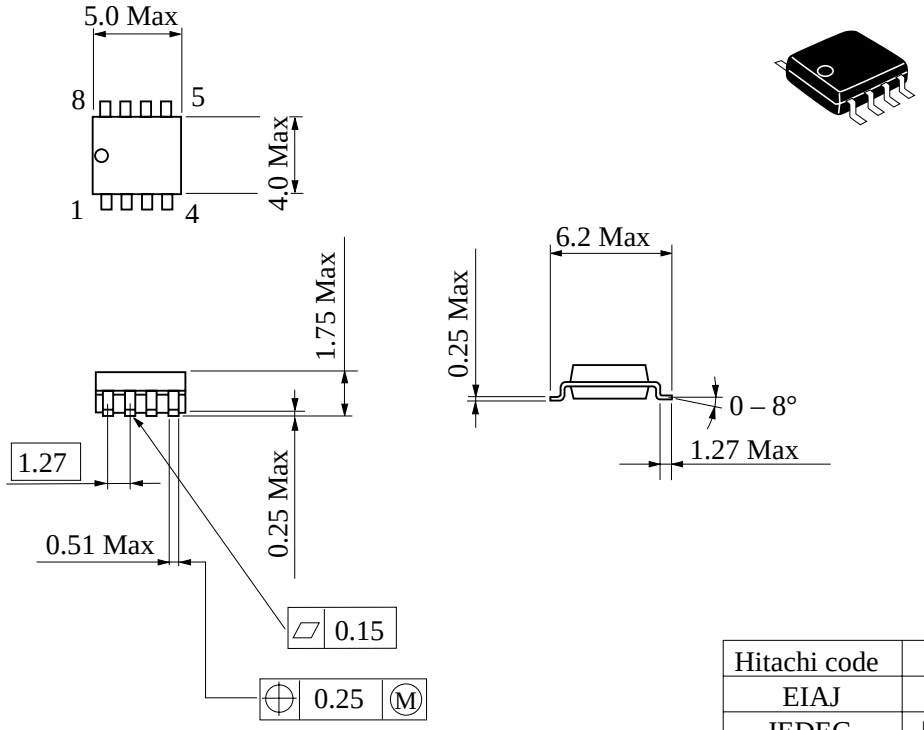


Normalized Transient Thermal Impedance vs. Pulse Width (2 Drive Operation)



Package Dimensions

Unit: mm



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