HVC321B

Variable Capacitance Diode for ET tuner

HITACHI

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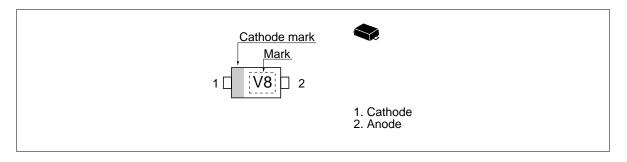
Features

- Low voltage type (tuning voltage 1 to 10V), it is suitable for ET without DC/DC converter.
- High capacitance ratio. (n = 10.5 min)
- Low series resistance. (rs = 1.0 Ω max) and good C-V linearity.
- <u>Ultra small Flat Package</u> (UFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HVC321B	V8	UFP

Outline





HVC321B

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V_R	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Electrical Characteristics ($Ta = 25^{\circ}C$)

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}	_	_	10	nA	V _R = 15V
	I _{R2}	_	_	100		V _R = 15V, Ta= 60°C
Capacitance	C ₁	29.0	_	33.0	pF	V _R = 1V, f = 1MHz
	C ₁₀	2.55	_	2.90		V _R = 10V, f = 1MHz
Capacitance ratio	n	10.5	_	_	_	C ₁ /C ₁₀
Series resistance	r _s	_	_	1.0	Ω	V _R = 5V, f = 470MHz
Matching error	ΔC/C*1	_	_	2.0	%	V _R = 1 to 10V, f = 1 MHz

Note 1. C.C system (Continuous Connected taping system) enable to make any 10 pcs of Δ C/C continuous in a reel , expect extention to another group. Calculate Matching Error,

$$\Delta \text{C/C=} \quad \frac{\text{(Cmax-Cmin)}}{\text{Cmin}} \quad \text{x 100 (\%)}$$

Main Characteristic

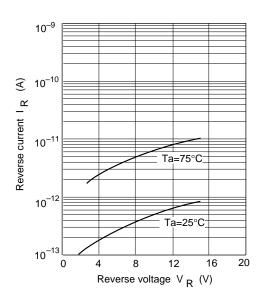


Fig.1 Reverse current Vs. Reverse voltage

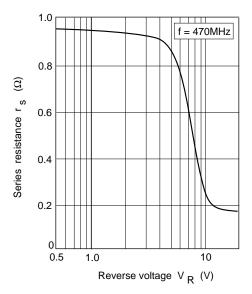


Fig.3 Series resistance Vs. Reverse voltage

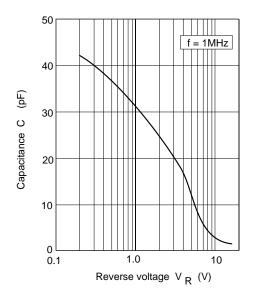
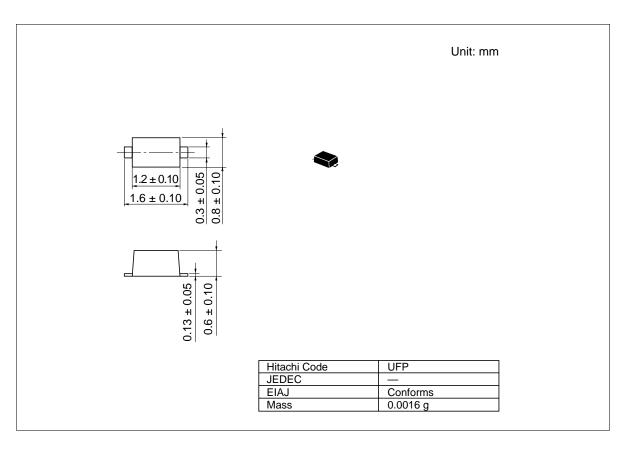


Fig.2 Capacitance Vs. Reverse voltage

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Package Dimensions



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Hitachi, Ltd.

Semiconductor & Integrated Circuits. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109 URL

NorthAmerica http:semiconductor.hitachi.com/ http://www.hitachi-eu.com/hel/ecg Europe Asia (Singapore)

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For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive, San Jose,CA 95134 Tel: <1> (408) 433-1990 Fax: <1>(408) 433-0223 Hitachi Europe GmbH Electronic components Group Dornacher Stra§e 3 D-85622 Feldkirchen, Munich Germany Tel: <49> (89) 9 9180-0 Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd. Electronic Components Group. Whitebrook Park Lower Cookham Road Maidenhead Berkshire SL6 8YA, United Kingdom Tel: <44> (1628) 585000

Fax: <44> (1628) 778322

Hitachi Tower Singapore 049318 Tel: 535-2100 Fax: 535-1533 Hitachi Asia Ltd. Taipei Branch Office

Hitachi Asia Pte. Ltd.

16 Collyer Quay #20-00

3F, Hung Kuo Building. No.167, Tun-Hwa North Road, Taipei (105) Tel: <886> (2) 2718-3666 Fax: <886> (2) 2718-8180

Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Tsim Sha Tsui, Kowloon, Hong Kong Tel: <852> (2) 735 9218 Fax: <852> (2) 730 0281

Telex: 40815 HITEC HX

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