

HVM27WK

Variable Capacitance Diode for FM tuner

HITACHI

ADE-208-060C (Z)

Rev. 3

Apl. 1993

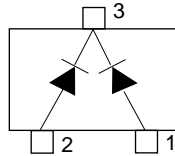
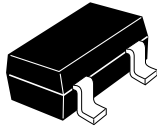
Features

- High capacitance ratio to wide tuning band width. ($C_1/C_8 = 1.8\text{min}$)
- Low series resistance.
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HVM27WK	T5	MPAK

Pin Arrangement



(Top View)

- 1 Anode
- 2 Anode
- 3 Cathode

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Item	Symbol	Value	Unit
Reverse voltage	V_R	20	V
Junction temperature	T_j	125	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +125	$^\circ\text{C}$

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse voltage	V_R	20	—	—	V	$I_R = 10\mu\text{A}$
Reverse current	I_R	—	—	50	nA	$V_R = 15\text{V}$
Capacitance	C_1	52.0	—	62.0	pF	$V_R = 1\text{V}, f = 1\text{MHz}$
	C_2	43.0	—	48.1		$V_R = 2\text{V}, f = 1\text{MHz}$
	C_8	24.0	—	28.0		$V_R = 8\text{V}, f = 1\text{MHz}$
Capacitance ratio	n_1	1.80	—	—	—	C_1/C_8
	n_2	1.70	—	—	—	C_2/C_8
Series resistance	r_s	—	—	0.4	Ω	$V_R = 2\text{V}, f = 100\text{MHz}$
Matching error	$\Delta C/C^{*1}$	—	—	3.0	%	$V_R = 1$ to 8V

Notes: 1. A set of HVM27WK is of uniform C-V characteristics.

Measure max. value and min. value of capacitance at each bias point of $V_R = 1\text{V}$ through 8V .

Calculate Matching Error, $\Delta C/C = \frac{(C_{\text{max}} - C_{\text{min}})}{C_{\text{min}}} \times 100 (\%)$

2. Each group shall uniform a multiple of 4 diodes.

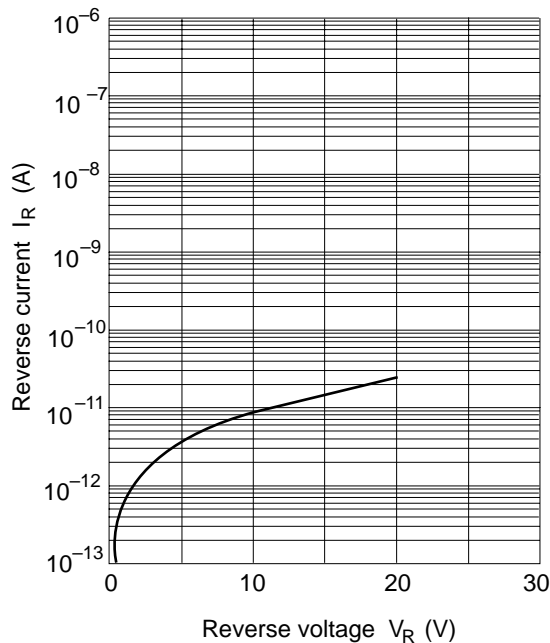


Fig.1 Reverse current Vs. Reverse voltage

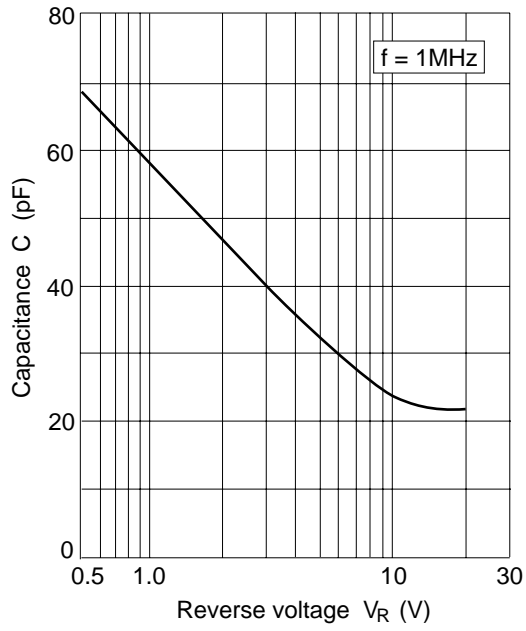
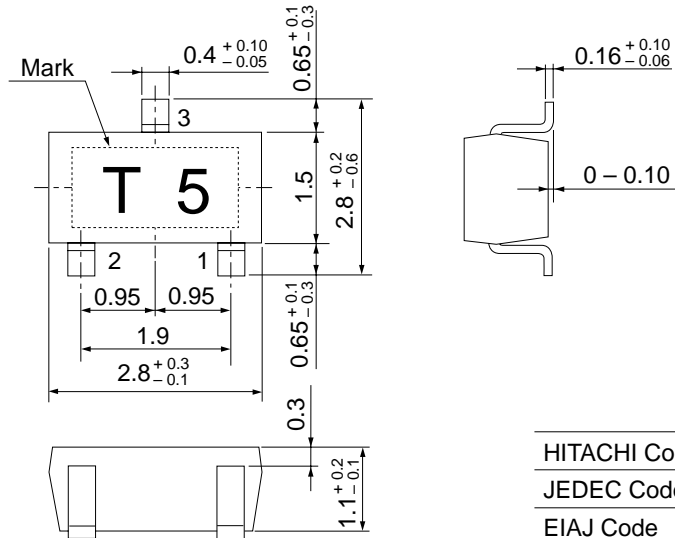


Fig.2 Capacitance Vs. Reverse voltage

Package Dimensions

Unit: mm



- 1 Anode
- 2 Anode
- 3 Cathode

HITACHI Code	MPAK(1)
JEDEC Code	—
EIAJ Code	SC-59A
Weight (g)	0.011

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HITACHI

Hitachi, Ltd.

Semiconductor & Integrated Circuits.
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL North America : <http://semiconductor.hitachi.com/>
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For further information write to:

Hitachi Semiconductor
(America) Inc.
179 East Tasman Drive,
San Jose, CA 95134
Tel: <1> (408) 433-1990
Fax: <1> (408) 433-0223

Hitachi Europe GmbH
Electronic components Group
Dornacher Straße 3
D-85622 Feldkirchen, Munich
Germany
Tel: <49> (89) 9 9180-0
Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd.
Electronic Components Group.
Whitebrook Park
Lower Cookham Road
Maidenhead
Berkshire SL6 8YA, United Kingdom
Tel: <44> (1628) 585000
Fax: <44> (1628) 778322

Hitachi Asia Pte. Ltd.
16 Collyer Quay #20-00
Hitachi Tower
Singapore 049318
Tel: 535-2100
Fax: 535-1533

Hitachi Asia Ltd.
Taipei Branch Office
3F, Hung Kuo Building, No.167,
Tun-Hwa North Road, Taipei (105)
Tel: <886> (2) 2718-3666
Fax: <886> (2) 2718-8180

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower, World Finance Centre,
Harbour City, Canton Road, Tsim Sha Tsui,
Kowloon, Hong Kong
Tel: <852> (2) 735 9218
Fax: <852> (2) 730 0281
Telex: 40815 HITEC HX

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