

Data Sheet January 2000 File Number 4792

8A, 1200V Ultrafast Dual Diode

The RURP8120CC is an ultrafast dual diode with soft recovery characteristics (t_{rr} < 100ns). It has low forward voltage drop and is of silicon nitride passivated ionimplanted epitaxial planar construction.

This device is intended for use as a freewheeling/clamping diode and rectifier in a variety of switching power supplies and other power switching applications. Its low stored charge and ultrafast soft recovery minimizes ringing and electrical noise in many power switching circuits, thus reducing power loss in the switching transistors.

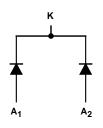
Formerly development type TA49095.

Ordering Information

PART NUMBER	PACKAGE	BRAND
RURP8120CC	TO-220AB	RUR8120C

NOTE: When ordering, use the entire part number.

Symbol



Features

•	Ultrafast with Soft Recovery <100ns
•	Operating Temperature
•	Reverse Voltage

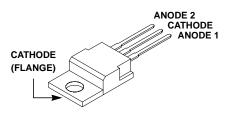
- Avalanche Energy Rated
- Planar Construction

Applications

- · Switching Power Supplies
- Power Switching Circuits
- · General Purpose

Packaging

JEDEC TO-220AB



Absolute Maximum Ratings (Per Leg) T_C = 25°C, Unless Otherwise Specified

	RURP8120CC	UNITS
Peak Repetitive Reverse VoltageV _{RRM}	1200	V
Working Peak Reverse Voltage	1200	V
DC Blocking VoltageV _R	1200	V
Average Rectified Forward Current $I_{F(AV)}$ ($T_C = 144^{\circ}C$)	8	Α
Repetitive Peak Surge Current	16	Α
Nonrepetitive Peak Surge Current	100	Α
Maximum Power Dissipation	75	W
Avalanche Energy (See Figure 10 and Figure 11)EAVL	20	mJ
Operating and Storage Temperature	-65 to 175	οС

Electrical Specifications (Per Leg) T_C = 25°C, Unless Otherwise Specified

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
V _F	I _F = 8A	-	-	2.1	V
	$I_F = 8A, T_C = 150^{\circ}C$	-	-	1.9	V
I _R	V _R = 1200V	-	-	100	μΑ
	V _R = 1200V, T _C = 150°C	-	-	500	μΑ
t _{rr}	I _F = 1A, dI _F /dt = 100A/μs	-	-	100	ns
	$I_F = 8A$, $dI_F/dt = 100A/\mu s$	-	-	110	ns
t _a	$I_F = 8A$, $dI_F/dt = 100A/\mu s$	-	60	-	ns
t _b	$I_F = 8A$, $dI_F/dt = 100A/\mu s$	-	40	-	ns
Q _{RR}	I _F = 8A, dI _F /dt = 100A/μs	-	380	-	nC
CJ	V _R = 10V, I _F = 0A	-	25	-	pF
$R_{ heta JC}$		-	-	2	°C/W

DEFINITIONS

 V_F = Instantaneous forward voltage (pw = 300 μ s, D = 2%).

 I_R = Instantaneous reverse current.

 t_{rr} = Reverse recovery time (See Figure 9), summation of $t_a + t_b$.

 t_a = Time to reach peak reverse current (See Figure 9).

 t_b = Time from peak I_{RM} to projected zero crossing of I_{RM} based on a straight line from peak I_{RM} through 25% of I_{RM} (See Figure 9).

 $R_{\theta,JC}$ = Thermal resistance junction to case.

Q_{RR} = Reverse Recovery Charge.

 C_J = Junction Capacitance.

pw = pulse width.

D = duty cycle.

Typical Performance Curves

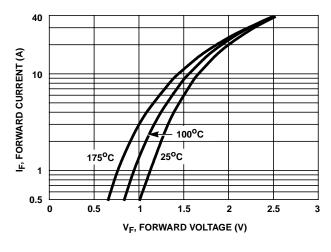


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

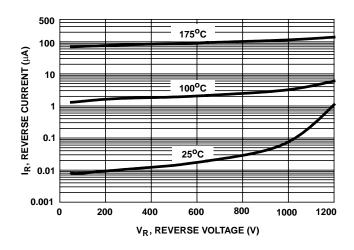


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

Typical Performance Curves (Continued)

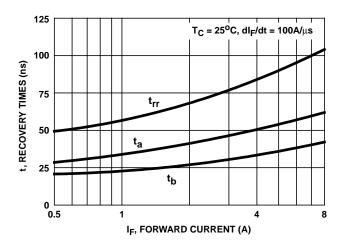


FIGURE 3. $t_{\rm rr}, t_{\rm a}$ and $t_{\rm b}$ curves vs forward current

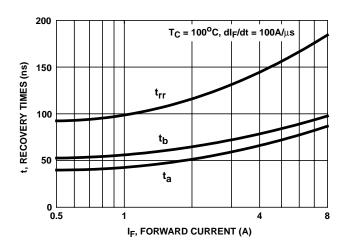


FIGURE 4. t_{rr}, t_a AND t_b CURVES vs FORWARD CURRENT

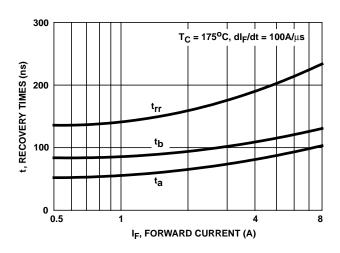


FIGURE 5. t_{rr} , t_a AND t_b CURVES vs FORWARD CURRENT

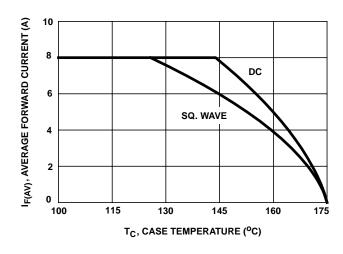


FIGURE 6. CURRENT DERATING CURVE

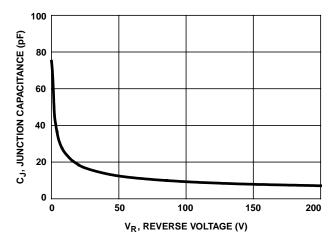


FIGURE 7. JUNCTION CAPACITANCE vs REVERSE VOLTAGE

Test Circuits and Waveforms

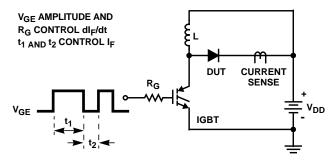


FIGURE 8. t_{rr} TEST CIRCUIT

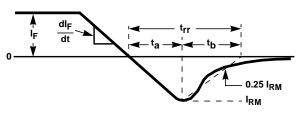


FIGURE 9. t_{rr} WAVEFORMS AND DEFINITIONS

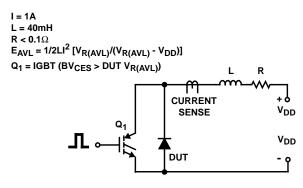


FIGURE 10. AVALANCHE ENERGY TEST CIRCUIT

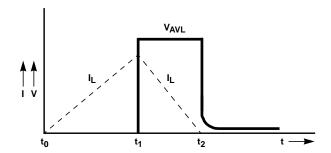


FIGURE 11. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

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