

## REPETITIVE AVALANCHE AND $dv/dt$ RATED HEXFET<sup>®</sup> TRANSISTOR

## IRHI7460SE

### N-CHANNEL SINGLE EVENT EFFECT (SEE) RAD HARD

#### 500 Volt, 0.32 $\Omega$ , (SEE) RAD HARD HEXFET

International Rectifier's (SEE) RAD HARD technology HEXFETs demonstrate virtual immunity to SEE failure. Additionally, under **identical** pre- and post-radiation test conditions, International Rectifier's RAD HARD HEXFETs retain **identical** electrical specifications up to  $1 \times 10^5$  Rads (Si) total dose. No compensation in gate drive circuitry is required. These devices are also capable of surviving transient ionization pulses as high as  $1 \times 10^{12}$  Rads (Si)/Sec, and return to normal operation within a few microseconds. Since the SEE process utilizes International Rectifier's patented HEXFET technology, the user can expect the highest quality and reliability in the industry.

RAD HARD HEXFET transistors also feature all of the well-established advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and temperature stability of the electrical parameters.

They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers and high-energy pulse circuits in space and weapons environments.

#### Product Summary

| Part Number | BV <sub>DSS</sub> | R <sub>DS(on)</sub> | I <sub>D</sub> |
|-------------|-------------------|---------------------|----------------|
| IRHI7460SE  | 500V              | 0.32 $\Omega$       | 20A            |

#### Features:

- Radiation Hardened up to  $1 \times 10^5$  Rads (Si)
- Single Event Burnout (SEB) Hardened
- Single Event Gate Rupture (SEGR) Hardened
- Gamma Dot (Flash X-Ray) Hardened
- Neutron Tolerant
- Identical Pre- and Post-Electrical Test Conditions
- Repetitive Avalanche Rating
- Dynamic  $dv/dt$  Rating
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Electrically Isolated
- Ceramic Eyelets

#### Absolute Maximum Ratings

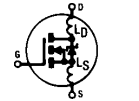
#### Pre-Radiation

|  | Parameter                       | IRHI7460SE                                    | Units |
|--|---------------------------------|---|-------|
| I <sub>D</sub> @ V <sub>GS</sub> = 12V, T <sub>C</sub> = 25°C  | Continuous Drain Current        | 20  | A     |
| I <sub>D</sub> @ V <sub>GS</sub> = 12V, T <sub>C</sub> = 100°C | Continuous Drain Current        | 12  |       |
| I <sub>DM</sub>  | Pulsed Drain Current ①          | 80  |       |
| P <sub>D</sub> @ T <sub>C</sub> = 25°C                         | Max. Power Dissipation          | 300   | W     |
|  | Linear Derating Factor          | 2.4   | W/K ⑤ |
| V <sub>GS</sub>  | Gate-to-Source Voltage          | ±20   | V     |
| EAS  | Single Pulse Avalanche Energy ② | 500   | mJ    |
| I <sub>AR</sub>  | Avalanche Current ①             | 20  | A     |
| EAR  | Repetitive Avalanche Energy ①   | 30  | mJ    |
| dv/dt  | Peak Diode Recovery dv/dt ③     | 3.5   | V/ns  |
| T <sub>J</sub>   | Operating Junction              | -55 to 150                                    | °C    |
| T <sub>STG</sub>   | Storage Temperature Range       |   |       |
|  | Lead Temperature                | 300 (0.063 in. (1.6mm) from case for 10 sec.) |       |
|  | Weight                          | 10.9 (typical)                                | g     |

Electrical Characteristics @ T<sub>j</sub> = 25°C (Unless Otherwise Specified)

|                                     | Parameter                                    | Min. | Typ. | Max. | Units  | Test Conditions  |
|-------------------------------------|--|------|------|------|--------|--|
| BV <sub>DSS</sub>                   | Drain-to-Source Breakdown Voltage            | 500  | —    | —    | V      | V <sub>GS</sub> = 0V, I <sub>D</sub> = 1.0 mA                                      |
| ΔBV <sub>DSS</sub> /ΔT <sub>J</sub> | Temperature Coefficient of Breakdown Voltage | —    | 0.68 | —    | V/°C   | Reference to 25°C, I <sub>D</sub> = 1.0 mA   |
| RDS(on)                             | Static Drain-to-Source                       | —    | —    | 0.32 | Ω      | V <sub>GS</sub> = 12V, I <sub>D</sub> = 12A  |
|                                     | On-State Resistance                          | —    | —    | 0.36 |        | V <sub>GS</sub> = 12V, I <sub>D</sub> = 20A <sup>④</sup>                           |
| VGS(th)                             | Gate Threshold Voltage                       | 2.5  | —    | 4.5  | V      | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 1.0 mA                        |
| g <sub>fs</sub>                     | Forward Transconductance                     | 3.5  | —    | —    | S (r̄) | V <sub>DS</sub> > 15V, I <sub>DS</sub> = 12A <sup>④</sup>                          |
| IDSS                                | Zero Gate Voltage Drain Current              | —    | —    | 50   | μA     | V <sub>DS</sub> = 0.8 x Max Rating, V <sub>GS</sub> = 0V                           |
|                                     |  | —    | —    | 250  |        | V <sub>DS</sub> = 0.8 x Max Rating<br>V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C |
| IGSS                                | Gate-to-Source Leakage Forward               | —    | —    | 100  | nA     | V <sub>GS</sub> = 20V  |
| IGSS                                | Gate-to-Source Leakage Reverse               | —    | —    | -100 | nA     | V <sub>GS</sub> = -20V   |
| Q <sub>g</sub>                      | Total Gate Charge                            | —    | —    | 260  | nC     | V <sub>GS</sub> = 12V, I <sub>D</sub> = 20A  |
| Q <sub>gs</sub>                     | Gate-to-Source Charge                        | —    | —    | 40   |        | V <sub>DS</sub> = Max. Rating x 0.5  |
| Q <sub>gd</sub>                     | Gate-to-Drain ("Miller") Charge              | —    | —    | 200  |        |  |
| t <sub>d(on)</sub>                  | Turn-On Delay Time                           | —    | —    | 45   | ns     | V <sub>DD</sub> = 250V, I <sub>D</sub> = 20A,<br>R <sub>G</sub> = 2.35Ω            |
| t <sub>r</sub>                      | Rise Time                                    | —    | —    | 140  |        |  |
| t <sub>d(off)</sub>                 | Turn-Off Delay Time                          | —    | —    | 140  |        |  |
| t <sub>f</sub>                      | Fall Time                                    | —    | —    | 110  |        |  |
| LD                                  | Internal Drain Inductance                    | —    | 8.7  | —    | nH     | Measured from the drain lead, 6mm (0.25 in.) from package to center of die.        |
| LS                                  | Internal Source Inductance                   | —    | 8.7  | —    |        | Measured from the source lead, 6mm (0.25 in.) from package to source bonding pad.  |
| C <sub>iss</sub>                    | Input Capacitance                            | —    | 6400 | —    | pF     | V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V<br>f = 1.0 MHz                         |
| C <sub>oss</sub>                    | Output Capacitance                           | —    | 1100 | —    |        |  |
| C <sub>rss</sub>                    | Reverse Transfer Capacitance                 | —    | 375  | —    |        |  |

Modified MOSFET symbol showing the internal inductances.



## Source-Drain Diode Ratings and Characteristics

|                 | Parameter                                      | Min.   | Typ. | Max. | Units | Test Conditions  |
|-----------------|--|--|------|------|-------|--|
| I <sub>S</sub>  | Continuous Source Current (Body Diode)         | —  | —    | 20   | A     | Modified MOSFET symbol showing the integral reverse p-n junction rectifier.                        |
| I <sub>SM</sub> | Pulse Source Current (Body Diode) <sup>①</sup> | —  | —    | 80   |       |  |
| V <sub>SD</sub> | Diode Forward Voltage                          | —  | —    | 1.8  | V     | T <sub>j</sub> = 25°C, I <sub>S</sub> = 20A, V <sub>GS</sub> = 0V <sup>④</sup>                     |
| t <sub>rr</sub> | Reverse Recovery Time                          | —  | —    | 1200 | ns    | T <sub>j</sub> = 25°C, I <sub>F</sub> = 20A, di/dt ≤ 100A/μs<br>V <sub>DD</sub> ≤ 50V <sup>④</sup> |
| Q <sub>RR</sub> | Reverse Recovery Charge                        | —  | —    | 16   | μC    |  |
| t <sub>on</sub> | Forward Turn-On Time                           | Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by L <sub>S</sub> + L <sub>D</sub> . |      |      |       |  |

## Thermal Resistance

|                   | Parameter           | Min. | Typ. | Max. | Units            | Test Conditions      |
|-------------------|---------------------|------|------|------|------------------|----------------------|
| R <sub>thJC</sub> | Junction-to-Case    | —    | —    | 0.42 | K/W <sup>⑤</sup> |                      |
| R <sub>thJA</sub> | Junction-to-Ambient | —    | —    | 30   |                  |                      |
| R <sub>thCS</sub> | Case-to-Sink        | —    | 0.21 | —    |                  | Typical socket mount |

**Radiation Performance of Rad Hard HEXFETs**

International Rectifier Radiation Hardened HEXFETs are tested to verify their hardness capability. The hardness assurance program at International Rectifier uses two radiation environments.

Every manufacturing lot is tested in a low dose rate (total dose) environment per MIL-STD-750, test method 1019. International Rectifier has imposed a standard gate voltage of 12 volts per note 6 and a  $V_{DSS}$  bias condition equal to 80% of the device rated voltage per note 7. Pre- and post-radiation limits of the devices irradiated to  $1 \times 10^5$  Rads (Si) are identical and are presented in Table 1. The values in Table 1 will be met for either of the two low dose rate test circuits that are used.

Both pre- and post-radiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison. It should be noted that at a radiation level of  $1 \times 10^5$  Rads (Si), no change in limits are specified in DC parameters.

High dose rate testing may be done on a special request basis, using a dose rate up to  $1 \times 10^{12}$  Rads (Si)/Sec.

International Rectifier radiation hardened HEXFETs have been characterized in neutron and heavy ion Single Event Effects (SEE) environments. Single Event Effects characterization is shown in Table 3.

**Table 1. Low Dose Rate** ⑥ ⑦

| Parameter     |   | IRHI7460SE          |      | Units    | Test Conditions ⑩                                    |
|---------------|---|---------------------|------|----------|--|
|               |   | 100K Rads (Si) min. | max. |          |  |
| $BV_{DSS}$    | Drain-to-Source Breakdown Voltage                   | 500                 | —    | V        | $V_{GS} = 0V, I_D = 1.0 \text{ mA}$                  |
| $V_{GS(th)}$  | Gate Threshold Voltage ④                            | 2.5                 | 4.5  |          | $V_{GS} = V_{DS}, I_D = 1.0 \text{ mA}$              |
| $I_{GSS}$     | Gate-to-Source Leakage Forward                      | —                   | 100  | nA       | $V_{GS} = 20V$                                       |
| $I_{GSS}$     | Gate-to-Source Leakage Reverse                      | —                   | -100 |          | $V_{GS} = -20V$                                      |
| $I_{DSS}$     | Zero Gate Voltage Drain Current                     | —                   | 50   | $\mu A$  | $V_{DS} = 0.8 \times \text{Max Rating}, V_{GS} = 0V$ |
| $R_{DS(on)1}$ | Static Drain-to-Source ④<br>On-State Resistance One | —                   | 0.32 | $\Omega$ | $V_{GS} = 12V, I_D = 12A$                            |
| $V_{SD}$      | Diode Forward Voltage ④                             | —                   | 1.8  | V        | $T_C = 25^\circ C, I_S = 20A, V_{GS} = 0V$           |

**Table 2. High Dose Rate** ⑧

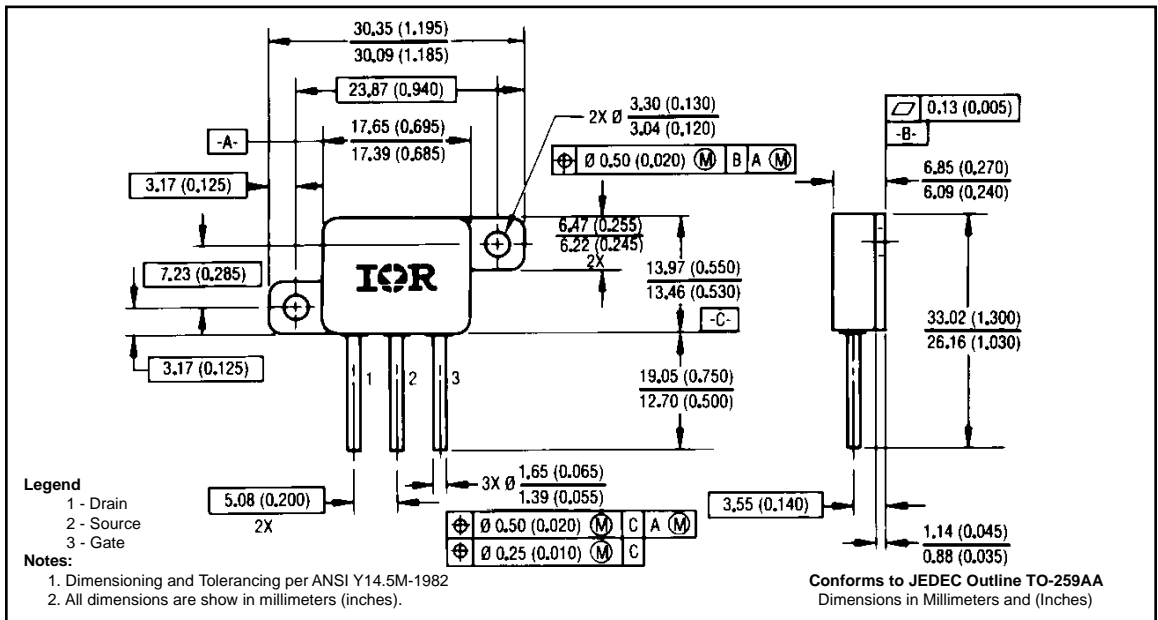
| Parameter |                         | 10 <sup>11</sup> Rads (Si)/sec |     |      | 10 <sup>12</sup> Rads (Si)/sec |      |      | Units        | Test Conditions                                  |
|-----------|-------------------------|--------------------------------|-----|------|--------------------------------|------|------|--------------|--|
|           |                         | Min.                           | Typ | Max. | Min.                           | Typ. | Max. |              |  |
| $V_{DSS}$ | Drain-to-Source Voltage | —                              | —   | 400  | —                              | —    | 400  | V            | Applied drain-to-source voltage during gamma-dot |
| $I_{PP}$  |                         | —                              | 7   | —    | —                              | 7    | —    | A            | Peak radiation induced photo-current             |
| di/dt     |                         | —                              | 16  | —    | —                              | 2.3  | —    | A/ $\mu$ sec | Rate of rise of photo-current                    |
| $L_1$     |                         | —                              | 27  | —    | —                              | 133  | —    | $\mu H$      | Circuit inductance required to limit di/dt       |

**Table 3. Single Event Effects** ⑨

| Parameter  | Typ. | Units | Ion | LET (Si)<br>(MeV/mg/cm <sup>2</sup> ) | Fluence<br>(ions/cm <sup>2</sup> ) | Range<br>( $\mu m$ ) | $V_{DS}$ Bias<br>(V) | $V_{GS}$ Bias<br>(V) |
|------------|------|-------|-----|---------------------------------------|------------------------------------|----------------------|----------------------|----------------------|
| $BV_{DSS}$ | 500  | V     | Ni  | 28                                    | $1 \times 10^6$                    | ~35                  | 400                  | -5                   |

- ① Repetitive Rating; Pulse width limited by maximum junction temperature. Refer to current HEXFET reliability report.
- ② @  $V_{DD} = 50V$ , Starting  $T_J = 25^\circ C$ ,  
 $E_{AS} = [0.5 * L * (I_L^2) * [BV_{DSS}/(BV_{DSS}-V_{DD})]]$   
 Peak  $I_L = 20A$ ,  $V_{GS} = 12V$ ,  $25 \leq R_G \leq 200\Omega$
- ③  $I_{SD} \leq 20A$ ,  $di/dt \leq 170 A/\mu s$ ,  
 $V_{DD} \leq BV_{DSS}$ ,  $T_J \leq 150^\circ C$   
 Suggested  $R_G = 2.35\Omega$
- ④ Pulse width  $\leq 300 \mu s$ ; Duty Cycle  $\leq 2\%$
- ⑤  $K/W = ^\circ C/W$   
 $W/K = W/^\circ C$
- ⑥ **Total Dose Irradiation with  $V_{GS}$  Bias.**  
 12 volt  $V_{GS}$  applied and  $V_{DS} = 0$  during irradiation per MIL-STD-750, method 1019.
- ⑦ **Total Dose Irradiation with  $V_{DS}$  Bias.**  
 $V_{DS} = 0.8$  rated  $BV_{DSS}$  (pre-radiation) applied and  $V_{GS} = 0$  during irradiation per MIL-STD-750, method 1019.
- ⑧ This test is performed using a flash x-ray source operated in the e-beam mode (energy ~2.5 MeV), 30 nsec pulse.
- ⑨ Process characterized by independent laboratory.
- ⑩ All Pre-Radiation and Post-Radiation test conditions are **identical** to facilitate direct comparison for circuit applications.

Case Outline and Dimensions — TO-259AA



**CAUTION**

**BERYLLIA WARNING PER MIL-PRF-19500**

Packages containing beryllia shall not be ground, sandblasted, machined, or have other operations performed on them which will produce beryllia or beryllium dust. Furthermore, beryllium oxides packages shall not be placed in acids that will produce fumes containing beryllium.

International  
**IR** Rectifier

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