



Micro Commercial Components  
 21201 Itasca Street Chatsworth  
 CA 91311  
 Phone: (818) 701-4933  
 Fax: (818) 701-4939

# LLSD103A THRU LLSD103C

## Features

- Low Reverse Recovery Time
- Low Reverse Capacitance
- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection

## Schottky Barrier Switching Diode

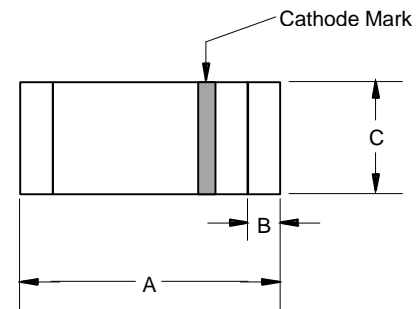
## Mechanical Data

- Case: MiniMELF, Glass
- Terminals: Solderable per MIL-STD-202, Method 208
- Polarity: Indicated by Cathode Band
- Weight: 0.05 grams ( approx.)

## MINIMELF

### Maximum Ratings @ 25°C Unless Otherwise Specified

Characteristic	Symbol	LLSD103A	LLSD103B	LLSD103C
Peak Repetitive Reverse Voltage	$V_{RRM}$			
Working Peak Reverse Voltage	$V_{RWM}$	40V	30V	20V
DC Blocking Voltage	$V_R$			
RMS Reverse Voltage	$V_{R(RMS)}$	28V	21V	14V
Forward Continuous Current(Note1)	$I_{FM}$	350mA		
Maximum Single cycle surge 60Hz sine wave	$I_{FSM}$	15A		
Power Dissipation(Note 1)	$P_d$	400mW		
Thermal Resistance(Note 1)	R	250K/W		
Operation/Storage Temp. Range	$T_j, T_{STG}$	-55 to 150 °C		

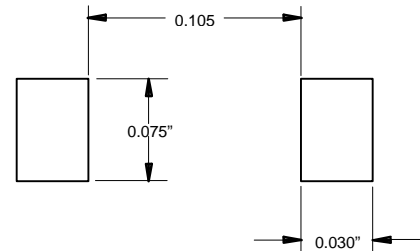


DIM	DIMENSION				NOTE
	INCHES		MM		
	MIN	MAX	MIN	MAX	
A	.134	.142	3.40	3.60	
B	.008	.016	0.20	0.40	
C	.055	.059	1.40	1.50	

### Electrical Characteristics @ 25°C Unless Otherwise Specified

Charateristic	Symbol	Min	Typ	Max	Test Cond.
Peak Reverse Current	$I_{RM}$	-----	-----	5.0uA	$V_R=30V$ $V_R=20V$ $V_R=10V$
Maximum Forward Voltage Drop	$V_{FM}$	-----	-----	0.37V 0.60V	$I_F=20mA$ $I_F=200mA$
Junction Capacitance	$C_j$	-----	50	pF	$V_R=0V, f=1.0MHz$
Reverse Recovery Time	$t_{rr}$	-----	10	ns	$I_F=I_R=50mA$ to 200mA, recover to 0.1 $I_R$

### SUGGESTED SOLDER PAD LAYOUT



**Note:** 1. Valid provided that electrodes are kept at ambient temperature

# LLSD103A thru LLSD103C

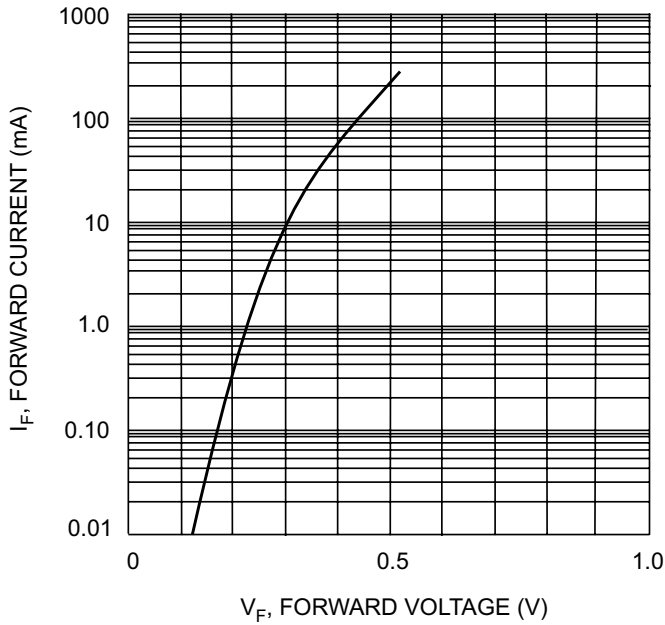


Fig. 1 Typical Forward Characteristics

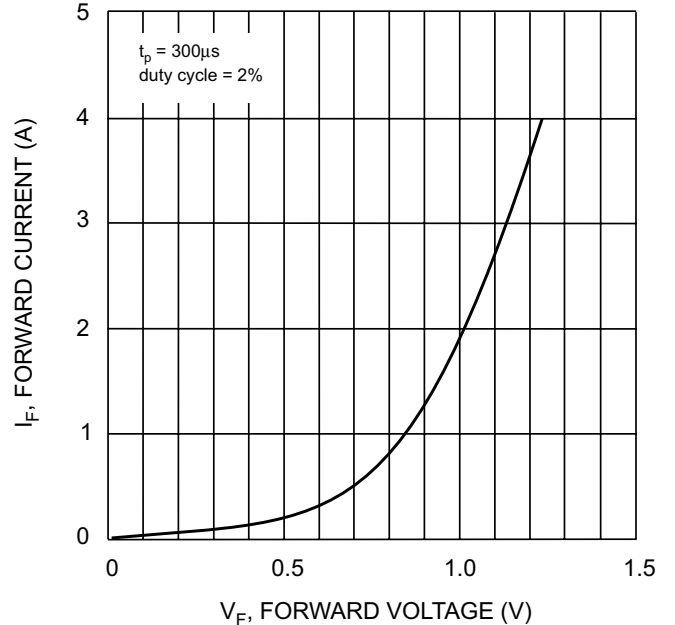


Fig. 2 Typical High Current Fwd Characteristics

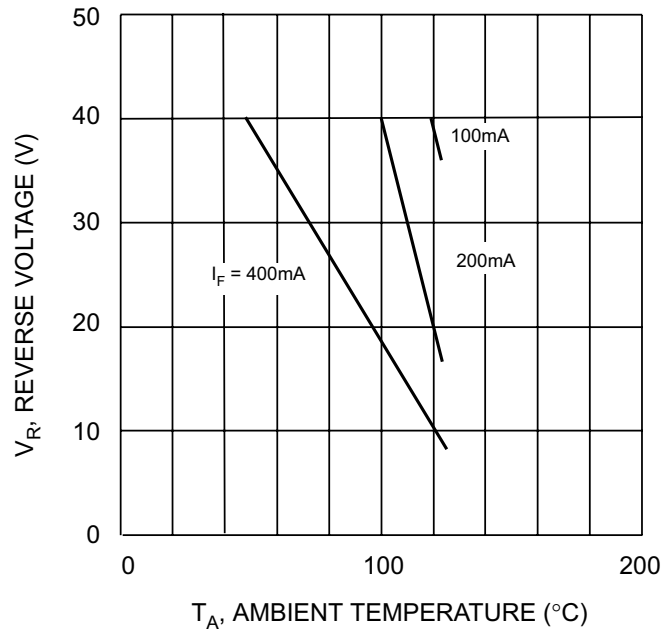


Fig. 3 Blocking Voltage Derating Curves