

### **Features and Benefits**

- Chopper stabilized amplifier stage
- Optimized for BDC motor applications
- New miniature package / thin, high reliability package
- Operation down to 2.2V
- CMOS for optimum stability, quality, and cost

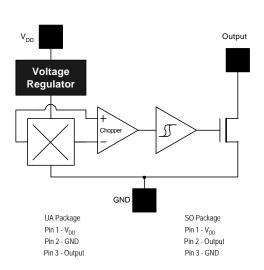
### **Applications**

- Solid state switch
- Brushless DC motor commutation
- Speed sensing
- Linear position sensing
- Angular position sensing
- Current sensing

#### **Ordering Information**

Part No.	Temperature Suffix	Package	Temperature Range
US3881	E	SO or UA	-40°C to 85°C Commercial
US3881	L	SO or UA	-40°C to 150°C Automotive
	* Contact factory	or Sale Representa	tive for legacy temperature options

# **Functional Diagram**



#### Description

The US3881 is a bipolar Hall effect sensor IC fabricated from mixed signal CMOS technology. It incorporates advanced chopper stabilization techniques to provide accurate and stable magnetic switch points. There are many applications for this sensor in addition to those listed above. The design, specifications, and performance have been optimized for commutation applications in 5V and 12V brushless DC motors.

The output transistor will be latched on  $(B_{OP})$  in the presence of a sufficiently strong South pole magnetic field facing the marked side of the package. Similarly, the output will be latched off  $(B_{RP})$  in the presence of a North field.

The SOT-23 device is reversed from the UA package. The SOT-23 output transistor will be latched on in the presence of a sufficiently strong North pole magnetic field subjected to the marked face.

**Note:** Static sensitive device; please observe ESD precautions. Reverse  $V_{DD}$  protection is not included. For reverse voltage protection, a 100 **W** resistor in series with  $V_{DD}$  is recommended.

US3881 CMOS Low Voltage Hall Effect Latch



**US3881 Electrical Specifications** DC Operating Parameters:  $T_A = 25$ ,  $V_{DD} = 12V_{DC}$  (unless otherwise specified).

Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Supply Voltage	V <sub>DD</sub>	Operating	2.2		18	V
Supply Current	I <sub>DD</sub>	B <b<sub>OP</b<sub>	1.5	2.5	4.0	mA
Saturation Voltage	VDS(on)	lout = 20 mA, B>Bop		0.4	0.5	V
Output Leakage	I <sub>OFF</sub>	B <b<sub>RP, V<sub>OUT</sub> = 18V</b<sub>		0.01	5.0	ì A
Output Rise Time	tr	$V_{DD} = 12V, R_L = 1.1K\tilde{U}, C_L = 20pf$	-	0.04		ÌS
Output Fall Time	tr	$V_{DD} = 12V, R_L = 1.1 K\dot{U}, C_L = 20 pf$		0.18		ÌS

**US3881 Magnetic Specifications** DC Operating Parameters:  $T_A = -40$  to 150°C,  $V_{DD} = 12 V_{DC}$  (unless otherwise specified). 1mT=10 Gauss.

Parameter	Symbol Test Conditions	Min	Тур	Max	Units
Operating Point	Bop	1.0	5.0	9.0	mT
Release Point	Brp	-9.0	-5.0	-1.0	mT
Hysteresis	B <sub>hys</sub>	5.5	10.0	12.0	mT

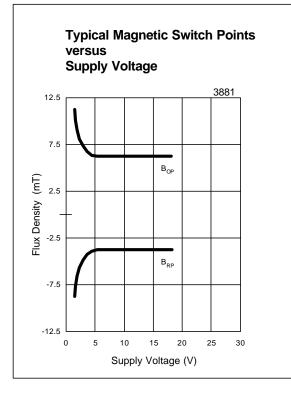
# **Absolute Maximum Ratings**

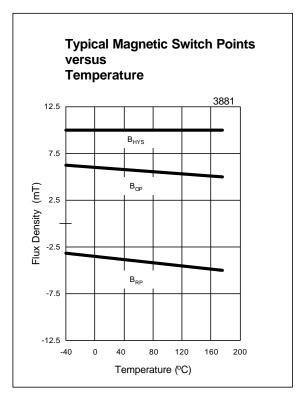
Supply Voltage (Operating), $V_{DD}$	18V		
Supply Current (Fault), IDD	50mA		
Output Voltage, Vout	18V		
Output Current (Fault), lout	50mA		
Power Dissipation, PD	100mW		
Operating Temperature Range, $T_A$	-40 to 150°C		
Storage Temperature Range, Ts	-65 to 150°C		
Maximum Junction Temp, TJ	175°C		
ESD Sensitivity (All Pins)	+/- 4KV		

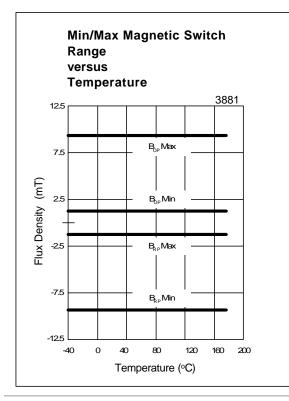
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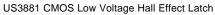


# **Performance Graphs**

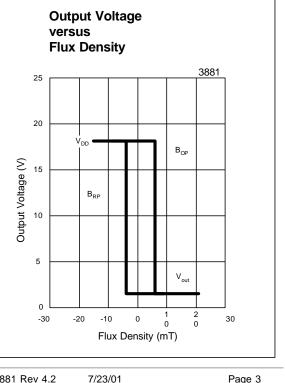








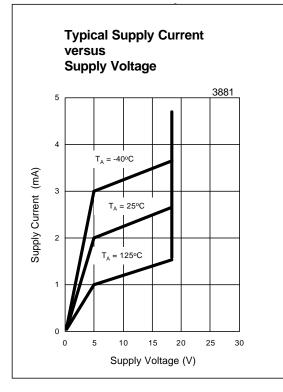
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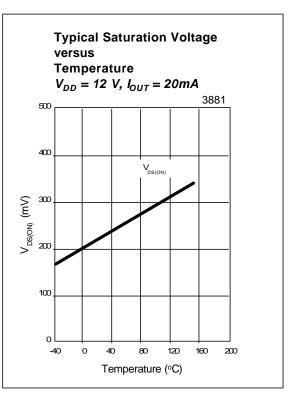


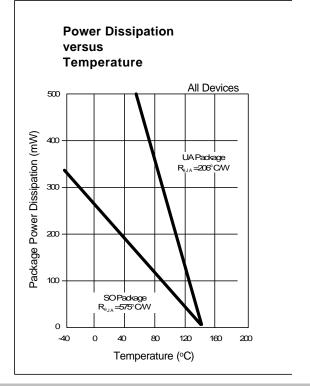
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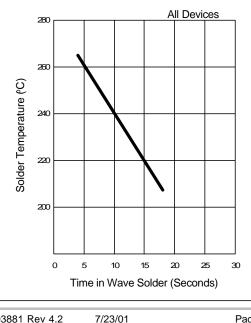
## **Performance Graphs**







Wave Soldering Parameters





## Unique Features CMOS Hall IC Technology

The chopper stabilized amplifier uses switched capacitor techniques to eliminate the amplifier offset voltage, which, in bipolar devices, is a major source of temperature sensitive drift. CMOS makes this advanced technique possible. The CMOS chip is also much smaller than a bipolar chip, allowing very sophisticated circuitry to be placed in less space. The small chip size also contributes to lower physical stress and less power consumption.

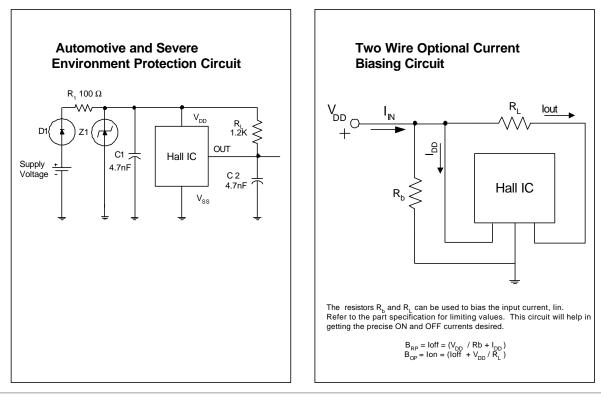
#### Installation

Consider temperature coefficients of Hall IC and magnetics, as well as air gap life time variations. Observe temperature limits during wave soldering.

#### Applications

If reverse supply protection is desired, use a resistor in series with the  $V_{DD}$  pin. The resistor will limit the Supply Current (Fault),  $I_{DD}$ , to 50 mA. For severe EMC conditions, use the application circuit below.

# Applications Examples

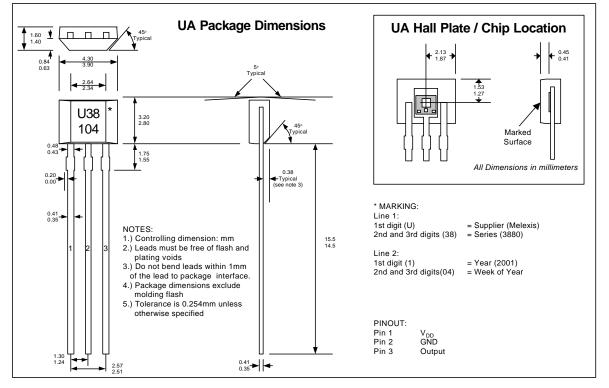


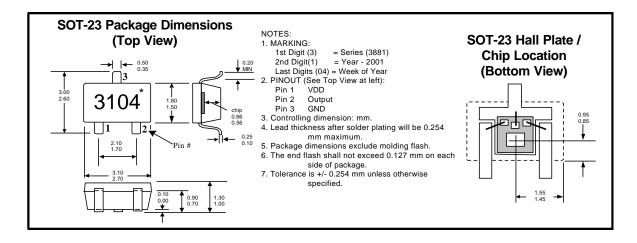
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# **Physical Characteristics**





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