



3.3GHz ÷ 4 fixed Modulus Divider Advance Information

DS2112

Issue 7.3

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Features

- Very High Speed Operation 3.3GHz
- Silicon Technology for low Phase Noise (Typically better than -140dBc/Hz at 10kHz)
- Specified Over the Full Military Temperature Range
- Low Power Dissipation 370mW (typ)
- 5V Single Supply Operation
- High Input Sensitivity
- Very Wide Operating Frequency Range
- Available as DESC SMD 5962-9056701MPA

Description

The SP8804 is one of a range of very high speed low power prescalers for professional and military applications. The device features a complementary output stage with on chip current source for the emitter follower outputs.

Ordering Information

SP8804/A/DG Military temperature range DES9056701/AC/DGAZ (SMD)

Thermal Characteristics

 θ ja = 150°C/W θ ic = 50°C/W

Absolute Maximum Ratings

Supply voltage $V_{\rm cc}$ 6.5V Clock Input voltage 2.5V p-p Storage temperature range -65°C to +150°C Junction temperature +175°C

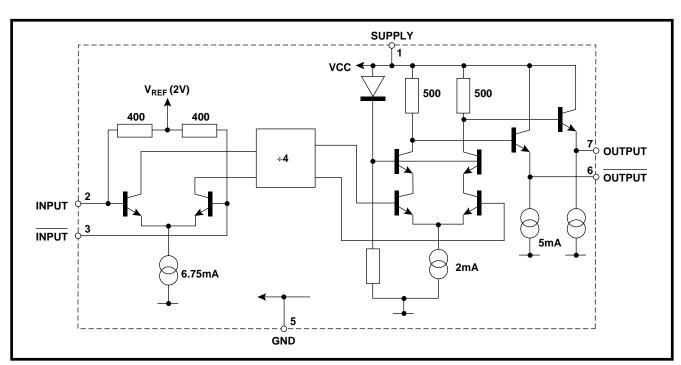


Figure 1 SP8804 Block diagram

SP8804 Advance Information

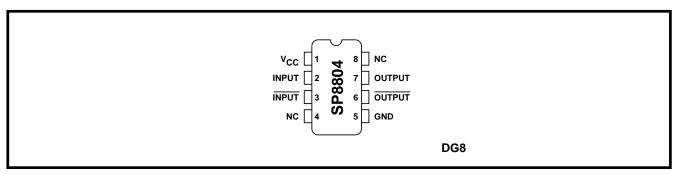


Figure 2 Pin connections

Electrical Characteristics

Guaranteed over the temperature range T_{amb} -55°C to +125°C (see note) and supply voltage range 4.75V to 5.25V. Tested at T_{amb} = -55°C and +105°C, V_{CC} = 4.75V and 5.25V.

Characteristic	Pin	Value			Units	Conditions
		Min	Тур	Max	Offics	Conditions
Supply current	1		74	90	mA	$V_{cc} = 5V$
Input sensitivity	2, 3					RMS sinewave
0.65GHz to 2.8GHz				175	m∨	measured in 50 ohm system.
3.3GHz				400	m∨	See Figs. 3 & 4
Input impedance	2, 3		50		Ω	
(series equivalent)			2		pF	
Output Voltage with f _{in} =1000MHz	6, 7	0.8	1		Vp-р	$V_{cc} = 5V$
Output Voltage with f _{in} = 3GHz	6, 7		0.25		Vp-p	V_{cc}^{cc} = 5V load as Fig. 4

NOTE: Devices must be used with a suitable heatsink to maintain chip temperature below 175°C when operating at T_{amb} >105°C.

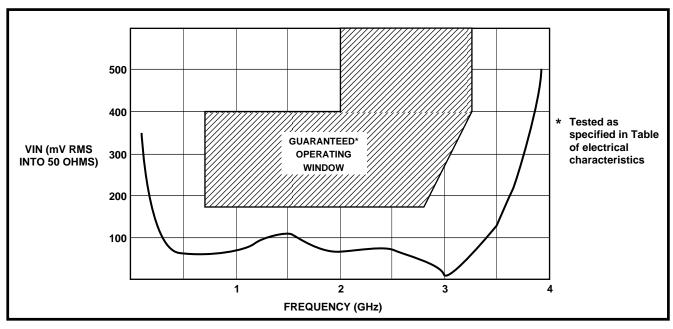


Figure 3 Typical input sensitivity

Advance Information SP8804

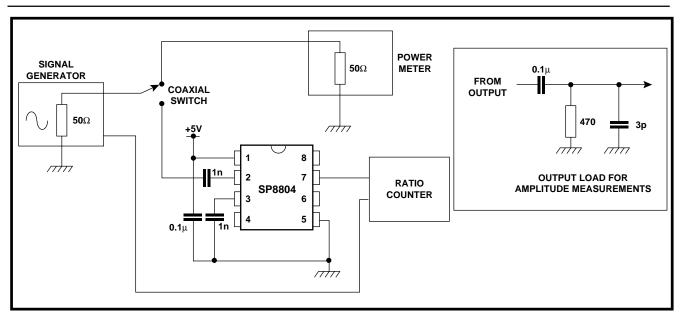


Figure 4 Test circuit

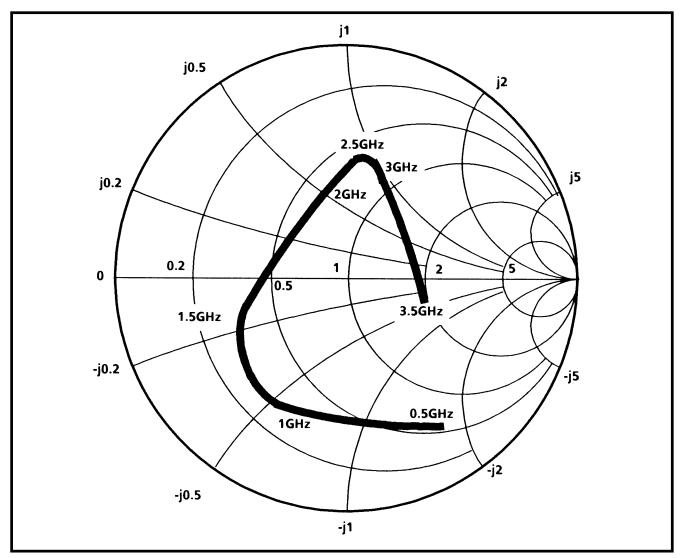
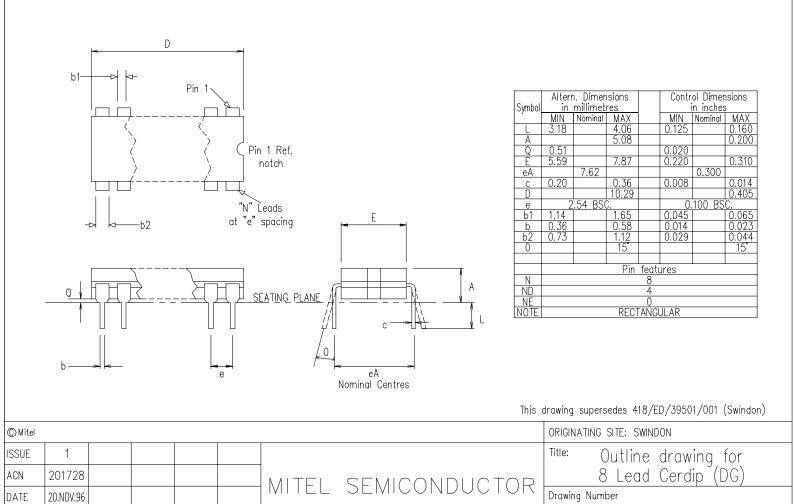


Figure 5 Typical input impedance



APPROVED

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