

MITSUBISHI Nch POWER MOSFET

FS50SMJ-3

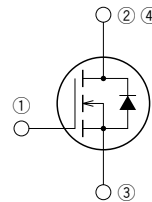
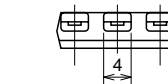
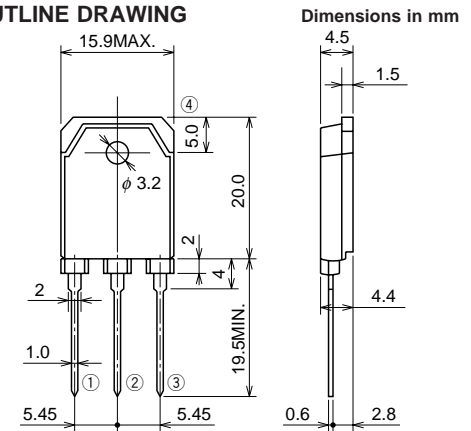
HIGH-SPEED SWITCHING USE

FS50SMJ-3



- 4V DRIVE
- V_{DSS} 150V
- $r_{DS(ON)}$ (MAX) 30m Ω
- I_D 50A
- Integrated Fast Recovery Diode (TYP.) 125ns

OUTLINE DRAWING



- ① GATE
- ② DRAIN
- ③ SOURCE
- ④ DRAIN

TO-3P

APPLICATION

Motor control, Lamp control, Solenoid control
DC-DC converter, etc.

MAXIMUM RATINGS (T_c = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit
V_{DSS}	Drain-source voltage	$V_{GS} = 0V$	150	V
V_{GSS}	Gate-source voltage	$V_{DS} = 0V$	± 20	V
I_D	Drain current		50	A
I_{DM}	Drain current (Pulsed)		200	A
I_{DA}	Avalanche drain current (Pulsed)	$L = 100\mu H$	50	A
I_S	Source current		50	A
I_{SM}	Source current (Pulsed)		200	A
P_D	Maximum power dissipation		150	W
T_{ch}	Channel temperature		-55 ~ +150	°C
T_{stg}	Storage temperature		-55 ~ +150	°C
—	Weight	Typical value	4.8	g

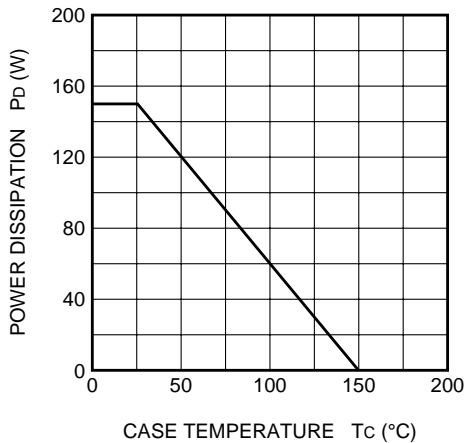
Feb.1999

ELECTRICAL CHARACTERISTICS (Tch = 25°C)

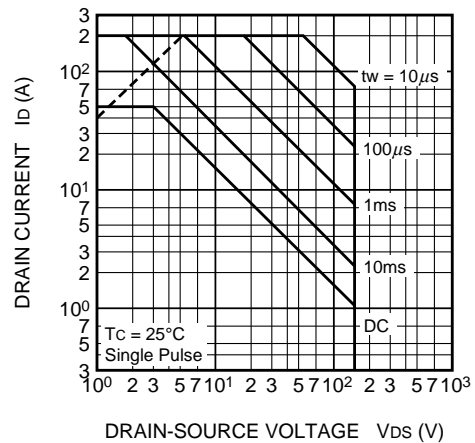
Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V(BR)DSS	Drain-source breakdown voltage	ID = 1mA, VGS = 0V	150	—	—	V
IGSS	Gate-source leakage current	VGS = ±20V, VDS = 0V	—	—	±0.1	μA
IDSS	Drain-source leakage current	VDS = 150V, VGS = 0V	—	—	0.1	mA
VGS(th)	Gate-source threshold voltage	ID = 1mA, VDS = 10V	1.0	1.5	2.0	V
rDS(ON)	Drain-source on-state resistance	ID = 25A, VGS = 10V	—	23	30	mΩ
rDS(ON)	Drain-source on-state resistance	ID = 25A, VGS = 4V	—	24	32	mΩ
VDS(ON)	Drain-source on-state voltage	ID = 25A, VGS = 10V	—	0.58	0.75	V
yfs	Forward transfer admittance	ID = 25A, VDS = 10V	—	62	—	S
Ciss	Input capacitance	VDS = 10V, VGS = 0V, f = 1MHz	—	8200	—	pF
Coss	Output capacitance		—	870	—	pF
Crss	Reverse transfer capacitance		—	440	—	pF
td(on)	Turn-on delay time	VDD = 80V, ID = 25A, VGS = 10V, RGEN = RGS = 50Ω	—	54	—	ns
tr	Rise time		—	110	—	ns
td(off)	Turn-off delay time		—	850	—	ns
tf	Fall time		—	340	—	ns
VSD	Source-drain voltage		IS = 25A, VGS = 0V	—	1.0	1.5
Rth(ch-c)	Thermal resistance	Channel to case	—	—	0.83	°C/W
trr	Reverse recovery time	IS = 50A, dis/dt = -100A/μs	—	125	—	ns

PERFORMANCE CURVES

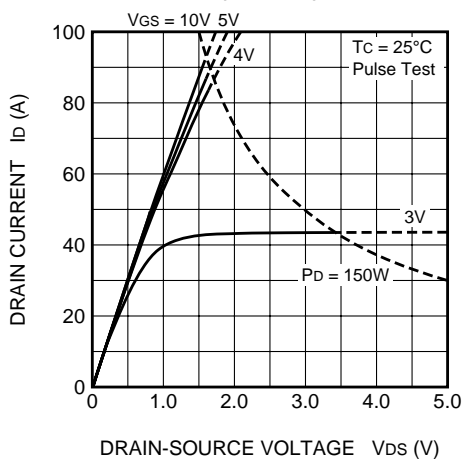
POWER DISSIPATION DERATING CURVE



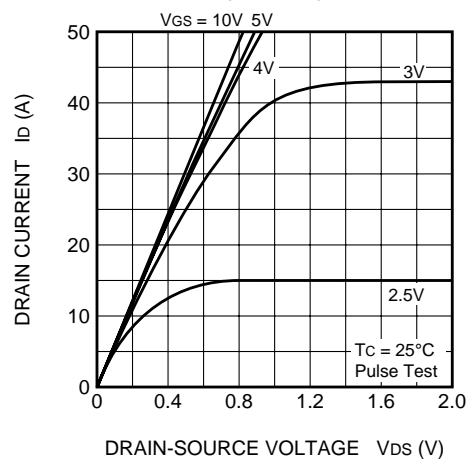
MAXIMUM SAFE OPERATING AREA

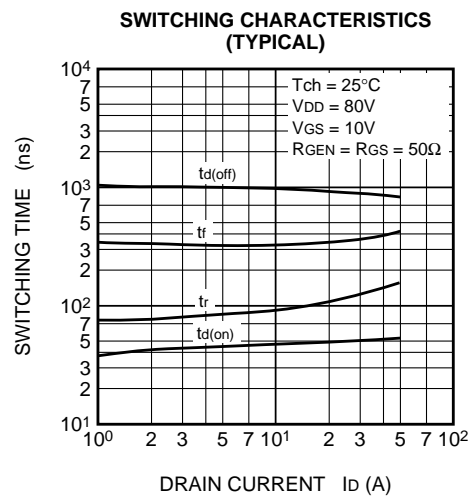
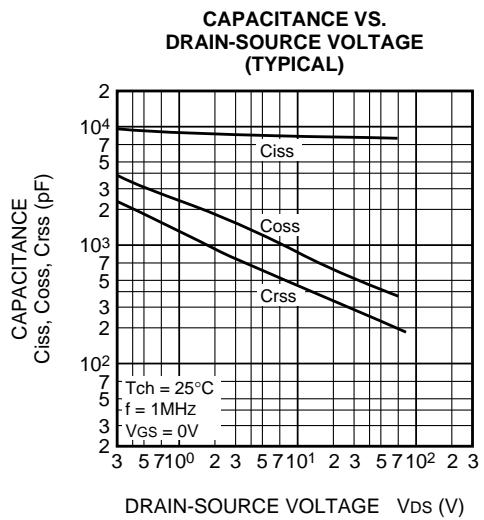
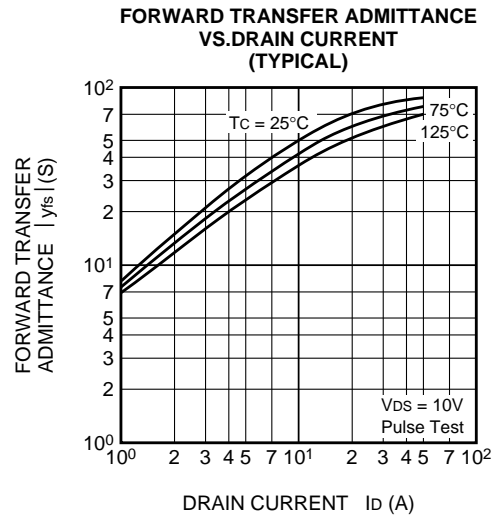
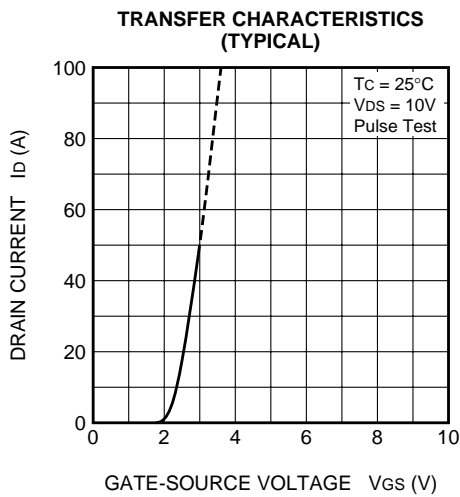
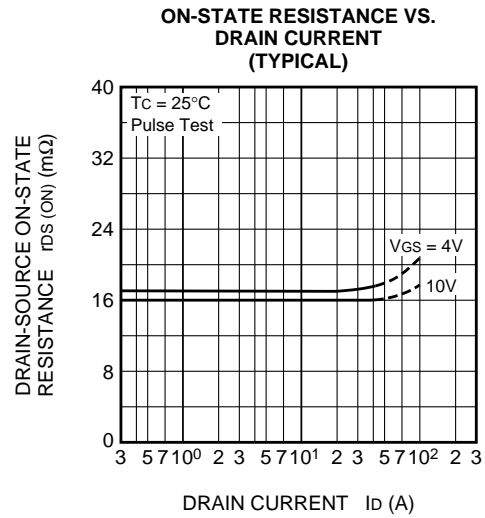
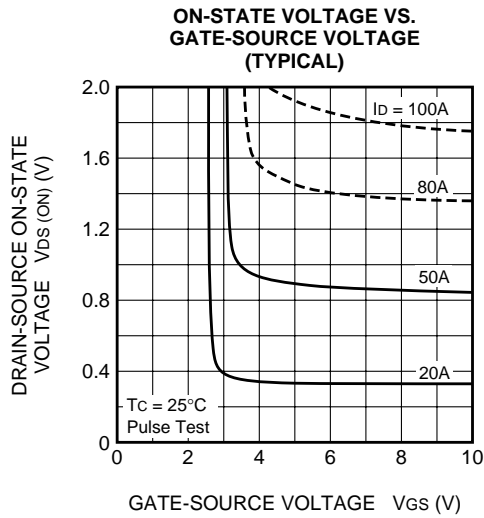


OUTPUT CHARACTERISTICS (TYPICAL)

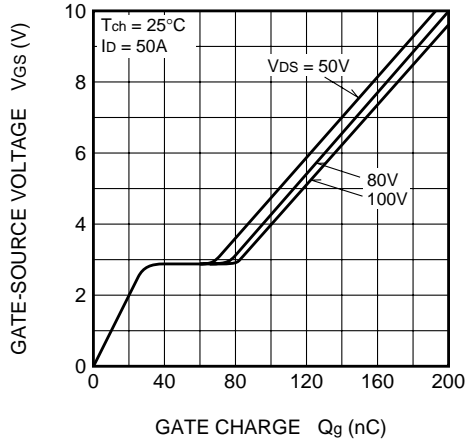


OUTPUT CHARACTERISTICS (TYPICAL)

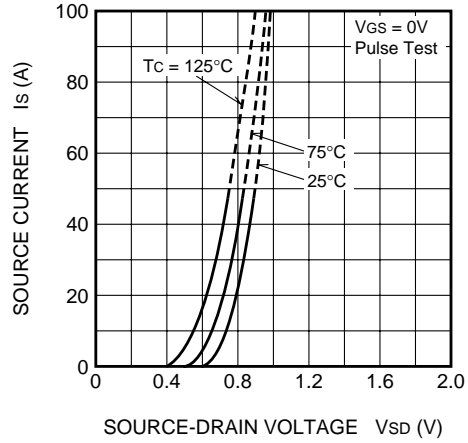




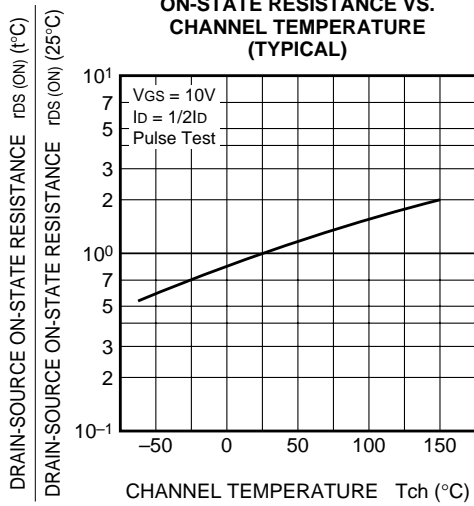
GATE-SOURCE VOLTAGE VS. GATE CHARGE (TYPICAL)



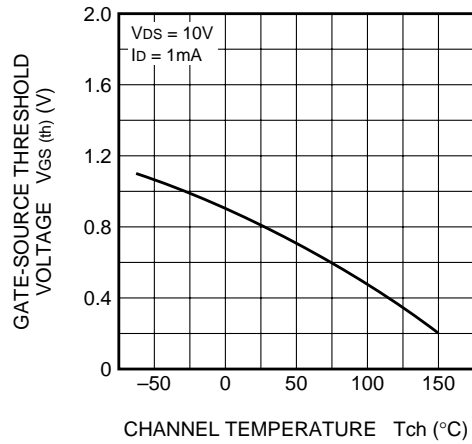
SOURCE-DRAIN DIODE FORWARD CHARACTERISTICS (TYPICAL)



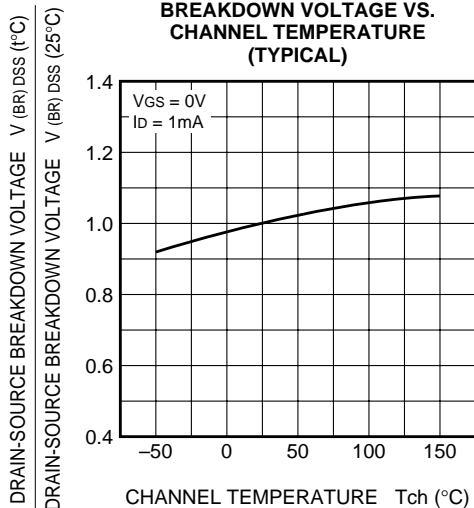
ON-STATE RESISTANCE VS. CHANNEL TEMPERATURE (TYPICAL)



THRESHOLD VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)



BREAKDOWN VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS

