

# FS50KM-3

HIGH-SPEED SWITCHING USE

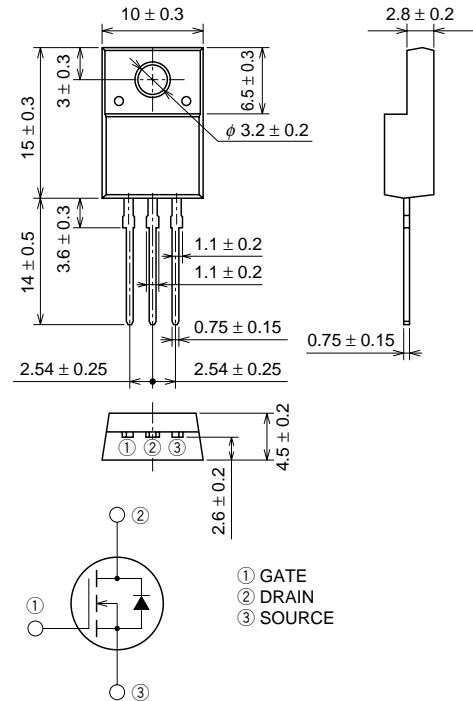
## FS50KM-3



- 10V DRIVE
- V<sub>DSS</sub> ..... 150V
- r<sub>DS (ON)</sub> (MAX) ..... 31mΩ
- I<sub>D</sub> ..... 50A
- Integrated Fast Recovery Diode (TYP.) ..... 130ns
- V<sub>iso</sub> ..... 2000V

## OUTLINE DRAWING

Dimensions in mm



TO-220FN

## APPLICATION

Motor control, Lamp control, Solenoid control  
DC-DC converter, etc.

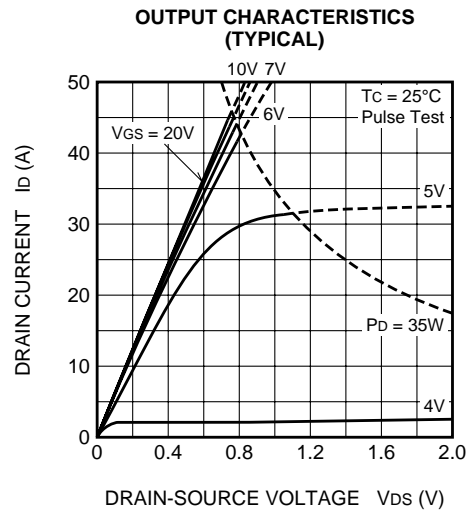
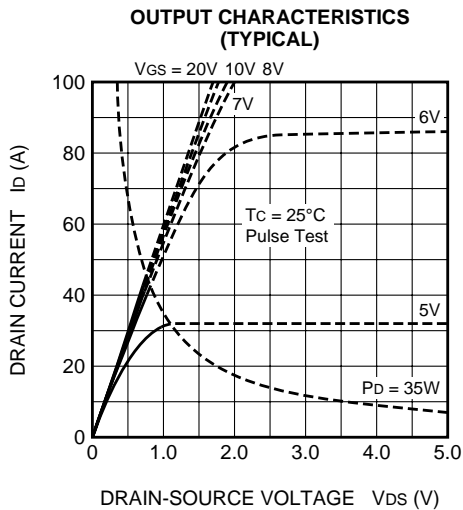
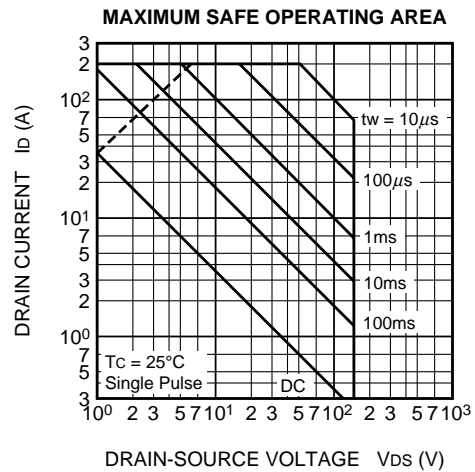
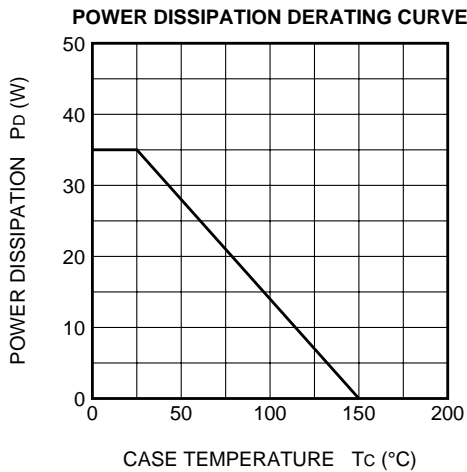
## MAXIMUM RATINGS (T<sub>c</sub> = 25°C)

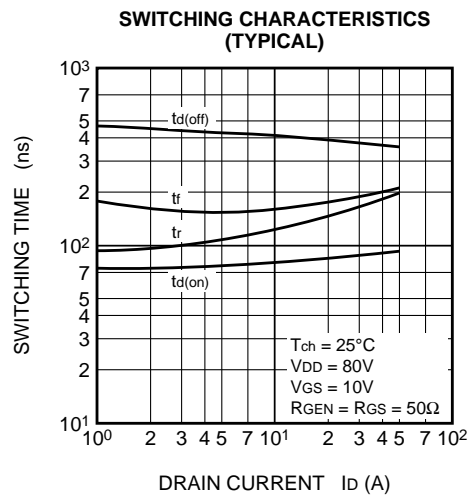
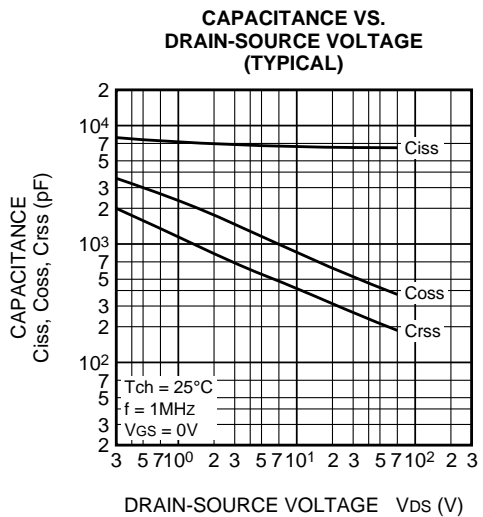
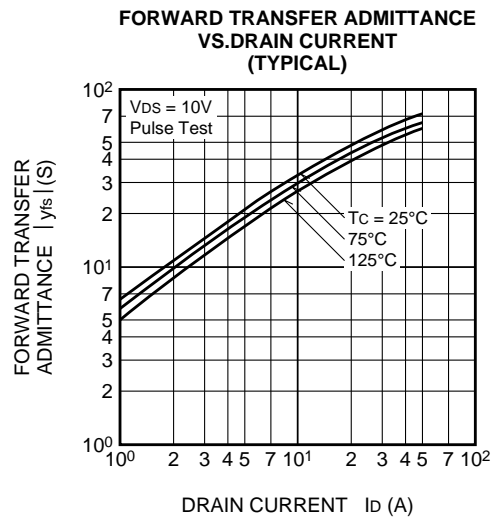
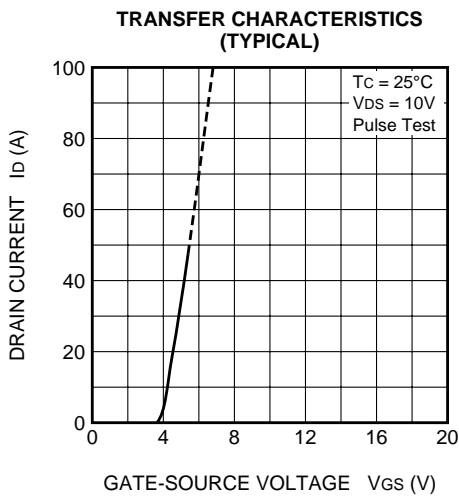
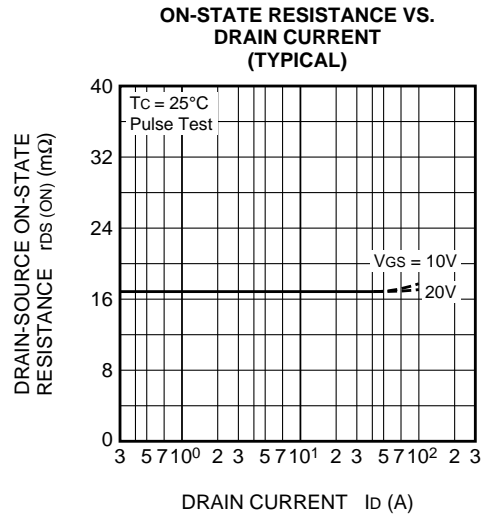
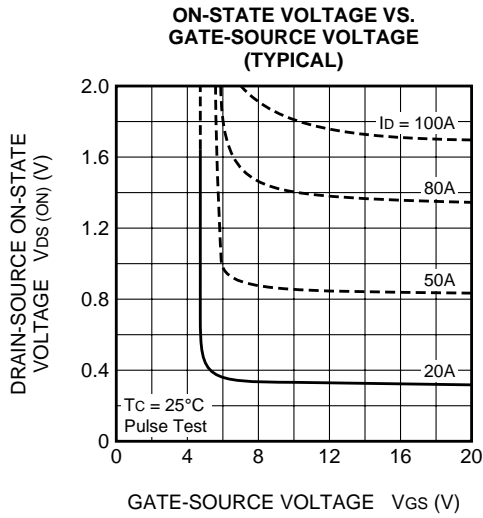
Symbol	Parameter	Conditions	Ratings	Unit
V <sub>DSS</sub>	Drain-source voltage	V <sub>GS</sub> = 0V	150	V
V <sub>GSS</sub>	Gate-source voltage	V <sub>DS</sub> = 0V	±20	V
I <sub>D</sub>	Drain current		50	A
I <sub>DM</sub>	Drain current (Pulsed)		200	A
I <sub>DA</sub>	Avalanche drain current (Pulsed)	L = 100μH	50	A
I <sub>S</sub>	Source current		50	A
I <sub>SM</sub>	Source current (Pulsed)		200	A
P <sub>D</sub>	Maximum power dissipation		35	W
T <sub>ch</sub>	Channel temperature		-55 ~ +150	°C
T <sub>stg</sub>	Storage temperature		-55 ~ +150	°C
V <sub>iso</sub>	Isolation voltage	AC for 1minute, Terminal to case	2000	V
—	Weight	Typical value	2.0	g

**ELECTRICAL CHARACTERISTICS** (Tch = 25°C)

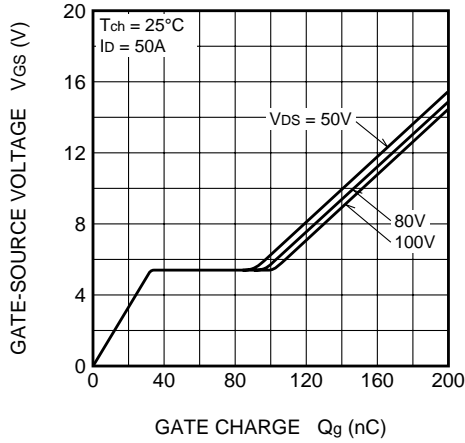
Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V (BR) DSS	Drain-source breakdown voltage	Id = 1mA, VGS = 0V	150	—	—	V
IGSS	Gate-source leakage current	VGS = ±20V, VDS = 0V	—	—	±0.1	μA
IDSS	Drain-source leakage current	VDS = 150V, VGS = 0V	—	—	0.1	mA
VGS (th)	Gate-source threshold voltage	Id = 1mA, VDS = 10V	2.0	3.0	4.0	V
rDS (ON)	Drain-source on-state resistance	Id = 25A, VGS = 10V	—	24	31	mΩ
VDS (ON)	Drain-source on-state voltage	Id = 25A, VGS = 10V	—	0.600	0.775	V
yfs	Forward transfer admittance	Id = 25A, VDS = 10V	—	55	—	S
Ciss	Input capacitance	VDS = 10V, VGS = 0V, f = 1MHz	—	6540	—	pF
Coss	Output capacitance		—	860	—	pF
Crss	Reverse transfer capacitance		—	360	—	pF
td (on)	Turn-on delay time		—	95	—	ns
tr	Rise time	VDD = 80V, Id = 25A, VGS = 10V, RGEN = RGS = 50Ω	—	155	—	ns
td (off)	Turn-off delay time		—	380	—	ns
tf	Fall time		—	180	—	ns
VSD	Source-drain voltage		IS = 25A, VGS = 0V	—	1.0	1.5
Rth (ch-c)	Thermal resistance	Channel to case	—	—	3.57	°C/W
trr	Reverse recovery time	IS = 50A, dis/dt = -100A/μs	—	130	—	ns

**PERFORMANCE CURVES**

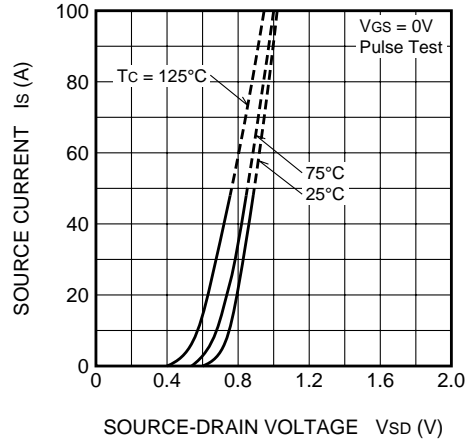




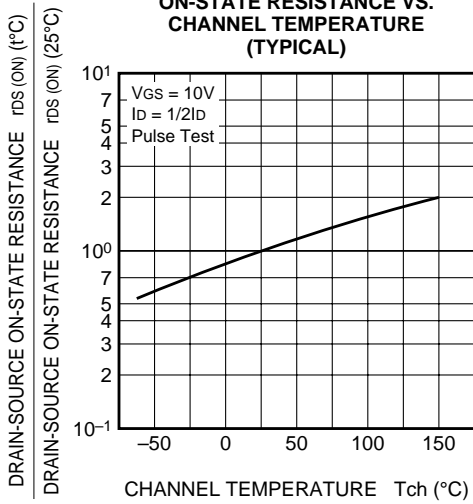
**GATE-SOURCE VOLTAGE VS. GATE CHARGE (TYPICAL)**



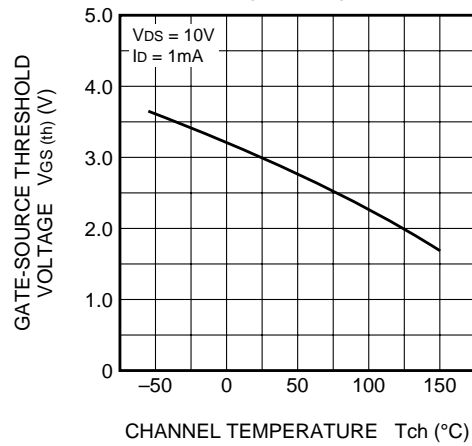
**SOURCE-DRAIN DIODE FORWARD CHARACTERISTICS (TYPICAL)**



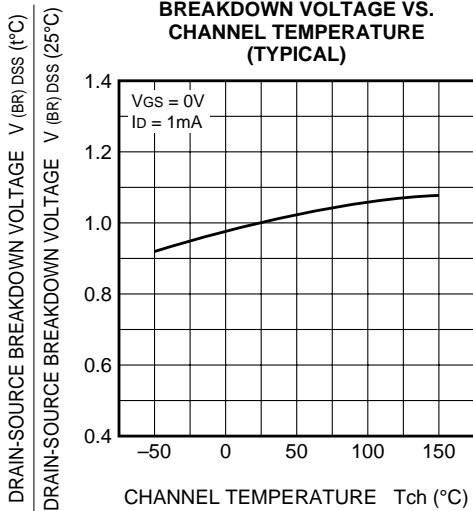
**ON-STATE RESISTANCE VS. CHANNEL TEMPERATURE (TYPICAL)**



**THRESHOLD VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)**



**BREAKDOWN VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)**



**TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS**

