

MITSUBISHI THYRISTOR MODULES

# TM25T3A-M,-H

MEDIUM POWER GENERAL USE

INSULATED TYPE

TM25T3A-M,-H



- **I<sub>O</sub>** DC output current ..... **60A**
- **V<sub>RMM</sub>** Repetitive peak reverse voltage ..... **400/800V**
- **V<sub>DRM</sub>** Repetitive peak off-state voltage ..... **400/800V**
- **3 Phase Mix Bridge**
- **Insulated Type**
- **UL Recognized**

Yellow Card No. E80276 (N)

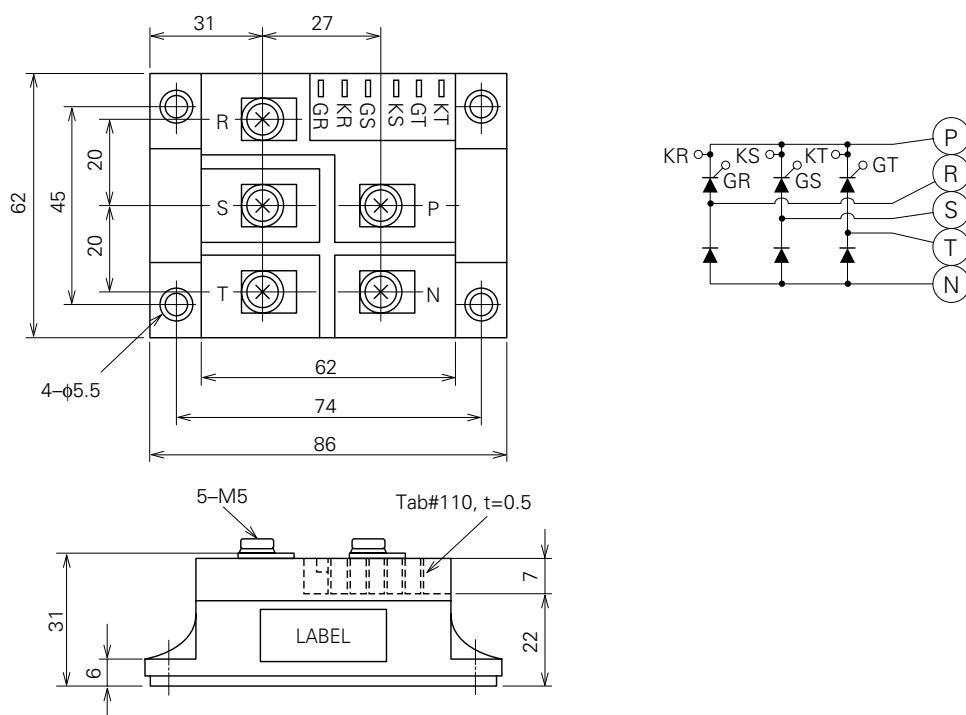
File No. E80271

## APPLICATION

DC motor control, NC equipment, AC motor control, contactless switches, electric furnace temperature control, light dimmers

OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm



Feb.1999

## ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Voltage class		Unit
		M	H	
V <sub>RRM</sub>	Repetitive peak reverse voltage	400	800	V
V <sub>RSM</sub>	Non-repetitive peak reverse voltage	480	960	V
V <sub>R (DC)</sub>	DC reverse voltage	320	640	V
V <sub>DRM</sub>	Repetitive peak off-state voltage	400	800	V
V <sub>D5M</sub>	Non-repetitive peak off-state voltage	480	960	V
V <sub>d (DC)</sub>	DC off-state voltage	320	640	V

Symbol	Parameter	Conditions	Ratings	Unit
I <sub>O</sub>	DC output current	3-phase fullwave rectified, T <sub>j</sub> =76.6°C	60	A
I <sub>TSM</sub> , I <sub>FSM</sub>	Surge (non-repetitive) current	One half cycle at 60Hz, peak value	500	A
I <sup>2</sup> t	I <sup>2</sup> t for fusing	Value for one cycle of surge current	1.0 x 10 <sup>3</sup>	A <sup>2</sup> s
dI/dt	Critical rate of rise of on-state current	V <sub>D</sub> =1/2V <sub>DRM</sub> , I <sub>G</sub> =0.5A, T <sub>j</sub> =125°C	100	A/μs
P <sub>GM</sub>	Peak gate power dissipation		5.0	W
P <sub>G (AV)</sub>	Average gate power dissipation		0.5	W
V <sub>FGM</sub>	Peak gate forward voltage		10	V
V <sub>RGM</sub>	Peak gate reverse voltage		5.0	V
I <sub>FGM</sub>	Peak gate forward current		2.0	A
T <sub>j</sub>	Junction temperature		-40~125	°C
T <sub>stg</sub>	Storage temperature		-40~125	°C
V <sub>iso</sub>	Isolation voltage	Charged part to case	2500	V
—	Mounting torque	Main terminal screw M5	1.47~1.96	N·m
			15~20	kg·cm
		Mounting screw M5	1.47~1.96	N·m
			15~20	kg·cm
—	Weight	Typical value	310	g

## ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
I <sub>RRM</sub>	Repetitive peak reverse current	T <sub>j</sub> =125°C, V <sub>RRM</sub> applied	—	—	4.0	mA
I <sub>DRM</sub>	Repetitive peak off-state current	T <sub>j</sub> =125°C, V <sub>DRM</sub> applied	—	—	4.0	mA
V <sub>TM</sub> , V <sub>FM</sub>	Forward voltage	T <sub>j</sub> =125°C, I <sub>TM</sub> =I <sub>FM</sub> =75A, instantaneous meas.	—	—	1.4	V
dV/dt	Critical rate of rise of off-state voltage	T <sub>j</sub> =125°C, V <sub>b</sub> =2/3V <sub>DRM</sub>	500	—	—	V/μs
V <sub>GT</sub>	Gate trigger voltage	T <sub>j</sub> =25°C, V <sub>d</sub> =6V, R <sub>L</sub> =2Ω	—	—	2.0	V
V <sub>GD</sub>	Gate non-trigger voltage	T <sub>j</sub> =125°C, V <sub>d</sub> =1/2V <sub>DRM</sub>	0.25	—	—	V
I <sub>GT</sub>	Gate trigger current	T <sub>j</sub> =25°C, V <sub>d</sub> =6V, R <sub>L</sub> =2Ω	10	—	50	mA
R <sub>th (j-c)</sub>	Thermal resistance	Junction to case (per 1/6 module)	—	—	1.5	°C/W
R <sub>th (c-f)</sub>	Contact thermal resistance	Case to fin, Conductive grease applied (per 1/6 module)	—	—	0.36	°C/W
—	Insulation resistance	Measured with a 500V megohmmeter between main terminal and case	10	—	—	MΩ

Note: Items of the above table applies to the Thyristor part and the Diode part as circled in the following tables.

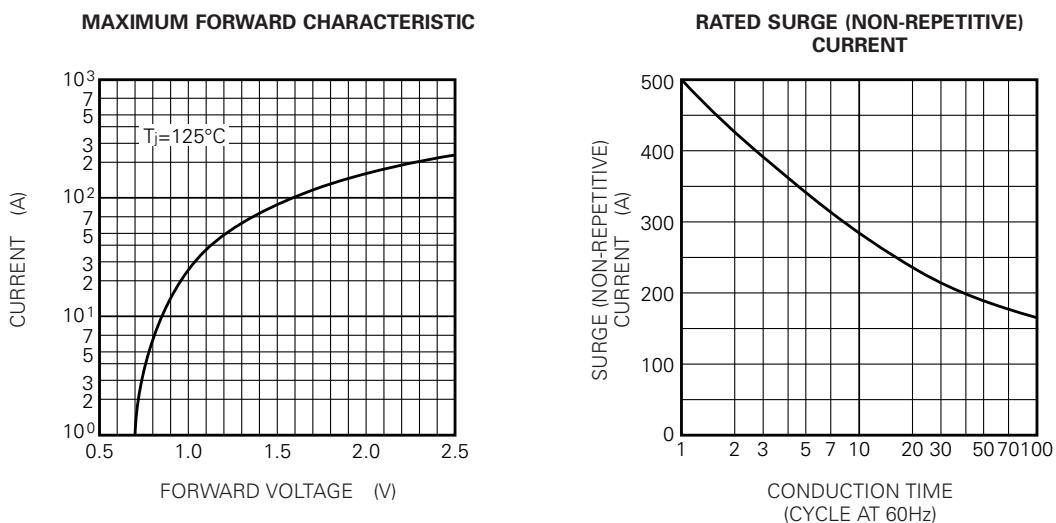
**MAXIMUM RATINGS**

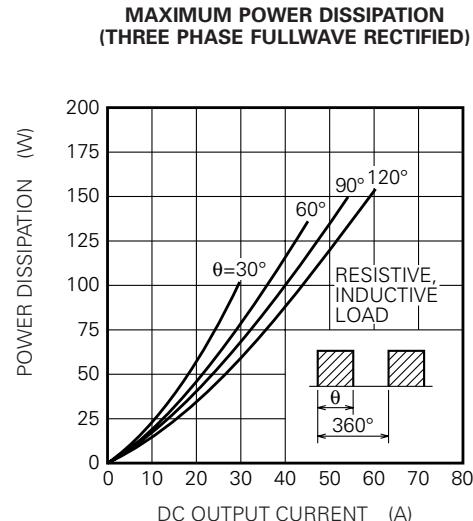
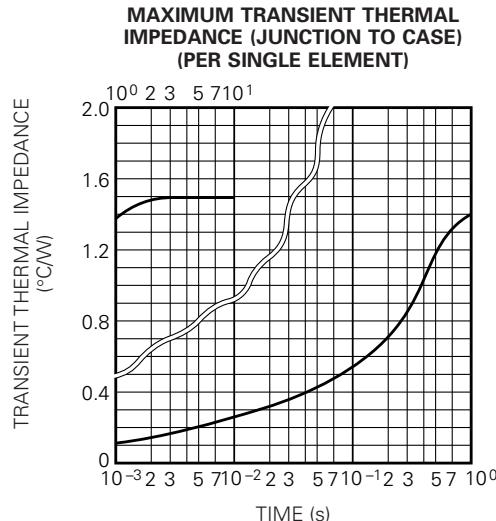
Item	V <sub>RRM</sub>	V <sub>RSM</sub>	V <sub>R</sub> (DC)	V <sub>DRM</sub>	V <sub>DSDM</sub>	V <sub>D</sub> (DC)	I <sub>T</sub> (RMS)	I <sub>T</sub> (AV)	I <sub>SM</sub>	I <sup>2</sup> <sub>t</sub>	di/dt
	I <sub>F</sub> (RMS)	I <sub>F</sub> (AV)	I <sub>FSM</sub>								
Thyristor	○	○	○	○	○	○	○	○	○	○	○
Diode	○	○	○	—	—	—	○	○	○	○	—

Item	P <sub>GM</sub>	P <sub>G</sub> (AV)	V <sub>FGM</sub>	I <sub>FGM</sub>	T <sub>j</sub>	T <sub>stg</sub>
Thyristor	○	○	○	○	○	○
Diode	—	—	—	—	○	○

**ELECTRICAL CHARACTERISTICS**

Item	I <sub>RRM</sub>	I <sub>DRM</sub>	V <sub>TM</sub>	dv/dt	V <sub>GT</sub>	V <sub>GD</sub>	I <sub>GT</sub>	R <sub>th</sub> (j-c)	R <sub>th</sub> (c-f)
			V <sub>FM</sub>						
Thyristor	○	○	○	○	○	○	○	○	○
Diode	○	—	○	—	—	—	—	○	○

**PERFORMANCE CURVES**



**LIMITING VALUE OF THE DC OUTPUT CURRENT (THREE PHASE FULLWAVE RECTIFIED)**

