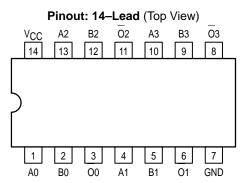
Product Preview Low-Voltage CMOS Quad 2-Input NAND Gate, Open Drain With 5V-Tolerant Inputs

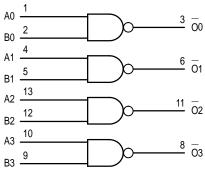
The MC74LCX38 is a high performance, open drain quad 2-input NAND gate operating from a 2.7 to 3.6V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers. A V_I specification of 5.5V allows MC74LCX38 inputs to be safely driven from 5V devices.

The open drain output with a 5V pull–up resistor can be utilized to drive 5V CMOS inputs. Current drive capability is 24mA at the outputs.

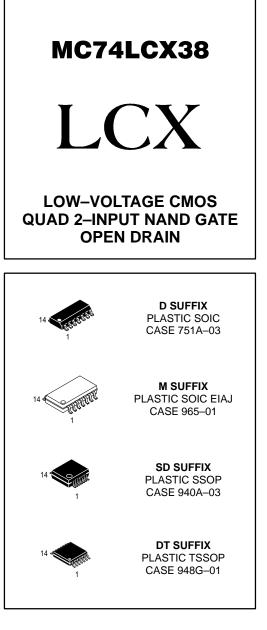
- Designed for 2.7 to 3.6V V_{CC} Operation
- 5V Tolerant Inputs Interface Capability With 5V TTL Logic
- LVTTL Compatible
- LVCMOS Compatible
- 24mA Output Sink Capability
- Near Zero Static Supply Current (10μA) Substantially Reduces System Power Requirements
- Latchup Performance Exceeds 500mA
- ESD Performance: Human Body Model >2000V; Machine Model >200V



LOGIC DIAGRAM



This document contains information on a product under development. Motorola reserves the right to change or discontinue this product without notice.



PIN NAMES

Pins	Function
<u>A</u> n, Bn	Data Inputs
On	Outputs

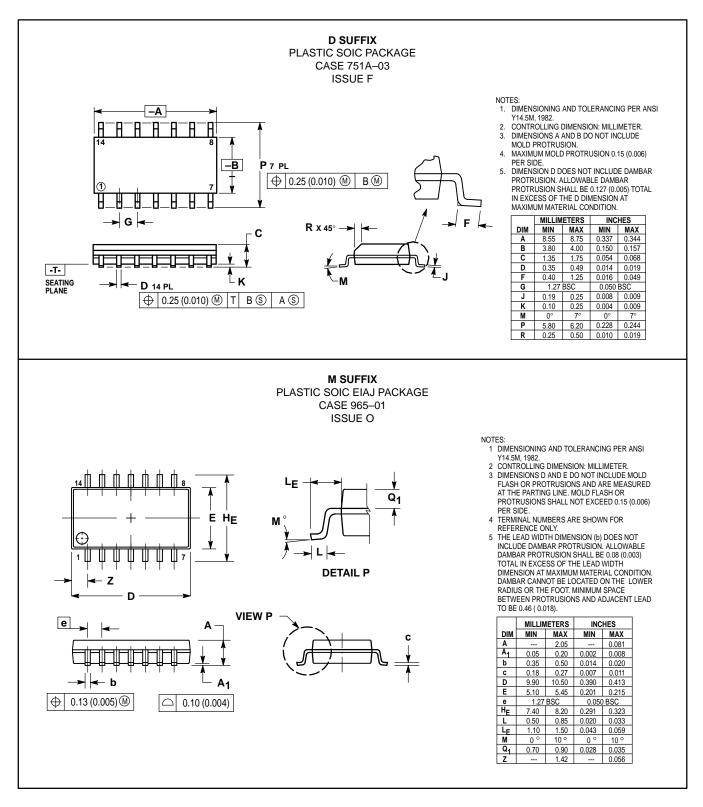
FUNCTION TABLE

Inputs		Outputs
An	Bn	On
L	L	Н
L	н	н
н	L	н
н	н	L

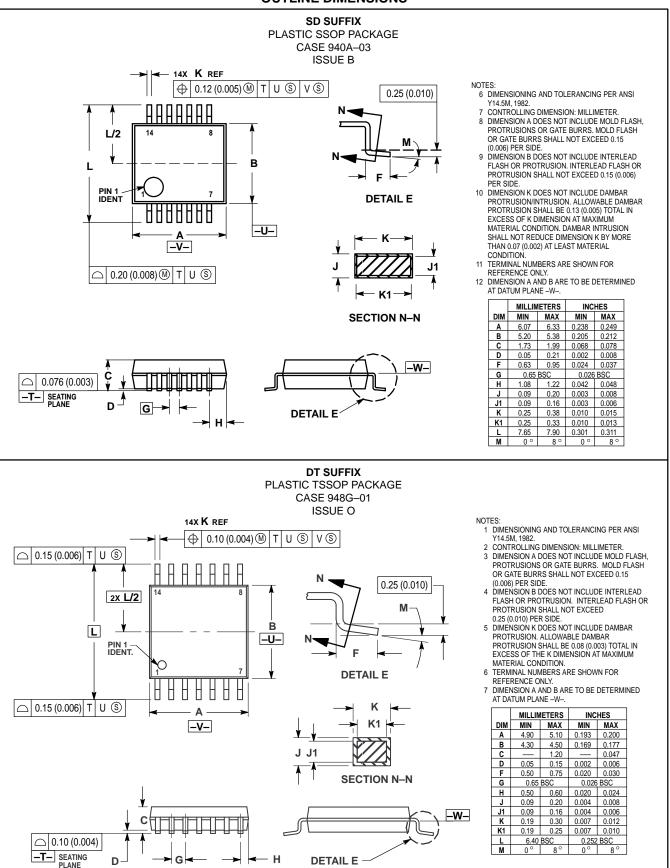


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OUTLINE DIMENSIONS



OUTLINE DIMENSIONS



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