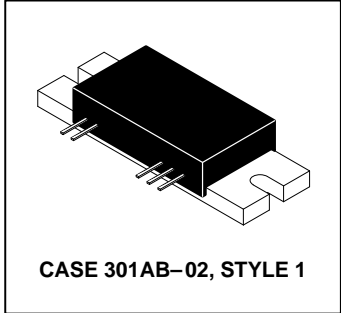
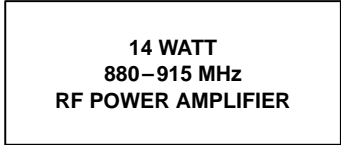


UHF Silicon FET Power Amplifier

Designed specifically for the Pan European digital 8.0 watt, GSM mobile radio. The MHW913 is capable of wide power range control, operates from a 12.5 volt supply and requires less than 100 mW of RF input power.

- Specified 12.5 V Characteristics
 - RF Input Power \leq 100 mW (20 dBm)
 - RF Output Power = 14 W
 - Minimum Gain = 21.5 dB
 - Minimum Efficiency = 35%
- 50 Ω Input/Output Impedance
- Guaranteed Stability and Ruggedness
- Epoxy Glass Substrate Eliminates Possibility of Substrate Fracture
- Circuit board photomaster available upon request by contacting RF Tactical Marketing in Phoenix, AZ.



MAXIMUM RATINGS (Flange Temperature = 25°C)

Rating	Symbol	Value	Unit
DC Supply Voltage	V_{bias} , V_{S2} , V_{S3}	5.0 15.6	Volt
RF Input Power	P_{in}	200	mW
RF Output Power	P_{out}	15	Watt
Storage Temperature	T_C	- 30 to +100	°C
Operating Case Temperature	T_{stg}	- 30 to +100	°C

ELECTRICAL CHARACTERISTICS ($V_{S2} = V_{S3} = 12.5$ Vdc, $V_{bias} = 4.8$ Vdc, $T_C = 25^\circ\text{C}$, 50 Ω system, unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Frequency Range	BW	880	915	MHz
Efficiency ($P_{out} = 14$ W) (1)	η	35	—	%
Power Gain ($P_{out} = 14$ W) (1)	G_p	21.5	—	dB
Harmonic Output ($P_{out} = 14$ W Reference) (1)	$2f_0$ $3f_0$	—	- 30 - 35	dBc
Input VSWR ($P_{out} = 14$ W) (1)	VSWR $_{in}$	—	3:1	
Linearity — % AM in Output $P_{out} = 0.02$ to 14 W; 135 kHz, 1.0% AM on Input (1)	—		6.0	%
Output Power at Decreased Voltage ($P_{in} = 100$ mW, $V_{S2} = V_{S3} = 10.8$ Vdc) (1)	P_{out}	10	—	Watt

(1) Adjust P_{in} for specified P_{out} .

(continued)

ELECTRICAL CHARACTERISTICS (continued) ($V_{S2} = V_{S3} = 12.5\text{ V}$, $V_{bias} = 4.8\text{ V}$, $T_C = 25^\circ\text{C}$, $50\ \Omega$ system, unless otherwise noted)

Load Mismatch Stress ($V_{Supply} = 15.6\text{ Vdc}$, $P_{Out} = 15\text{ W}$; Load VSWR = 10:1, All Phase Angles) (1)	—	No degradation in output power		
Stability ($V_{Supply} = 10.8$ to 16 Vdc ; $P_{Out} = 0.03$ to 14 W ; Load VSWR = 6:1, All Phase Angles) (1)	—	All spurious outputs more than 60 dB below desired signal		
Quiescent Current (With No RF Applied) ($V_{S2} = V_{S3} = 12.5\text{ Vdc}$, $V_{bias} = 4.8\text{ Vdc}$)	I_{sq}	—	500	mA
Leakage Current ($P_{in} = 0\text{ mW}$, $V_{S2} = V_{S3} = 12.5\text{ Vdc}$, $V_b = 0\text{ Vdc}$)	I_L	—	0.6	mA
Bias P_{in} Current ($P_{Out} = 14\text{ W}$) (1)	I_{bias}	—	0.8	mA
Noise Power (In 30 kHz Bandwidth, 20 MHz above f_o) ($P_{Out} = 0.03$ to 14 W , $V_{S2} = V_{S3} = 10.8$ to 15.6 Vdc ; $V_{bias} = 4.8\text{ Vdc}$) (1)	—	—	-70	dBm

(1) Adjust P_{in} for specified P_{Out} .

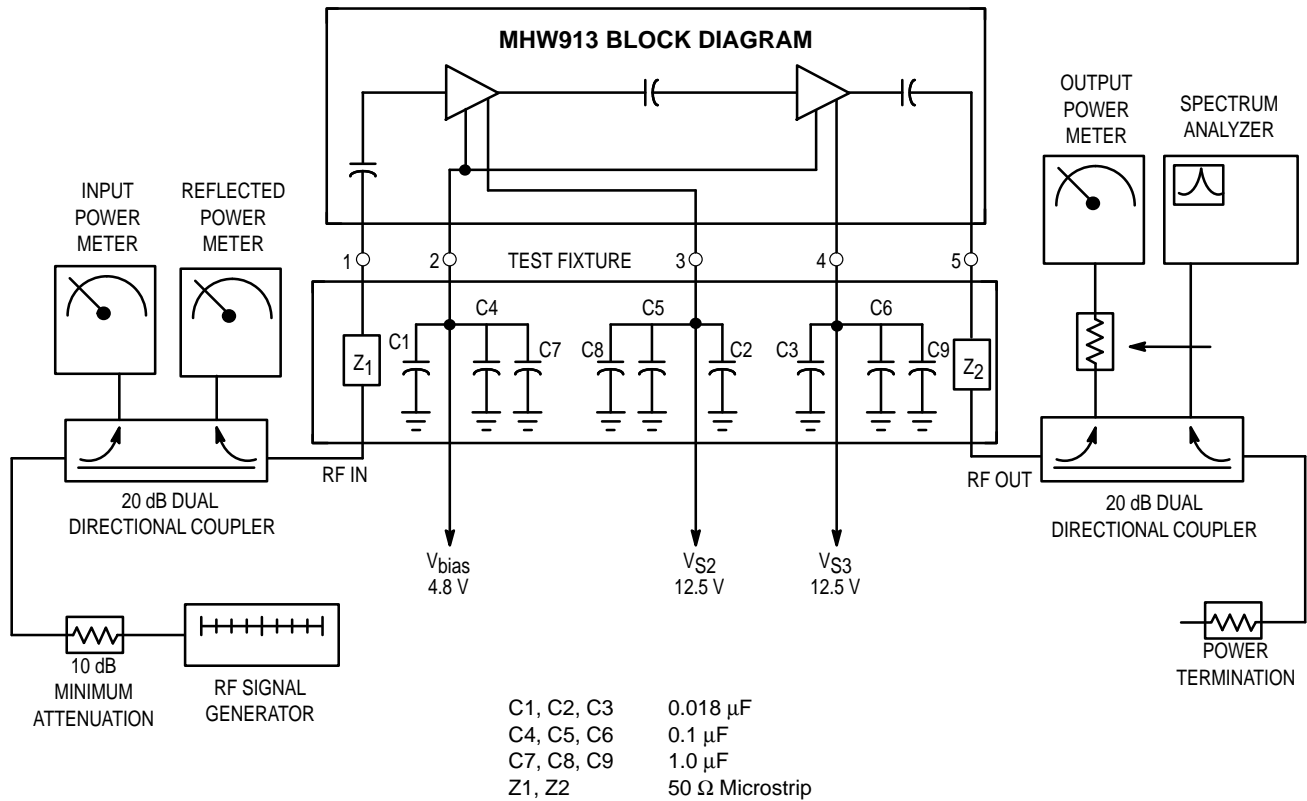


Figure 1. MHW913 Test Circuit Diagram

Typical Characteristics

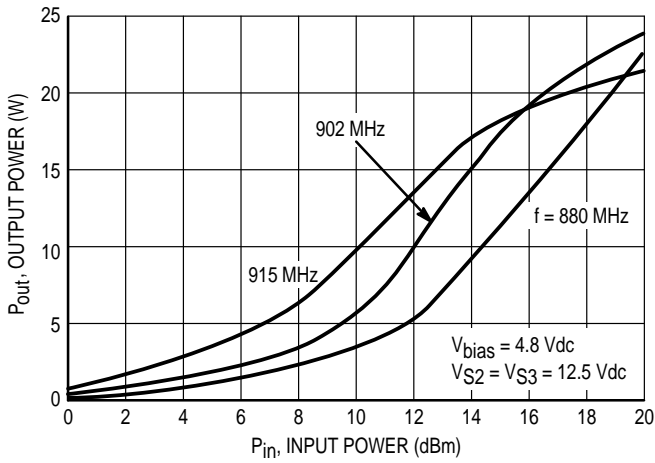


Figure 2. Output Power versus Input Power

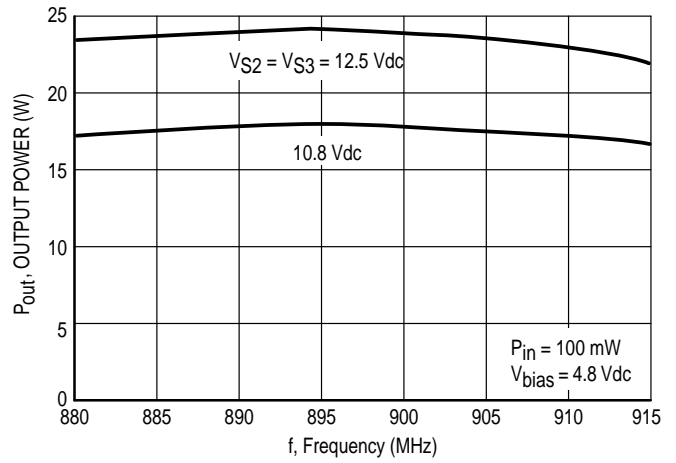


Figure 3. Output Power versus Frequency

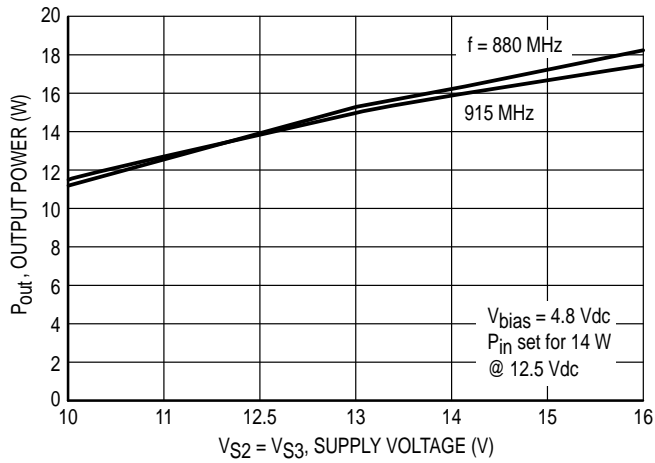


Figure 4. Output Power versus Supply Voltage

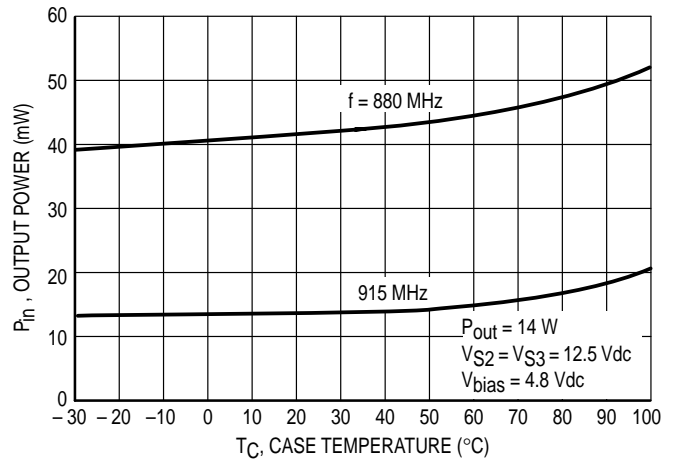


Figure 5. Input Power versus Case Temperature for $P_{out} = 14\text{ W}$

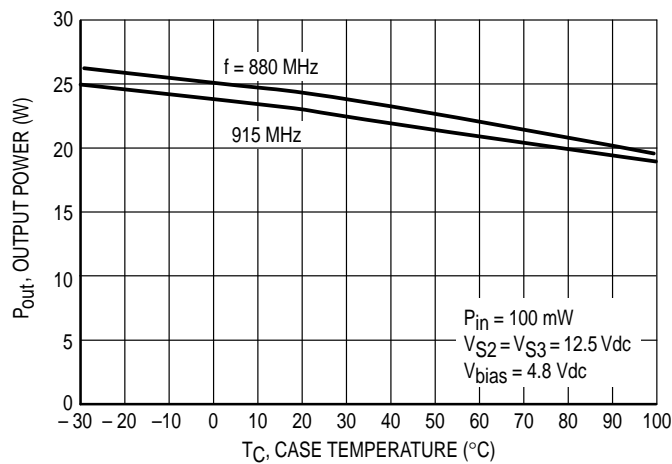
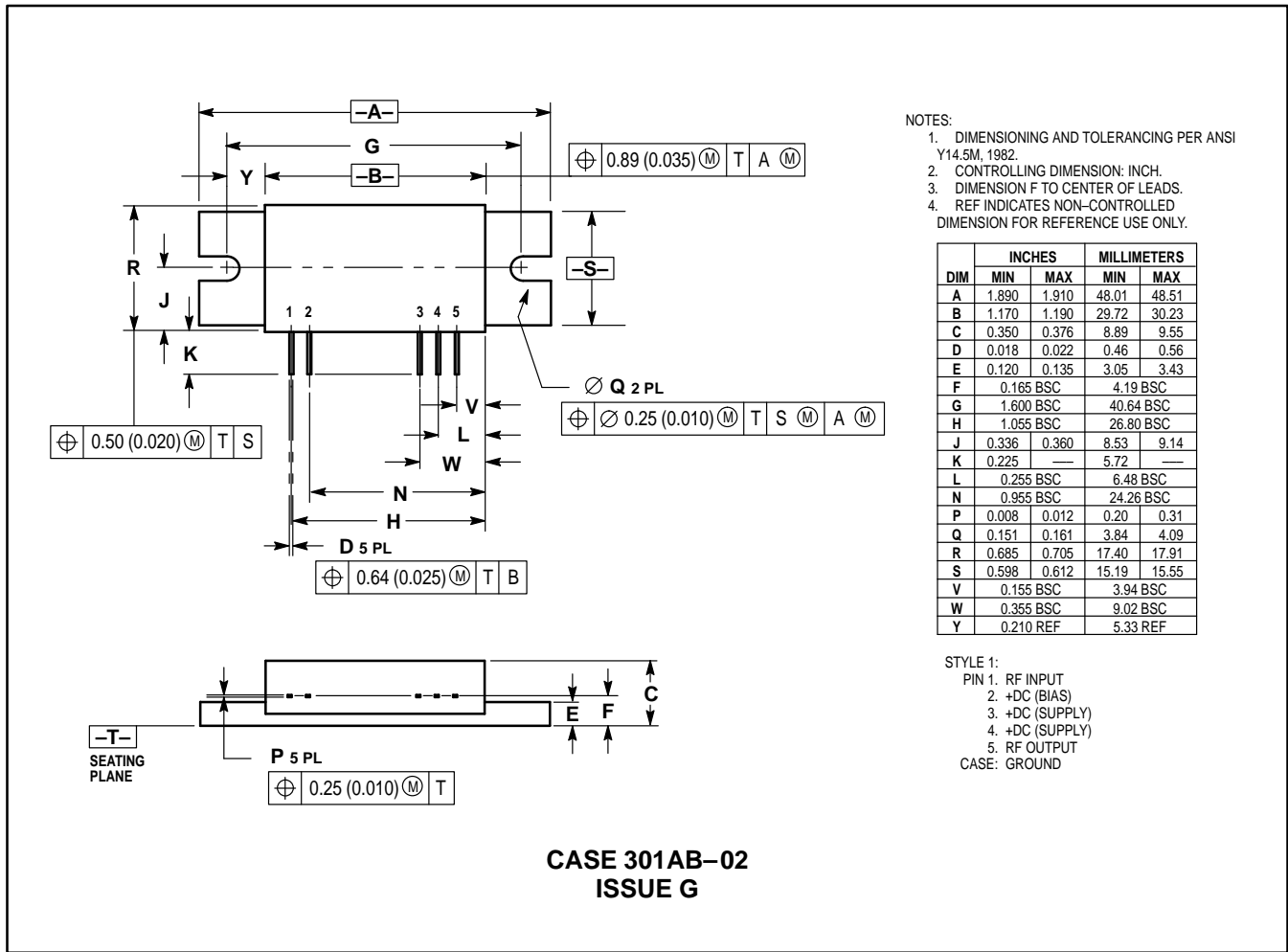


Figure 6. Output Power versus Case Temperature for Maximum Input Power

PACKAGE DIMENSIONS



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