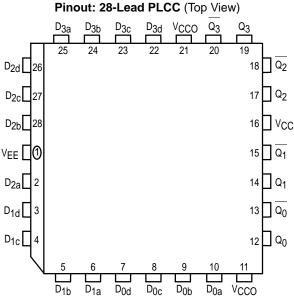
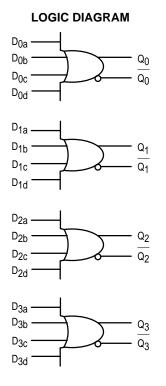
Quad 4-Input OR/NOR Gate

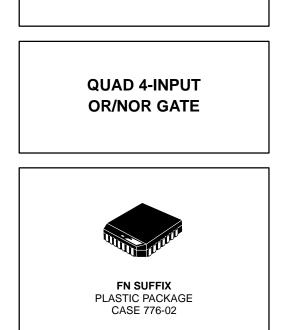
The MC10E/100E101 is a quad 4-input OR/NOR gate.

- 500ps Max. Propagation Delay
- Extended 100E VEE Range of 4.2V to 5.46V
- 75kΩ Input Pulldown Resistors



 * All V_{CC} and V_{CCO} pins are tied together on the die.





MC10E101 MC100E101

PIN NAMES

Pin	Function						
D _{0a} – D _{3d}	Data Inputs						
Q ₀ – Q3	True Outputs						
$\overline{Q_0} - \overline{Q_3}$	Inverting Outputs						



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MC10E101 MC100E101

DC CHARACTERISTICS (VEE = VEE(min) to VEE(max); VCC = VCCO = GND)

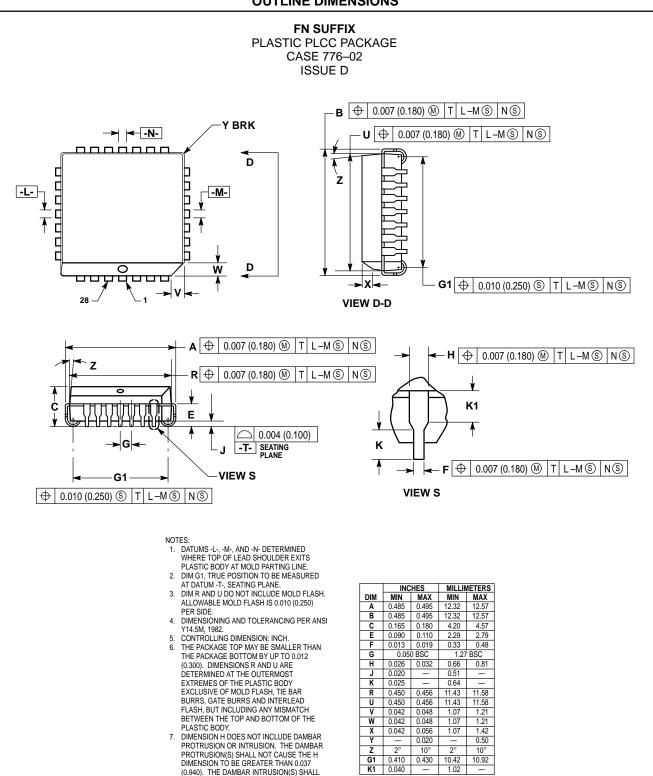
		O°C			25°C			85°C				
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
IIH	Input HIGH Current			150			150			150	μΑ	
IEE	Power Supply Current										mA	
	10E 100E		30 30	36 36		30 30	36 36		30 35	36 42		

AC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

		O°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
^t PLH ^t PHL	Propagation Delay to Output D to Q	200	350	500	200	350	500	200	350	500	ps	
^t SKEW ^t SKEW	Within-Device Skew Within-Gate Skew		50 25			50 25			50 25		ps	1 2
t _r t _f	Rise/Fall Time 20 - 80%	300	380	575	300	380	575	300	380	575	ps	

1. Within-device skew is defined as identical transitions on similar paths through a device.

2. Within-gate skew is defined as the variation in propagation delays of a gate when driven from its different inputs.



NOT CAUSE THE H DIMENSION TO BE SMALLER THAN 0.025 (0.635).

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How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1–800–441–2447 or 602–303–5454

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MFAX: RMFAX0@email.sps.mot.com - TOUCHTONE 602-244-6609 INTERNET: http://Design-NET.com JAPAN: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 03–81–3521–8315

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298



