Dual 2-Bit Adder/Subtractor

The MC10H180 is a high–speed, low–power, general–purpose adder/ subtractor. It is designed to be used in special purpose adders/subtractors or in high–speed multiplier arrays.

Inp<u>uts f</u>or each adder are Carry–in, Operand A, and Operand B; outputs are Sum, Sum and Carry–out. The common select inputs serve as a control line to Invert A for subtract, and a control line to Invert B.

- Propagation Delay, 1.8 ns Typical, Operand and Select to Output
- Power Dissipation, 360 mW Typical
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10K–Compatible

MAXIMUM RATINGS

Characteristic	Symbol	Rating	Unit
Power Supply ($V_{CC} = 0$)	VEE	-8.0 to 0	Vdc
Input Voltage ($V_{CC} = 0$)	VI	0 to V _{EE}	Vdc
Output Current — Continuous — Surge	l _{out}	50 100	mA
Operating Temperature Range	TA	0 to +75	°C
Storage Temperature Range — Plastic — Ceramic	T _{stg}	–55 to +150 –55 to +165	လို လိ

ELECTRICAL CHARACTERISTICS (V_{EE} = -5.2 V ±5%) (See Note)

		0	0	2	5°	7		
Characteristic	Symbol	Min	Max	Min	Max	Min	Max	Unit
Power Supply Current	ΙE	I	95		86		95	mA
Input Current High Pins 4, 12 Pins 7, 9 Pins 5, 6, 10, 11	linH		665 515 410		417 320 255		417 320 255	μΑ
Input Current Low	l _{inL}	0.5	_	0.5		0.3		μΑ
High Output Voltage	Vон	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
Low Output Voltage	VOL	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
High Input Voltage (1) VIH		-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
Low Input Voltage (1)	/Input Voltage (1) VIL		-1.48	-1.95	-1.48	-1.95	-1.45	Vdc

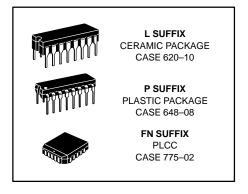
AC PARAMETERS

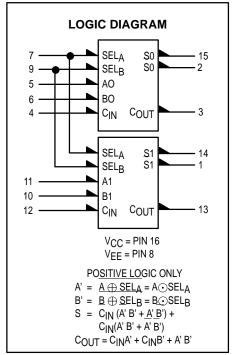
Propagation Delay Operand to Output Select to Output Carry–in to Output	^t pd	0.6 0.6 0.4	2.4 2.2 1.6	0.7 0.7 0.4	2.5 2.3 1.7	0.8 0.8 0.4	2.8 2.6 1.8	ns
Rise Time	tr	0.5	2.0	0.5	2.1	0.5	2.2	ns
Fall Time	t _f	0.5	2.0	0.5	2.1	0.5	2.2	ns

NOTES:

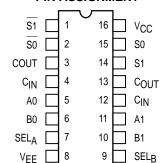
Each MECL 10H series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained. Outputs are terminated through a 50–ohm resistor to –2.0 volts.

MC10H180





DIP PIN ASSIGNMENT



Pin assignment is for Dual-in-Line Package. For PLCC pin assignment, see the Pin Conversion Tables on page 6–11 of the Motorola MECL Data Book (DL122/D).



3/93

FUNCTION SELECT TABLE

TRUTH TABLE

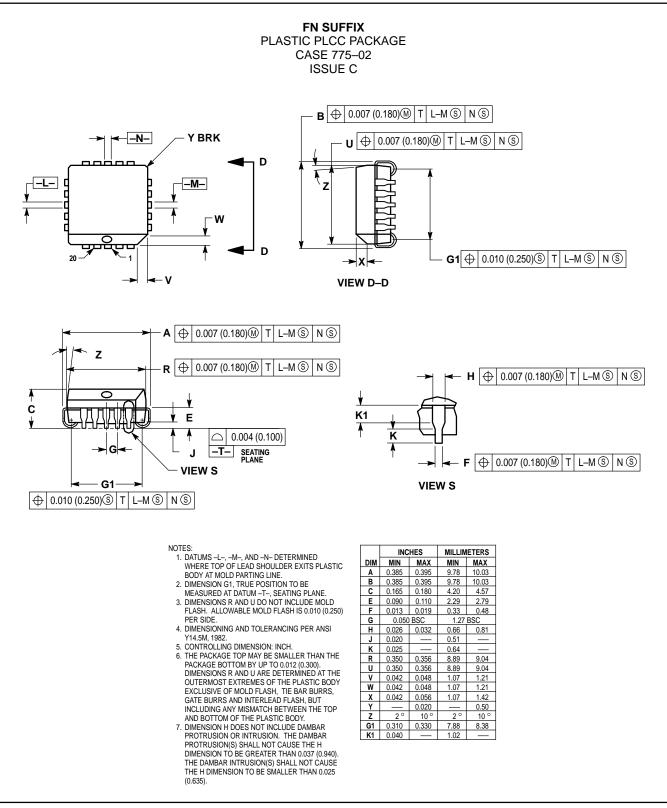
S0 Cout

HHLHLL

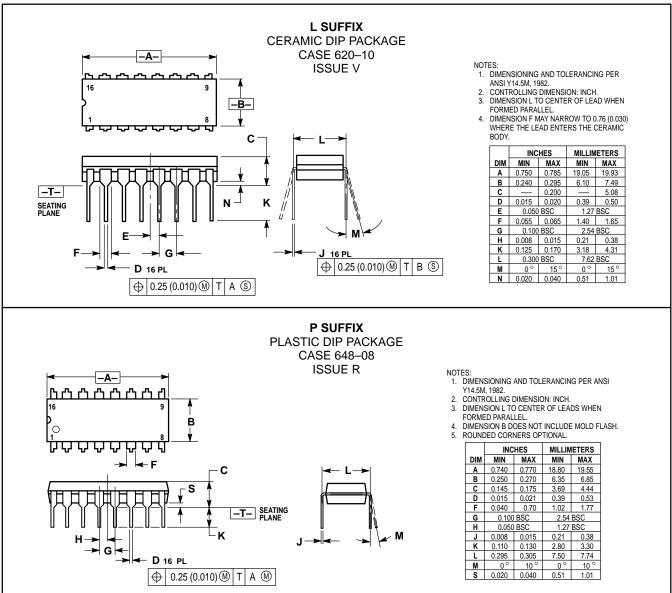
SelA	SelB	Function	[
Н	Н	S = A plus B	
Н	L	S = A minus B	ſ
L	Н	S = B minus A	
L	L	S = 0 minus A minus B	

FUNCTION		INPUT	rs					FUNCTION		INPUT	ſS			
FUNCTION	STION Sel _A Sel _B A0 B0 C _{in} S0 S0 C _{out}	C _{out}	FUNCTION	Sel _A	Sel _B	A0	B0	C _{in}	S0					
ADD	ΙΙΙΙΙΙΙ	ΙΙΙΙΙΙΙ				HLLTLTL		REVERSE SUBTRACT		ΙΙΙΙΙΙ	LLLLTT			TLLTLTL
SUBTRACT	ΙΙΙΙΙΙΙ				H L L H L H H L									

OUTLINE DIMENSIONS



OUTLINE DIMENSIONS



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