

Advance Information

Wideband FM IF Subsystem

The MC13159 is a wideband FM IF subsystem that is designed for high performance data and digital applications. Excellent high frequency performance is achieved, with low cost, through the use of Motorola's RF bipolar process. The MC13159 includes a mixer, local oscillator buffer amplifier, IF amplifier, limiter amplifier and RSSI functions. The mixer is useful for a 240 MHz input used in a single—ended/balanced differential configuration. The IF and limiter amplifier are separated so that an external filter can be used in series, or connected directly with an external capacitor. The RSSI output is derived by summing the output of both the IF and Limiter sections. An enable control is provided to power down the IC for power management in battery powered applications.

Suitable applications include PHS, DECT, PDC, and PCS telephones, wideband wireless data links, and other battery powered radio systems.

- Designed for PHS Applications
- 2.7 to 5.5 V Operating Voltage
- Low Drain Current: 5.5 mA (Typ)
- Wide Input Dynamic Range of Mixer (Maximum -16 dBm Input)
- Enable Function for Power Down Mode
- Over 80 dB of RSSI Dynamic Range (AC Coupling between IF Amplifier and Limiter Amplifier)
- Few External Components Required

WIDEBAND FM IF SUBSYSTEM FOR PHS AND DIGITAL APPLICATIONS

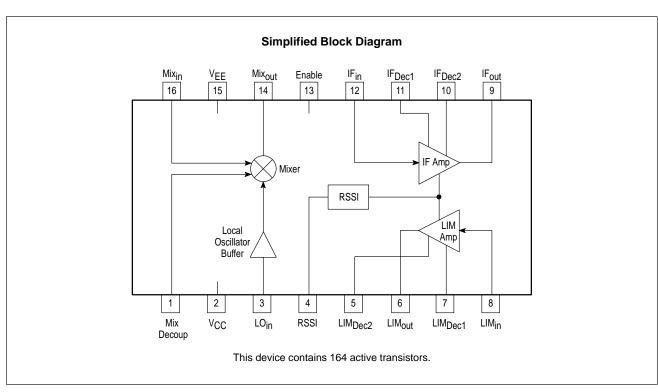
SEMICONDUCTOR TECHNICAL DATA



DTB SUFFIX
PLASTIC PACKAGE
CASE 948F
(TSSOP-16)

ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC13159DTB	$T_A = -30^{\circ} \text{ to } +85^{\circ}\text{C}$	TSSOP-16



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Power Supply Voltage	V _{S(max)}	6.0	Vdc
Junction Temperature	T _{Jmax}	150	°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

NOTE: ESD data available upon request.

RECOMMENDED OPERATING CONDITIONS

Rating	Symbol	Value	Unit
Power Supply Voltage	VS	2.7 to 5.5	Vdc
Input Frequency	f _{in}	10 to 600	MHz
Ambient Temperature Range	TA	-30 to +85	°C
Input Signal Level at Local Input	V _{in}	-10	dBm

DC ELECTRICAL CHARACTERISTICS (T_A = 25° C, V_S = 3.0 V; No Input Signal)

Characteristics	Conditions	Symbol	Min	Тур	Max	Unit
Total Drain Current 1	Active Mode	I _{CC1}	4.5	5.5	7.5	mA
Total Drain Current 2	Disable Mode	I _{CC2}	_	0.1	10	μΑ

AC ELECTRICAL CHARACTERISTICS

Characteristics	Conditions	Symbol	Min	Тур	Max	Unit
MIXER $(T_A = 25^{\circ}C; V_S = 3.0; f_{RF} = 240)$	MHz, f _{LO} = 229.3 MHz)	•	•	•		•
Mixer Conversion Gain	50 Ω Termination Input Matched	_	11 -	14 21	17 -	dB
Noise Figure	Input Matched	NF	-	14	-	dB
Mixer Input Impedance	Single-Ended	Rp Cp	_ _	400 4.0	_ _	Ω pF
Mixer Output Impendance	-	_	-	330	-	Ω
1.0 dB Gain Compression	@Mix _{in}	Vicp	_	-16	-	dBm
3rd Order Input Intercept	50 Ω Termination	IIP3	_	-8.0	-	dBm
IF AMPLIFIER SECTION (T _A = 25°C;	$V_S = 3.0 \text{ V; f}_{IF} = 10.7 \text{ MHz}$					
IF Gain	f = 10.7 MHz	_	32	36	45	dB
Input Impedance	-	_	_	330	_	Ω
Output Impedance	-	_	_	330	-	Ω
LIMITING AMPLIFIER SECTION (TA	$= 25$ °C; $V_S = 3.0 \text{ V}$; $f_{IF} = 10.7 \text{ MHz}$	z)				
Limiter Gain	f = 10.7 MHz	_	_	70	-	dB
Input Impedance	_	_	_	330	_	Ω
Output Swing	_	_	400	500	600	mVpp
Output Rise Time	-	_	_	10	-	ns
Output Fall Time	-	-	_	20	-	ns
RSSI SECTION ($T_A = 25^{\circ}C$; $V_S = 3.0 \text{ V}$;	f _{IF} = 10.7 MHz)				_	
RSSI Slope	-	_	10	14	18	mV/dE
			İ			1

RSSI Slope	_	-	10	14	18	mV/dB
RSSI Output DC Voltage 1	No Input Signal	_	0.8	0.9	1.0	V
RSSI Output DC Voltage 2	V _{IF} = −85 dBm	_	0.82	0.95	1.02	V
RSSI Output DC Voltage 3	$V_{IF} = -80 \text{ dBm}$	_	0.85	1.0	1.15	V
RSSI Output DC Voltage 4	V _{IF} = −40 dBm	_	1.4	1.5	1.6	V
RSSI Output DC Voltage 5	V _{in} = 0 dBm	ı	1.95	2.1	2.25	V

Figure 1. Test Circuit

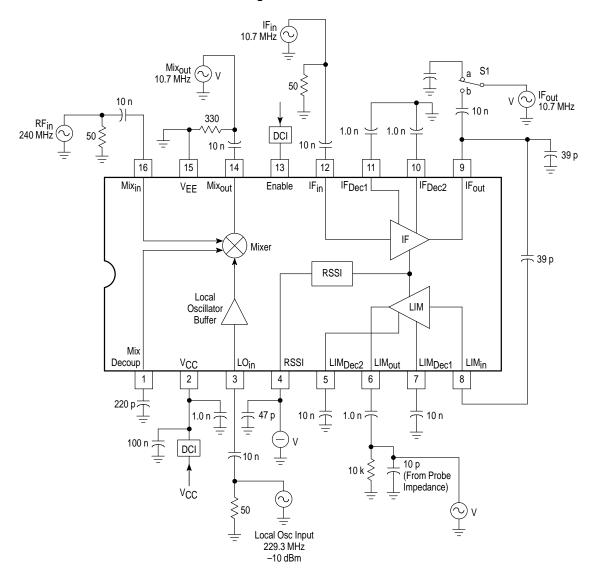
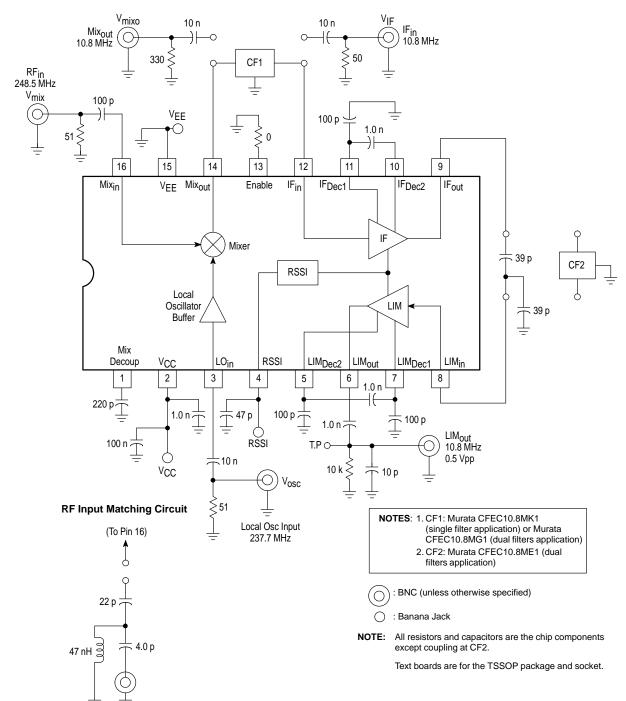


Figure 2. Test Circuit for Evaluation



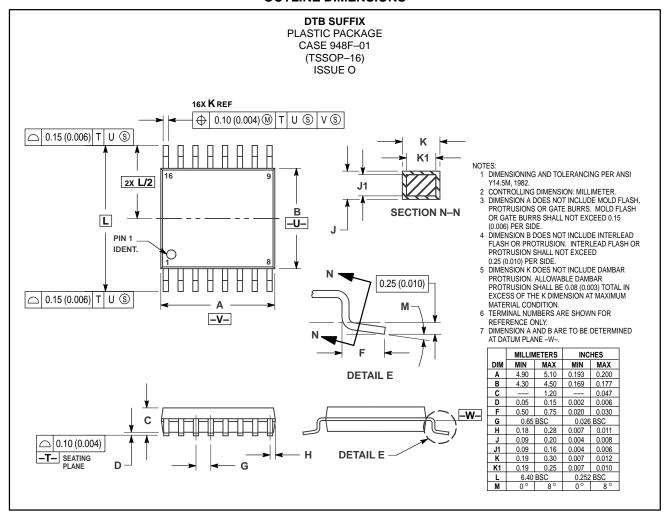
PIN FUNCTION DESCRIPTION

Pin	Symbol	Internal Equivalent Circuit	Description
1	Mix Decoup	VCC	Mixer Decoupling Mixer decoupling pin. 220 pF is decoupled to the RF ground. This pin also can be used for differential input with Mixin.
16	Mix _{in}	160	Mixer Input Input impedance is about 400 Ω at 240 MHz. Single—ended matching section at 240 MHz is referenced at application circuit.
2	Vcc		Supply Voltage Supply voltage range range is from 2.7 Vdc to 5.5 Vdc. 1.0 nF of decoupling capacitor is placed directly at this pin to reduce the floor noise.
3	LO _{in}	1.0 k	Local Oscillator Input Connected to external local oscillator. Input impedance is about 900 Ω at 230 MHz.
4	RSSI	V _{CC} V _{CC} V _{CC} 15 k ≥ = =	RSSI The RSSI current creates a voltage drop across an internal 15 $k\Omega$ resistor.
5 7	LIMDec2 LIMDec1	Vcc Vcc Vcc Vcc S	Limiter Decoupling Limiter decoupling pins. Decoupling capacitors are connected to the RF ground, and one is placed between Dec1 and Dec2.
8	LIM _{in}	70	Limiter Input The input impedance is 330 $Ω$; it matches the 330 input resistance of a 10.7/10.8 MHz ceramic filter.
6	LIM _{out}	V _{CC} 10 800 μA = 6	Limiter Output The output level is about 0.5 Vpp.

PIN FUNCTION DESCRIPTION (continued)

Pin	Symbol	Internal Equivalent Circuit	Description
9	IF _{out}	V _{CC} V _{CC} 220 V _{CC} 9	IF Output The output impedance is 330 Ω ; it matches the 330 input resistance of a 10.7/10.8 MHz ceramic filter.
10 11	IF _{Dec2} IF _{Dec1}	Vcc Vcc Vcc Vcc 120 11	IF Decoupling IF decoupling pins. Decoupling capacitor is connected from Dec1 to the RF ground, and one is placed between Dec1 and Dec2.
12	IF _{in}	330	IF Input The input impedance is 330 $Ω$; it matches the 330 input resistance of a 10.7/10.8 MHz ceramic filter.
13	Enable	13 O VCC VCC 40 k	Enable The IC regulators are enabled by placing this pin at VEE.
14	Mix _{out}	VCC 6.0 k VCC 220 700 μΑ	Mixer Output The mixer output impedance is 330 Ω ; it matches the 330 input resistance of a 10.7/10.8 MHz ceramic filter.
15	VEE		Supply Ground

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