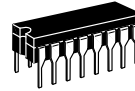


# Quad 2-Input NOR Gate

## ELECTRICAL CHARACTERISTICS

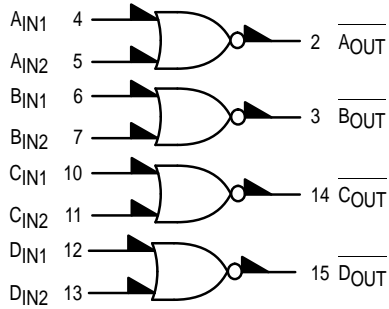
Characteristic	Symbol	-30°C		+25°C		+85°C		Unit
		Min	Max	Min	Max	Min	Max	
Power Supply Drain Current	$I_E$	—	—	—	56	—	—	mAdc
Input Current	$I_{inH}$	—	—	—	350	—	—	$\mu$ Adc
Switching Times Propagation Delay	$t_{-+}$	0.6	1.6	0.6	1.5	0.6	1.7	ns
	$t_{+-}$	0.6	1.8	0.6	1.7	0.6	1.9	
Rise Time, Fall Time (10% to 90%)	$t_{+}, t_{-}$	0.6	2.2	0.6	2.1	0.6	2.3	ns

**MC1662**



**L SUFFIX**  
CERAMIC PACKAGE  
CASE 620-10

### LOGIC DIAGRAM



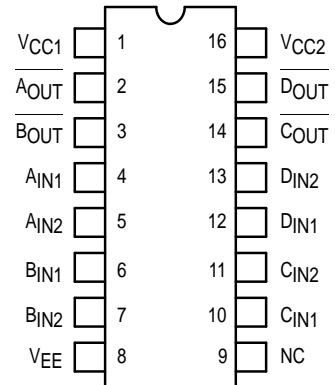
$$OUT = \overline{IN1 + IN2}$$

$V_{CC1} = \text{PIN } 1$   
 $V_{CC2} = \text{PIN } 16$   
 $V_{EE} = \text{PIN } 8$

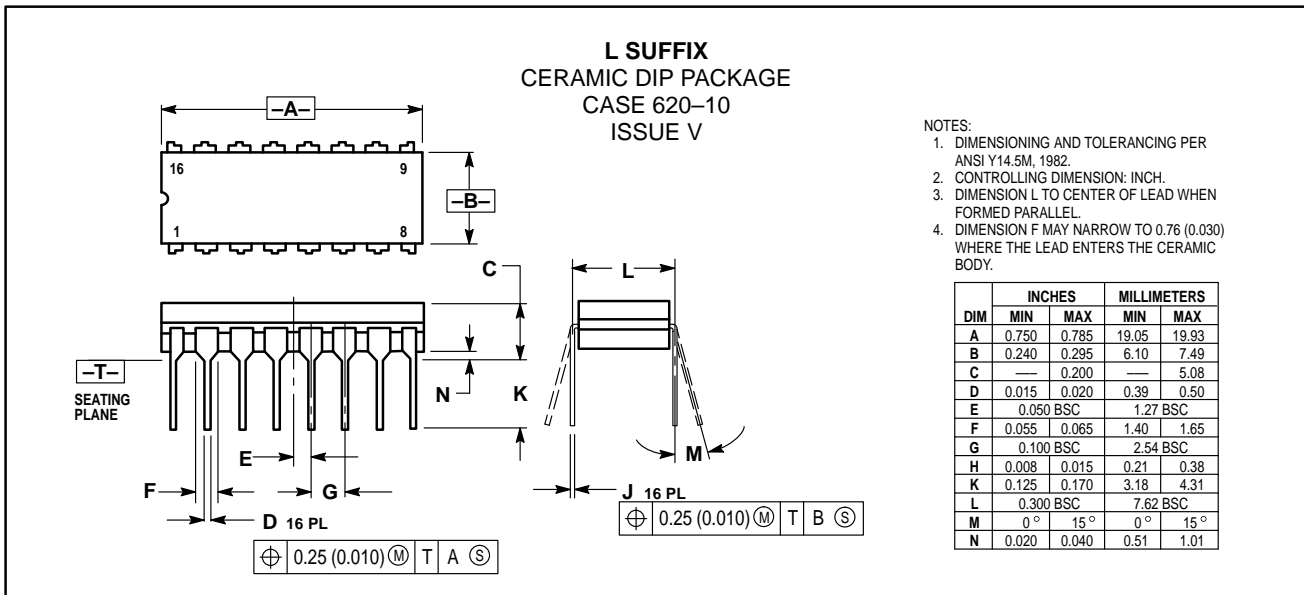
$t_{pd} = 0.9 \text{ ns typ (510 ohm load)}$   
 $= 1.1 \text{ ns typ (50 ohm load)}$

$P_D = 240 \text{ mW typ/pkg (No load)}$   
Full Load Current,  $I_L = -25 \text{ mAdc max}$

### PIN ASSIGNMENT



OUTLINE DIMENSIONS



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**How to reach us:**

**USA/EUROPE/Locations Not Listed:** Motorola Literature Distribution; P.O. Box 5405, Denver, Colorado 80217. 1-800-441-2447

**JAPAN:** Nippon Motorola Ltd.; Tatsumi-SPD-JLDC, 6F Seibu-Butsuryu-Center, 3-14-2 Tatsumi Koto-Ku, Tokyo 135, Japan. 03-81-3521-8315

**Mfax™:** RMFAX0@email.sps.mot.com – TOUCHTONE 602-244-6609  
**INTERNET:** http://Design-NET.com

**ASIA/PACIFIC:** Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852-26629298

