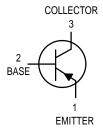
# **One Watt Amplifier Transistors PNP Silicon**



#### **MAXIMUM RATINGS**

Rating	Symbol	BDB02C	BDB02D	Unit				
Collector-Emitter Voltage	VCEO	-80	-100	Vdc				
Collector-Base Voltage	VCES	-80	-100	Vdc				
Emitter-Base Voltage	VEBO	-5.0		-5.0		Vdc		
Collector Current — Continuous	IC	-0.5		-0.5		-0.5		Adc
Total Device Dissipation  @ T <sub>A</sub> = 25°C  Derate above 25°C	PD	1.0 8.0		Watt mW/°C				
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	2.5 20				Watt mW/°C		
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150		-55 to +150		°C		

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	125	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	50	°C/W

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic			Min	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Voltage (IC = -10 mA, I <sub>B</sub> = 0)	BDB02C BDB02D	V(BR)CEO	-80 -100	_ _	Vdc
Collector Cutoff Current ( $V_{CB} = -80 \text{ V}, I_{E} = 0$ ) ( $V_{CB} = -100 \text{ V}, I_{E} = 0$ )	BDB02C BDB02D	ICBO	_ _	-0.1 -0.1	μAdc
Emitter Cutoff Current (I <sub>C</sub> = 0, V <sub>EB</sub> = -5.0 V)		I <sub>EBO</sub>	_	-100	nAdc
ON CHARACTERISTICS	_			_	_
DC Current Gain		hee			

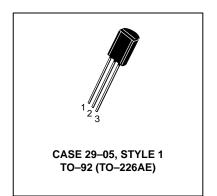
DC Current Gain $(I_C = -100 \text{ mA}, V_{CE} = -1.0 \text{ V})$ $(I_C = -500 \text{ mA}, V_{CE} = -2.0 \text{ V})$	hFE	40 25	400 —	_
Collector-Emitter Saturation Voltage <sup>(1)</sup> ( $I_C = -1000 \text{ mA}$ , $I_B = -100 \text{ mA}$ )	V <sub>CE(sat)</sub>	_	-0.7	Vdc
Collector-Emitter On Voltage <sup>(1)</sup> ( $I_C = -1000 \text{ mA}$ , $V_{CE} = -1.0 \text{ V}$ )	V <sub>BE(on)</sub>	_	-1.2	Vdc

#### **DYNAMIC CHARACTERISTICS**

Current–Gain — Bandwidth Product ( $I_C = -200 \text{ mA}$ , $V_{CE} = -5.0 \text{ V}$ , $f = 20 \text{ MHz}$ )	fT	50	_	MHz
Output Capacitance (V <sub>CB</sub> = -10 V, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	_	30	pF

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle 2.0%.

## BDB02C,D





### BDB02C,D

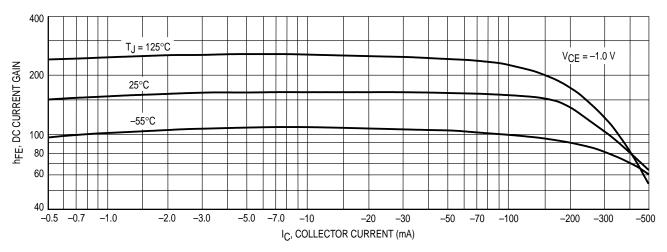


Figure 1. DC Current Gain

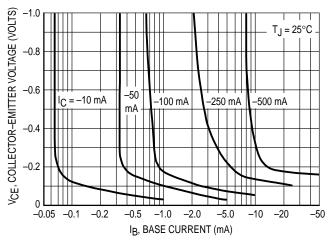


Figure 2. Collector Saturation Region

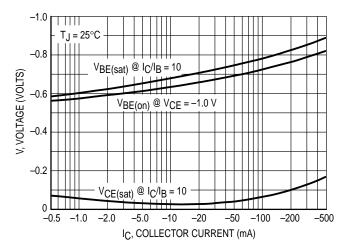


Figure 3. On Voltages

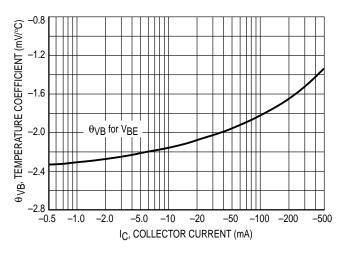


Figure 4. Base-Emitter Temperature Coefficient

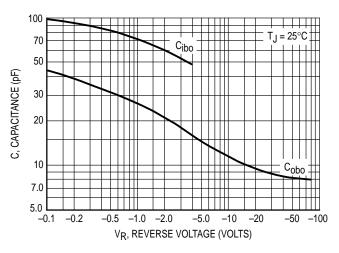
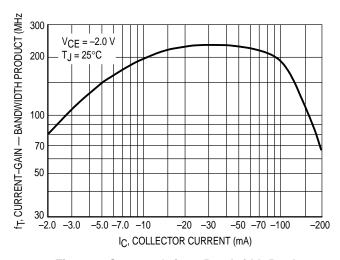
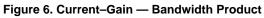


Figure 5. Capacitance





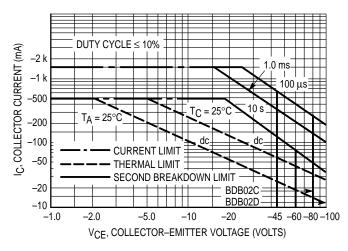
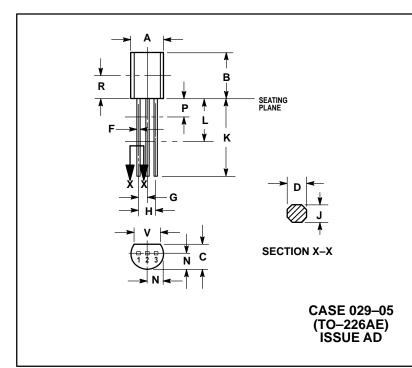


Figure 7. Active Region — Safe Operating Area

#### PACKAGE DIMENSIONS



- 1. DIMENSIONING AND TOLERANCING PER ANSI
- 714.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- 4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSIONS D AND J APPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.44	5.21
В	0.290	0.310	7.37	7.87
С	0.125	0.165	3.18	4.19
D	0.018	0.022	0.46	0.56
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.018	0.024	0.46	0.61
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.135		3.43	
V	0.135		3.43	

STYLE 1: PIN 1. EMITTER 2. BASE 3. COLLECTOR

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How to reach us:

**USA/EUROPE**: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1-800-441-2447

MFAX: RMFAX0@email.sps.mot.com - TOUCHTONE (602) 244-6609 INTERNET: http://Design-NET.com

JAPAN: Nippon Motorola Ltd.; Tatsumi-SPD-JLDC, Toshikatsu Otsuki, 6F Seibu-Butsuryu-Center, 3-14-2 Tatsumi Koto-Ku, Tokyo 135, Japan. 03-3521-8315

HONG KONG: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852-26629298



