

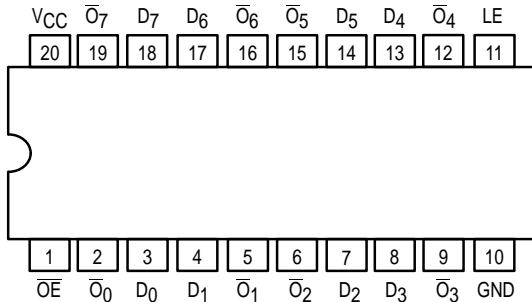


OCTAL TRANSPARENT LATCH WITH 3-STATE OUTPUTS

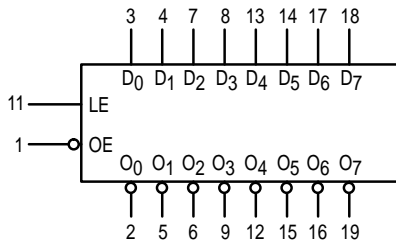
The MC54/74F533 consists of eight latches with 3-state outputs for bus organized system applications. The flip-flops appear transparent to the data when Latch Enable (LE) is HIGH. When LE is LOW, the data that meets the setup times is latched. Data appears on the bus when the Output Enable (\overline{OE}) is LOW. When \overline{OE} is HIGH the bus output is in the high-impedance state. The F533 is the same as the F373, except that the outputs are inverted. For description and logic diagram please see the F373 data sheet.

- Eight Latches in a Single Package
- 3-State Outputs for Bus Interfacing
- ESD Protection > 4000 Volts

CONNECTION DIAGRAM



LOGIC SYMBOL

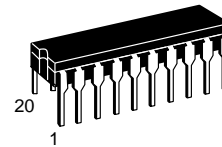


V_{CC} = PIN 20
GND = PIN 10

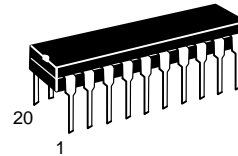
MC54/74F533

OCTAL TRANSPARENT LATCH WITH 3-STATE OUTPUTS

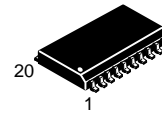
FAST™ SCHOTTKY TTL



J SUFFIX
CERAMIC
CASE 732-03



N SUFFIX
PLASTIC
CASE 738-03



DW SUFFIX
SOIC
CASE 751D-03

ORDERING INFORMATION

MC54FXXXJ Ceramic
MC74FXXXN Plastic
MC74FXXXDW SOIC

GUARANTEED OPERATING RANGES

Symbol	Parameter	Min	Typ	Max	Unit	
V_{CC}	Supply Voltage	54, 74	4.5	5.0	5.5	V
T_A	Operating Ambient Temperature Range	54	-55	25	125	°C
		74	0	25	70	
I_{OH}	Output Current — High	54, 74			-3.0	mA
I_{OL}	Output Current — Low	54, 74			24	mA

MC54/74F533

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

Symbol	Parameter	Limits			Unit	Test Conditions	
		Min	Typ	Max			
V _{IH}	Input HIGH Voltage	2.0			V	Guaranteed Input HIGH Voltage	
V _{IL}	Input LOW Voltage			0.8	V	Guaranteed Input LOW Voltage	
V _{IK}	Input Clamp Diode Voltage			-1.2	V	I _{IN} = -18 mA	V _{CC} = MIN
V _{OH}	Output HIGH Voltage	54, 74	2.4	3.3	V	I _{OH} = -3.0 mA	V _{CC} = 4.5 V
		74	2.7	3.3	V	I _{OH} = -3.0 mA	V _{CC} = 4.75 V
V _{OL}	Output LOW Voltage		0.35	0.5	V	I _{OL} = 24 mA	V _{CC} = MIN
I _{OZH}	Output OFF Current — HIGH			50	μA	V _{OUT} = 2.7 V	V _{CC} = MAX
I _{OZL}	Output OFF Current — LOW			-50	μA	V _{OUT} = 0.5 V	V _{CC} = MAX
I _{IH}	Input HIGH Current			20	μA	V _{IN} = 2.7 V	V _{CC} = MAX
				100		V _{IN} = 7.0 V	
I _{IL}	Input LOW Current			-0.6	mA	V _{IN} = 0.5 V	V _{CC} = MAX
I _{OS}	Output Short Circuit Current (Note 2)	-60		-150	mA	V _{OUT} = 0 V	V _{CC} = MAX
I _{CCZ}	Power Supply Current		41	61	mA	OE = 4.5 V D _n , LE = Gnd	V _{CC} = MAX

NOTES:

- For conditions such as MIN or MAX, use the appropriate value specified under guaranteed operating ranges.
- Not more than one output should be shorted at a time, nor for more than 1 second.

AC CHARACTERISTICS

Symbol	Parameter	54/74F		54F		74F		Unit
		T _A = +25°C V _{CC} = +5.0 V C _L = 50 pF		T _A = -55 to +125°C V _{CC} = 5.0 V ±10% C _L = 50 pF		T _A = 0 to +70°C V _{CC} = 5.0 V ±10% C _L = 50 pF		
		Min	Max	Min	Max	Min	Max	
t _{PLH}	Propagation Delay D _n to O _n	4.0	9.0	4.0	12	4.0	10	ns
t _{PHL}		3.0	7.0	3.0	9.0	3.0	8.0	
t _{PLH}	Propagation Delay LE to O _n	5.0	11	5.0	14	5.0	13	ns
t _{PHL}		3.0	7.0	3.0	9.0	3.0	8.0	
t _{PZH}	Output Enable Time	2.0	10	2.0	12.5	2.0	11	ns
t _{PZL}		2.0	7.5	2.0	9.0	2.0	8.5	
t _{PHZ}	Output Disable Time	1.5	6.5	1.5	8.5	1.5	7.0	ns
t _{PLZ}		1.5	5.5	1.5	7.5	1.5	6.5	

AC OPERATING REQUIREMENTS

Symbol	Parameter	54/74F		54F		74F		Unit
		T _A = +25°C V _{CC} = +5.0 V		T _A = -55 to +125°C V _{CC} = 5.0 V ±10%		T _A = 0 to +70°C V _{CC} = 5.0 V ±10%		
		Min	Max	Min	Max	Min	Max	
t _S (H)	Setup Time, HIGH or LOW D _n to LE	2.0		2.0		2.0		ns
t _S (L)		2.0		2.0		2.0		
t _H (H)	Hold Time, HIGH or LOW D _n to LE	3.0		3.0		3.0		ns
t _H (L)		3.0		3.0		3.0		
t _W (H)	LE Pulse Width HIGH	6.0		6.0		6.0		ns