

1N4728A DO-41 thru 1N4764A DO-41

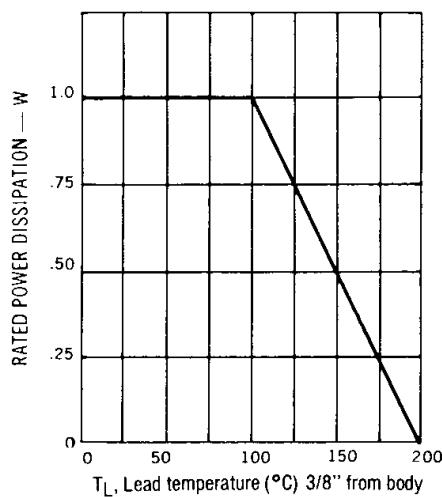


FIGURE 2. POWER DERATING CURVE

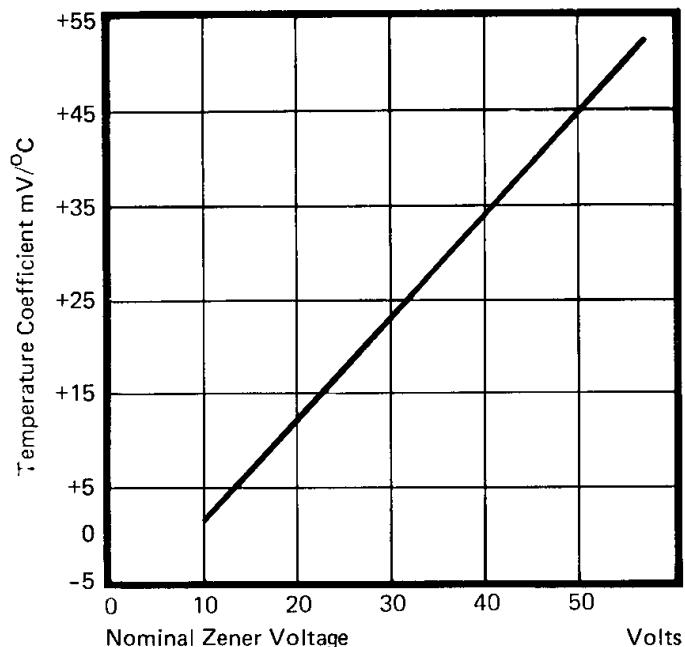


FIGURE 3. TYPICAL TEMPERATURE COEFFICIENT vs ZENER VOLTAGE

NOTE 1. The JEDEC type numbers shown with an A suffix have a 5% tolerance on nominal zener voltage. No suffix signifies a 10% tolerance, C signifies 2%, and D suffix signifies 1% tolerance.

NOTE 2. The Zener impedance is derived from the 60 Hz ac voltage, which results when an ac current having an rms value equal to 10% of the DC Zener current (I_{ZT} or I_{ZK}) is superimposed on I_{ZT} or I_{ZK} . Zener impedance is measured at two points to insure

a sharp knee on the breakdown curve and eliminate unstable units.

NOTE 3. The reverse surge current is measured at 25°C ambient using a 1/2 square wave or equivalent sine wave pulse 1/120 second duration superimposed on I_{ZT} .

NOTE 4. Voltage measurements to be performed 90 seconds after application of DC current.