## **DATA SHEET**



## MOS FIELD EFFECT TRANSISTOR

2SK2482

### SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

#### **DESCRIPTION**

The 2SK2482 is N-Channel MOS Field Effect Transistor designed for high voltage switching applications.

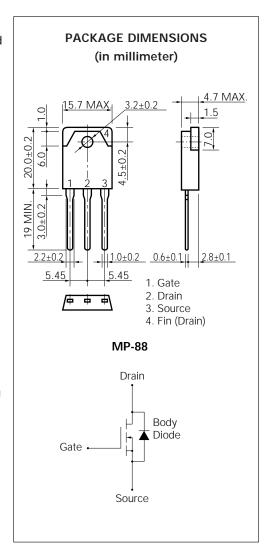
#### **FEATURES**

- Low On-Resistance  $R_{DS (on)} = 4.0 \Omega (V_{GS} = 10 \text{ V}, I_{D} = 3.0 \text{ A})$
- Low Ciss Ciss = 900 pF TYP.
- High Avalanche Capability Ratings

#### ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

Drain to Source Voltage	$V_{\text{DSS}}$	900	V
Gate to Source Voltage	Vgss	±30	V
Drain Current (DC)	ID (DC)	±5.0	Α
Drain Current (pulse)*	ID (pulse)	±12	Α
Total Power Dissipation (Tc = 25 °C)	P <sub>T1</sub>	100	W
Total Power Dissipation (T <sub>A</sub> = 25 °C)	$P_{T2}$	3.0	W
Channel Temperature	$T_ch$	150	.C
Storage Temperature	Tstg -	-55 to +150	.C
Single Avalanche Current**	las	5.0	Α
Single Avalanche Energy**	Eas	73.5	mJ

- \* PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1 %
- \*\* Starting Tch = 25 °C, Rg = 25  $\Omega$ , Vgs = 20 V  $\rightarrow$  0

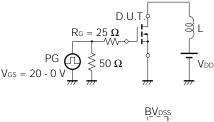


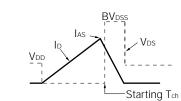


### ELECTRICAL CHARACTERISTICS (TA = 25 °C)

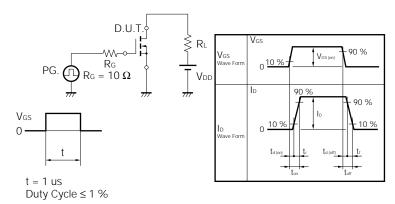
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source On-Resistance	RDS (on)		3.2	4.0	Ω	Vgs = 10 V, ID = 3.0 A
Gate to Source Cutoff Voltage	VGS (off)	2.5		3.5	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Forward Transfer Admittance	<b>y</b> fs	1.0			S	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 3.0 A
Drain Leakage Current	IDSS			100	μΑ	VDS = VDSS, VGS = 0
Gate to Source Leakage Current	Igss			±100	nA	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Input Capacitance	Ciss		900		pF	V <sub>DS</sub> = 10 V
Output Capacitance	Coss		130		pF	V <sub>GS</sub> = 0
Reverse Transfer Capacitance	Crss		25		pF	f = 1 MHz
Turn-On Delay Time	td (on)		17		ns	ID = 3.0 A
Rise Time	tr		8		ns	V <sub>GS</sub> = 10 V
Turn-Off Delay Time	td (off)		60		ns	V <sub>DD</sub> = 150 V
Fall Time	tf		10		ns	$R_G = 10 \Omega$
Total Gate Charge	QG		30		nC	ID = 5.0 A
Gate to Source Charge	Qgs		5		nC	V <sub>DD</sub> = 450 V
Gate to Drain Charge	QgD		13		nC	V <sub>GS</sub> = 10 V
Body Diode Forward Voltage	VF (S-D)		1.0		V	IF = 5.0 A, VGS = 0
Reverse Recovery Time	trr		780		ns	I <sub>F</sub> = 5.0 A, V <sub>GS</sub> = 0
Reverse Recovery Charge	Qrr		4.2		μC	$di/dt = 50 A/\mu s$

### Test Circuit 1 Avalanche Capability

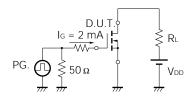




### Test Circuit 2 Switching Time



### Test Circuit 3 Gate Charge

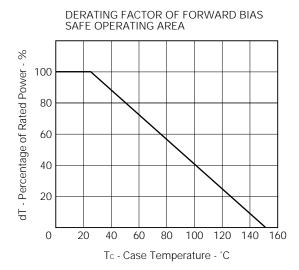


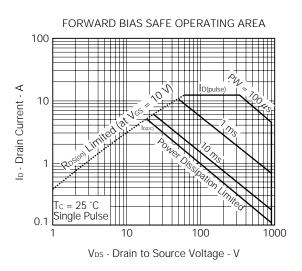
The application circuits and their parameters are for references only and are not intended for use in actual design-in's.

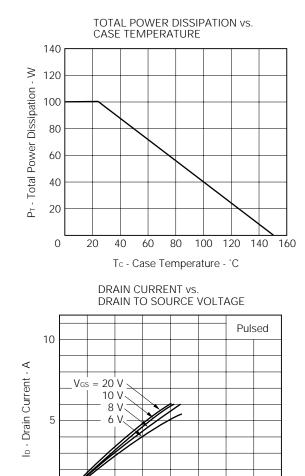
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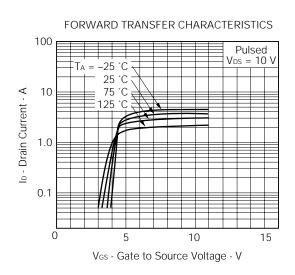
#### TYPICAL CHARACTERISTICS (TA = 25 °C)





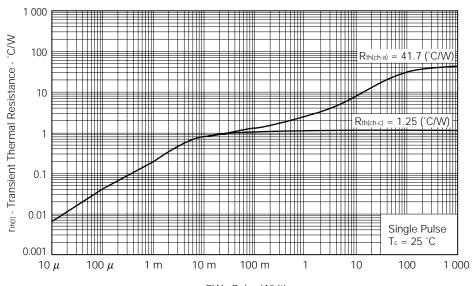


V<sub>DS</sub> - Drain to Source Voltage - V



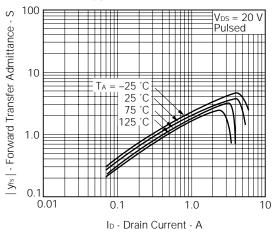
## **NEC**

#### TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

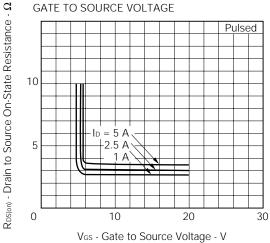


PW - Pulse Width - s

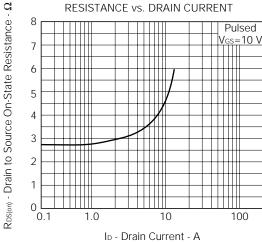




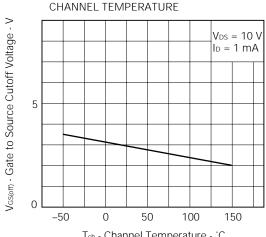
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



#### DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

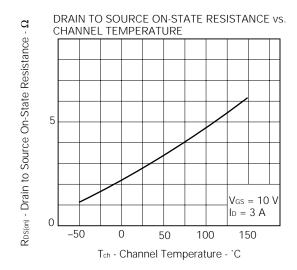


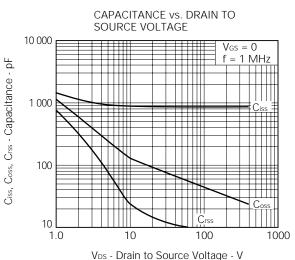
# GATE TO SOURCE CUTOFF VOLTAGE vs.

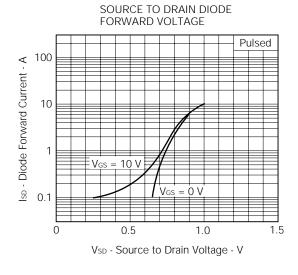


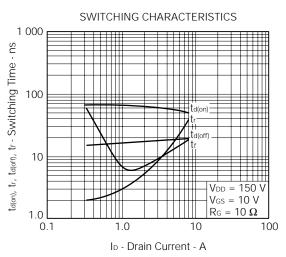
Tch - Channel Temperature - °C

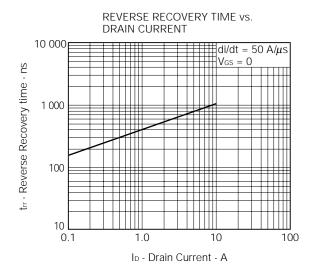


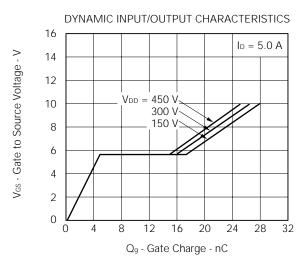




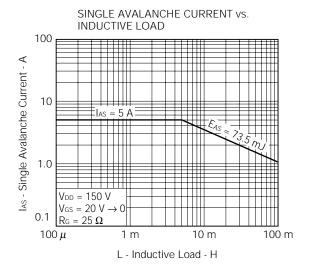


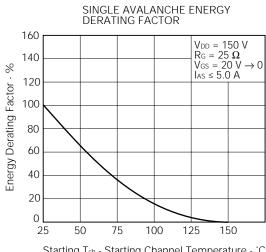












Starting  $T_{\text{ch}}$  - Starting Channel Temperature -  $^{\circ}\text{C}$ 



### REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134
Power MOS FET features and application switching power supply.	TEA-1034
Application circuits using Power MOS FET.	TEA-1035
Safe operating area of Power MOS FET.	TEA-1037

7

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