

SILICON POWER MOS FIELD EFFECT TRANSISTOR

2SK2597

N-CHANNEL SILICON POWER MOSFET FOR BASE STATION OF 900 MHz BAND CELLULAR PHONE POWER AMPLIFICATION

FEATURES

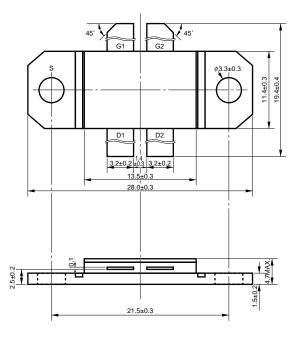
- High output, high gain
 Po = 100 W, G_L = 13 dB (TYP.) (f = 900 MHz)
 Po = 90 W, G_L = 12 dB (TYP.) (f = 960 MHz)
- · Low intermodulation distortion
- Covers all base station frequencies such as 800-MHz PDC and GSM
- · High-reliability gold electrodes
- · Hermetic sealed package
- · Internal matching circuit
- · Push-pull structure

ABSOLUTE MAXIMUM RATINGS (TA = 25 $^{\circ}$ C)

Parameter	Symbol	Ratings	Unit	
Drain-source voltage	VDS	60	V	
Gate-source voltage	Vgs	7	V	
Drain current (D.C.)	ΙD	15 ^{Note}	Α	
Total power dissipation	Рт	290	W	
Thermal resistance	Rth	0.6	°C/W	
Channel temperature	Tch	200	°C	
Storage temperature	Tstg	-65 to +150	°C	

Note Per side

PACKAGE DRAWING (Unit: mm)



G₁, G₂: gate D₁, D₂: drain S : source

Flange is connected to the source.

ELECTRICAL CHARACTERISTICS (TA = 25 °C)

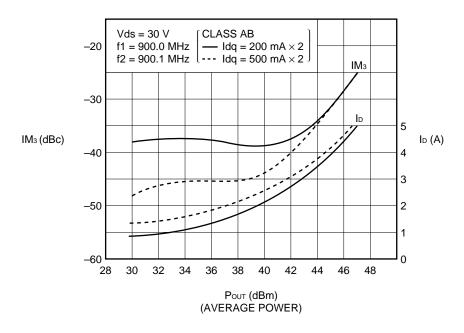
Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Gate leakage current	Igss	V _{GS} = 7 V			1	μΑ
Cut-off voltage	V _{GS(off)}	V _{DS} = 5 V, I _D = 50 mA	1.5		4	V
Drain current	IDSS	V _{DS} = 60 V			2	mA
Mutual conductance	g m	$V_{DS} = 5 \text{ V}, \text{ ID} = 3 \text{ A}, \Delta I_{D} = 100 \text{ mA}$	2.0			S
Output power	Po	f = 960 MHz, V _{DD} = 30 V	80	90		W
Drain efficiency	η ο	$I_{DQ} = 200 \text{ mA} \times 2, P_{in} = 40 \text{ dBm}$	35	40		%
Linear gain	GL	f = 960 MHz, V _{DD} = 30 V I _{DQ} = 200 mA × 2, P _{In} = 30 dBm	11	12		dB
Third intermodulation distortion	IMз	$f = 900 \text{ MHz}, \Delta f = 0.1 \text{ MHz}, V_{DD} = 30 \text{ V}$ $I_{DQ} = 200 \text{ mA} \times 2, P_{O} = 42 \text{ dBm}$		-38		dBc

The information in this document is subject to change without notice.



OUTPUT v.s. IM3, ID CHARACTERISTICS

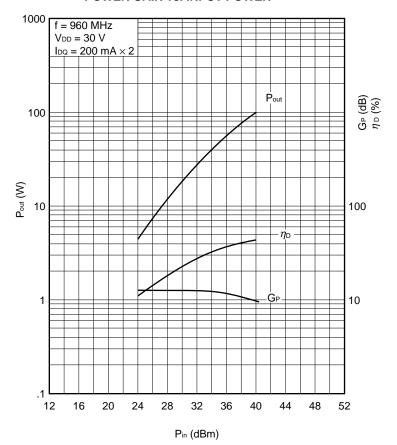
THIRD ORDER INTERMODULATION DISTORTION / DRAIN CURRENT v.s. OUTPUT POWER



INPUT v.s. OUTPUT, POWER GAIN, EFFICIENCY

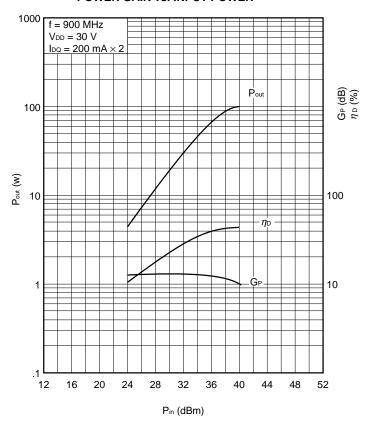
(1) f = 960 MHz

OUTPUT POWER / DRAIN EFFICIENCY / POWER GAIN vs. INPUT POWER



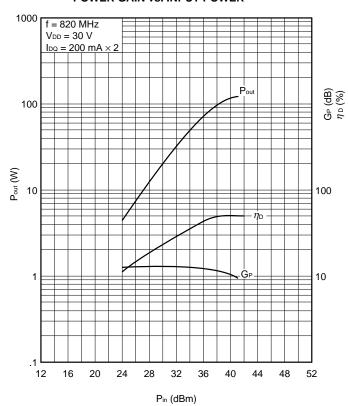
(2) f = 900 MHz

OUTPUT POWER / DRAIN EFFICIENCY / POWER GAIN vs. INPUT POWER



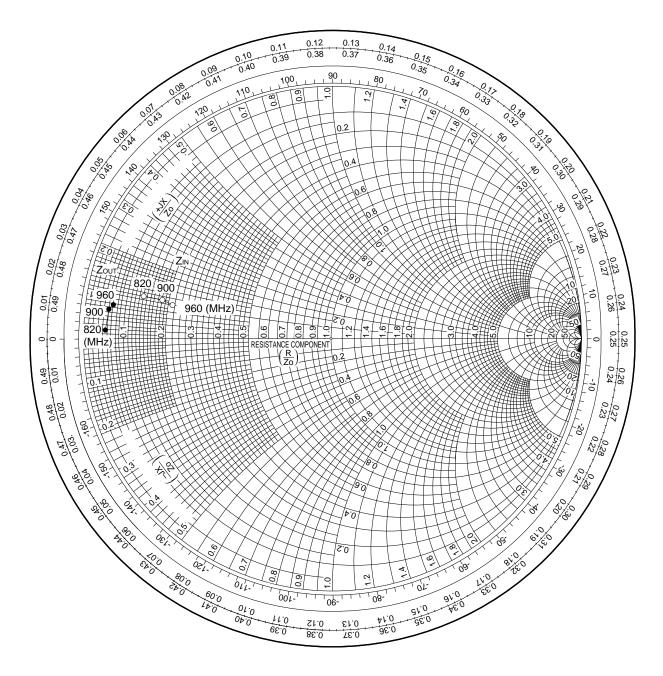
(3) f = 820 MHz

OUTPUT POWER / DRAIN EFFICIENCY / POWER GAIN vs. INPUT POWER





ZIN, ZOUT

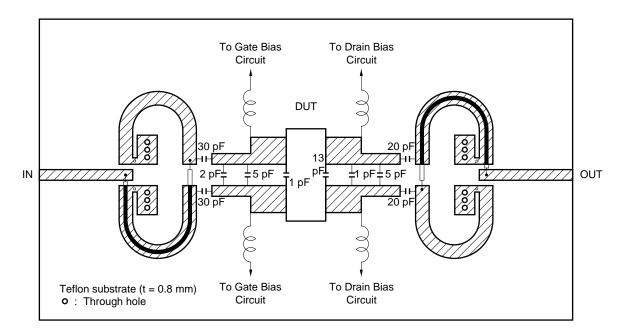


 V_{DD} = 30 V, I_{DQ} = 200 mA \times 2, P_{in} = 40 dBm

f (MHz)	Zin (Ω)	Ζουτ (Ω)
820	6.52 + j5.52	2.34 + j0.91
900	8.86 + j5.49	2.78 + j3.23
960	10.36 + j4.79	2.95 + j3.37

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APPLICATION CIRCUIT EXAMPLE (f = 960 MHz)



Notes on Handling

This product internally uses beryllie porcelain (beryllium oxide). If powder or vapor of beryllium oxide enters your respiratory organs, you will have a difficulty in breathing, which is dangerous. Therefore, do no disassemble or chemically process the product.

Be sure to abolish the product separately from general industrial wastes or garbage.

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