

MOS FIELD EFFECT TRANSISTOR 2SK2858

N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR HIGH SPEED SWITCHING

DESCRIPTION

The 2SK2858 is a switching device which can be driven directly by a 2.5-V power source.

The 2SK2858 has excellent switching characteristics, and is suitable for use as a high-speed switching device in digital circuits.

FEATURES

- Can be driven by a 2.5-V power source
- · Low gate cut-off voltage

ORDERING INFORMATION

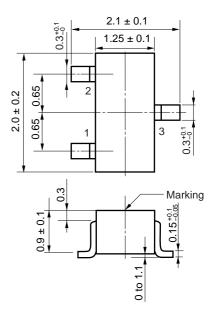
| PART NUMBER | PACKAGE |
|-------------|------------|
| 2SK2858 | SC-70(SSP) |

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

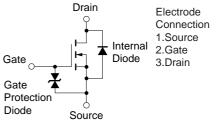
| | Drain to Source Voltage | VDSS | 30 | V |
|---|----------------------------|-----------|-------------|----|
| | Gate to Source Voltage | Vgss | ±20 | V |
| | Drain Current (DC) | ID(DC) | ±0.1 | Α |
| | Drain Current (pulse) Note | ID(pulse) | ±0.4 | Α |
| * | Total Power Dissipation | Рт | 150 | mW |
| | Channel Temperature | T_ch | 150 | °C |
| | Storage Temperature | Tstg | -55 to +150 | °C |
| | | | | |

Note PW \leq 10 μ s, Duty Cycle \leq 1 %

PACKAGE DRAWING (Unit: mm)



EQUIVALENT CIRCUIT



Marking: G24

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.



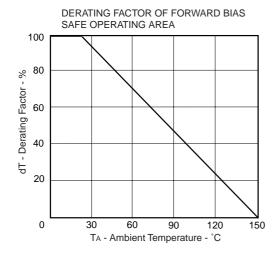
ELECTRICAL CHARACTERISTICS (TA = 25 °C)

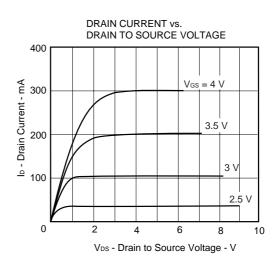
| CHARACTERISTICS | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|-------------------------------------|----------------------|---|------|------|------|------|
| Drain Cut-off Current | Ipss | V _{DS} = 30 V, V _{GS} = 0 V | | | 1 | μΑ |
| Gate Leakage Current | lgss | Vgs = ±20 V, Vps = 0 V | | | ±10 | μΑ |
| Gate Cut-off Voltage | V _{GS(off)} | $V_{DS} = 3 \text{ V}, I_{D} = 10 \mu \text{A}$ | 1.0 | 1.4 | 1.8 | V |
| Forward Transfer Admittance | yfs | V _{DS} = 3 V, I _D = 10 m A | 20 | | | mS |
| Drain to Source On-state Resistance | RDS(on)1 | Vgs = 2.5 V, ID = 1 m A | | 8 | 15 | Ω |
| | RDS(on)2 | Vgs = 4 V, ID = 10 mA | | 4 | 8 | Ω |
| | RDS(on)3 | Vgs = 10 V, ID = 10 mA | | 3 | 5 | Ω |
| Input Capacitance | Ciss | Vps = 3 V | | 9 | | pF |
| Output Capacitance | Coss | V _G S = 0 V | | 12 | | pF |
| Reverse Transfer Capacitance | Crss | f = 1 MHz | | 2.1 | | pF |
| Turn-on Delay Time | td(on) | VDD = 3 V | | 40 | | ns |
| Rise Time | tr | I _D = 10 mA | | 55 | | ns |
| Turn-off Delay Time | td(off) | V _{GS(on)} = 4 V | | 68 | | ns |
| Fall Time | tr | $R_G = 10 \Omega$, $R_L = 300 \Omega$ | | 64 | | ns |

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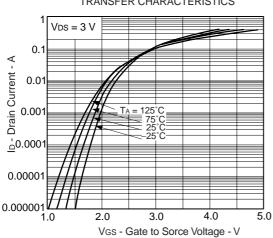


TYPICAL CHARACTERISTICS (TA = 25 °C)

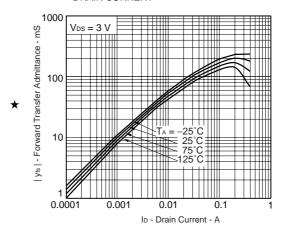


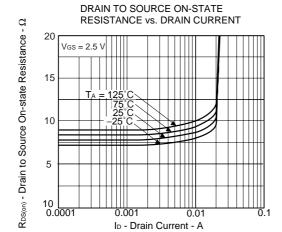


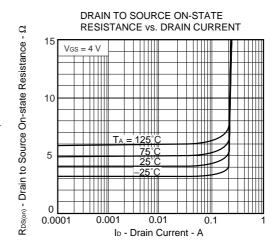




FORWARD TRANSFER ADMMITTANCE Vs. DRAIN CURRENT

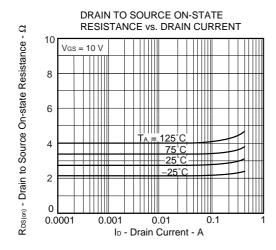


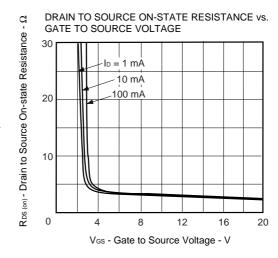


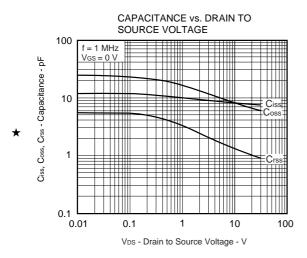


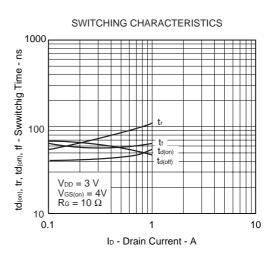
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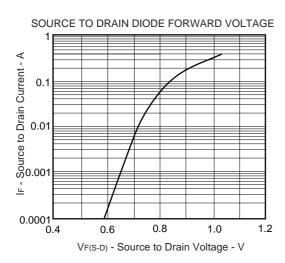












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